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1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

M60 - PVT

09/27/06

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
H		464264	PRODUCTION RELEASED	09/27/06	06/22/04

PAGE	PDF	CIRCUIT
1 JD	1	TABLE OF CONTENTS
2 JD	2	SYSTEM BLOCK DIAGRAM
3 MY	3	POWER BLOCK DIAGRAM
4 JD	4	TABLE ITEMS & REVISION HISTORY
5 JD	5	FUNC TEST
6 MY	6	POWER CONNECTOR / POWER ALIAS
7 JD	7	CPU - BUS INTERFACE
8 JD	8	CPU - PWR & GND
9 JD	9	CPU - DECAPS
10 JD	10	CPU - THERMAL SENSOR
11 JD	11	CPU - ITP CONN
12 JH	12	NB - CPU INTERFACE
13 JH	13	NB - VIDEO INTERFACE
14 JH	14	NB - MISC INTERFACES
15 JH	15	NB - DDR2 INTERFACE
16 JH	16	NB - POWER 1
17 JH	17	NB - POWER 2
18 JH	18	NB - GROUNDS
19 JH	19	NB - DECAPS
20 JH	20	NB - CONFIG STRAPS
21 JD	21	SB - RTC, LAN, AUDIO, ATA, CPU, LPC
22 JD	22	SB - PCIE, SPI, USB, DMI, PCI
23 JD	23	SB - SMB, GPIO, PM, CLKS
24 JD	24	SB - POWERS AND GROUNDS
25 JD	25	SB - DECAPS
26 JD	26	SB - MISC
27 JD	27	SB - SMB BUS CONNECTIONS
28 JD	28	DDR2 - SO-DIMM CONN A
29 JD	29	DDR2 - SO-DIMM CONN B (REVERSED)
30 JD	30	DDR2 - TERMINATION
31 MY	31	DDR2 - VTT SUPPLY
33 JD	32	CLOCKS - GENERATOR
34 JD	33	CLOCKS - TERMINATIONS
38 JD	34	ATA (SATA AND IDE) CONN'S
41 JD	35	LAN - YUKON'S PCIE INTERFACE
42 JD	36	LAN - YUKON'S PWR, MISC
43 JD	37	LAN - CONN
44 JD	38	FIREWIRE - FW323-06
45 JD	39	FIREWIRE - DECAPS
46 JD	40	FIREWIRE - CONN'S
47 JD	41	USB - CONN'S
53 JD	42	PCI-E - AIRPORT MINI-PCIE CONN
54 JD	43	PCI-E - UNUSED PORTS

PAGE	PDF	CIRCUIT
57 MY	44	VR - "S5" 3.3V AND 2.5V
58 MY	45	SMC - H8S2116
59 MY	46	SMC - SMB BUSSES, MISC
60 MY	47	SMC - LPC+ CONN
61 MY	48	SMC - GPU/NB THERMAL SENSOR
63 MY	49	SMC - SPI BOOTROM
65 MY	50	SMC - FANS
66 MY	51	SMC - FANS
67 JD	52	SMC - TPM
TG 68	53	AUDIO - CODEC, VREG, MIC BIAS
TG 72	54	AUDIO - INTERNAL SPEAKER AMP
TG 73	55	AUDIO - I/O CONN'S, EMC
TG 74	56	AUDIO - DETECT TRANSLATORS
75 MY	57	VR - CPU CORE
76 MY	58	VR - CPU I-V SENSE CKT
77 MY	59	VR - "S3" 1.2V & "S0" 1.2V
78 MY	60	VR - "S0" 1.8V
79 MY	61	VR - "S3" 1.8V
80 MY	62	VR - "S0" 1.5V
81 MY	63	VR - "S0" 1.05V
82 MY	64	VR - "S5" 5V AND "S0" 4.5V
83 MY	65	VR - FETS FOR REMAINING RAILS
84 JH	66	GPU - M56 PCI-E
85 JH	67	GPU - VCORE SUPPLY
86 JH	68	GPU - M56 CORE PWR
87 JH	69	GPU - M56 FRAME BUFFER
88 JH	70	GPU - MISC
89 JH	71	GPU - GDDR SDRAM A
90 JH	72	GPU - GDDR SDRAM B
91 JH	73	GPU - M56 GPIO, DVO, MISC
92 JH	74	GPU - M56 CLOCKS
93 JH	75	GPU - M56 VIDEO INTERFACES
94 JH	76	GPU - INTERNAL DISPLAY CONN'S
95 JH	77	GPU - TP'S
96 JH	78	GPU - TMDS, INVERTER, EXT VGA
97 JH	79	GPU - EXTERNAL DISPLAY CONN'S

DIMENSIONS ARE IN MILLIMETERS


XX ± _____

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X.XXX ± _____

ANGLES ± _____

DO NOT SCALE DRAWING



THIRD ANGLE PROJECTION

METRIC

DRAFTER	/	DESIGN CK	/
ENG APPD	/	MFG APPD	/
QA APPD	/	DESIGNER	/
RELEASE	/	SCALE	NONE
MATERIAL/FINISH NOTED AS APPLICABLE		SIZE	D

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TITLE

SCH, MLB, M60

DRAWING NUMBER

051-7124

REV.

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SHT 1 OF 111

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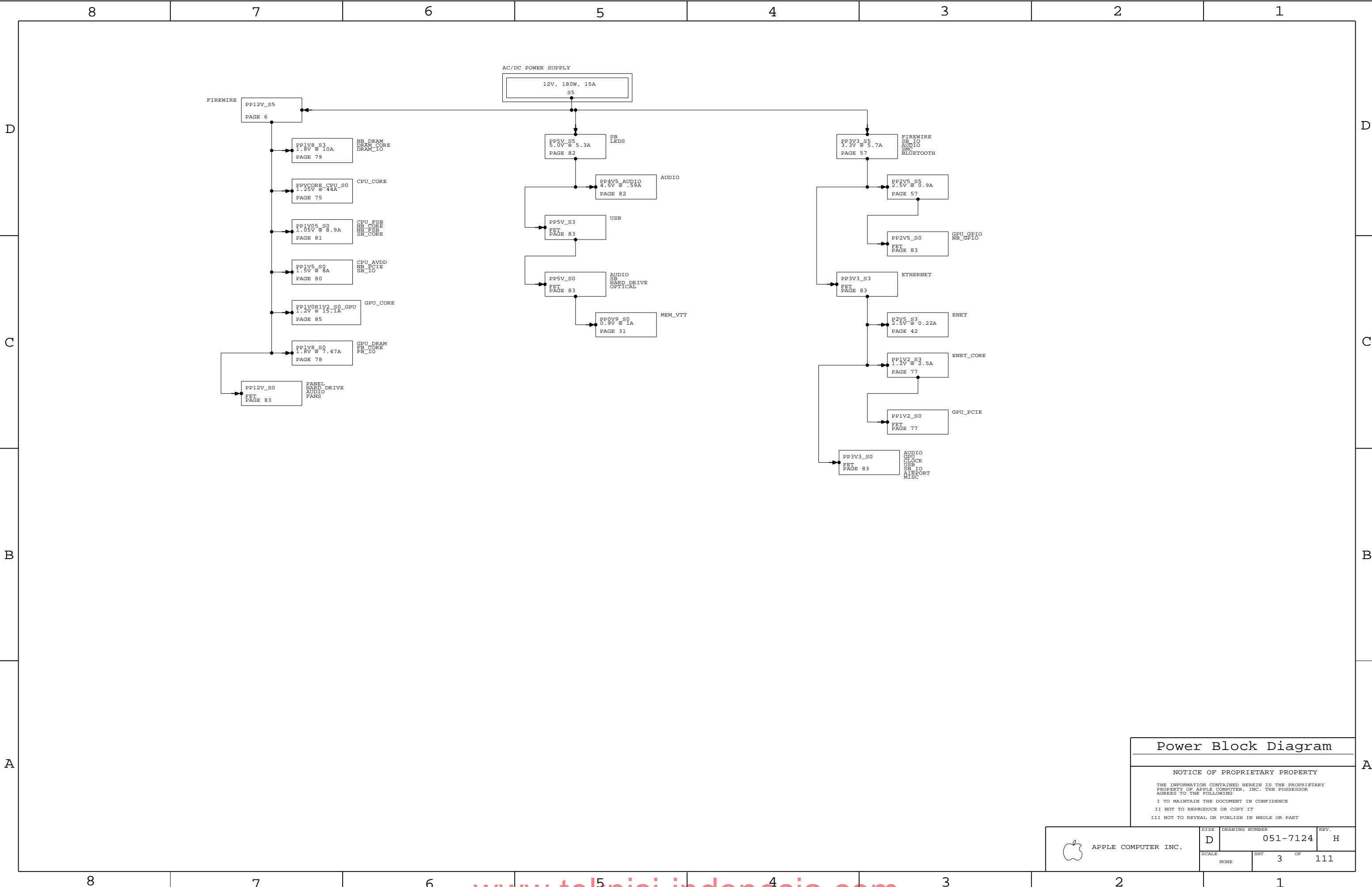
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Power Block Diagram

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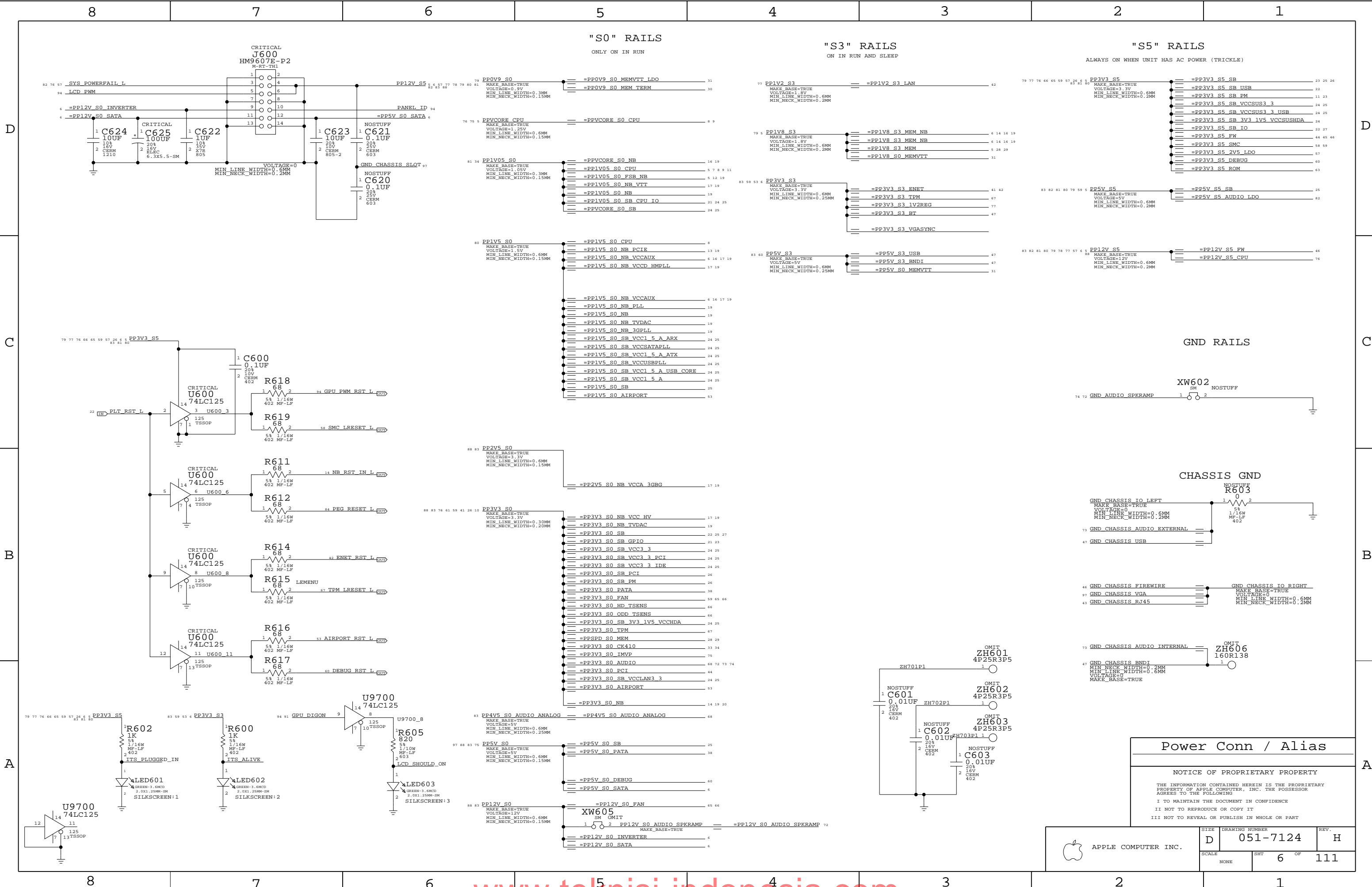
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

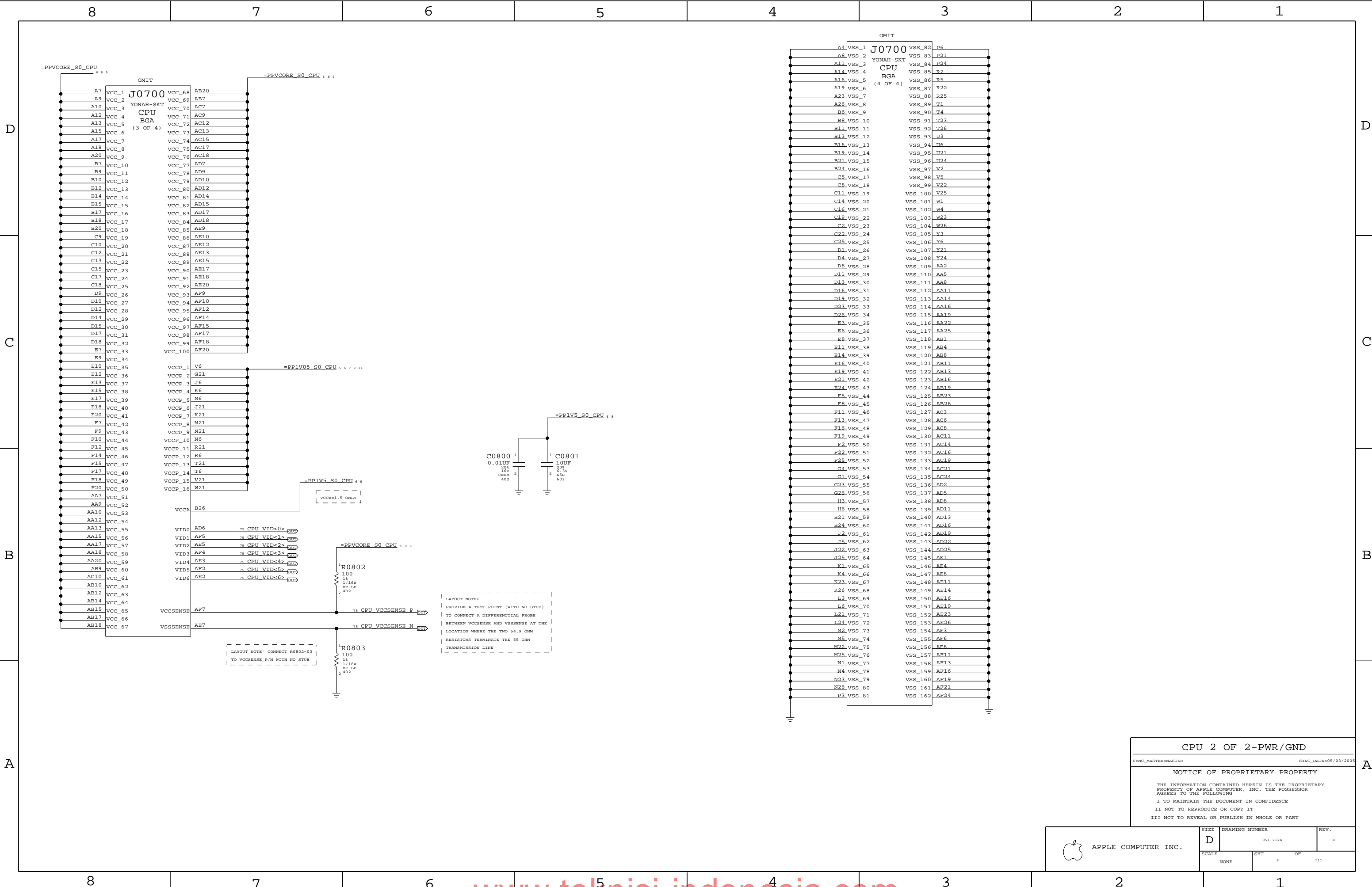
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	COMMON																																																																												
	<table><tr><th>PART#</th><th>QTY</th><th>DESCRIPTION</th><th>REFERENCE DESIGNATOR(S)</th><th>CRITICAL</th><th>BOM OPTION</th></tr><tr><td>511S0025</td><td>1</td><td>IC,CPU-SKT,479BGA</td><td>J0700</td><td>CRITICAL</td><td></td></tr><tr><td>338S0328</td><td>1</td><td>IC,945PM,NORTHBRIDGE</td><td>U1200</td><td>CRITICAL</td><td></td></tr><tr><td>343S0385</td><td>1</td><td>IC,SB,652BGA</td><td>U2100</td><td>CRITICAL</td><td></td></tr><tr><td>338S0345</td><td>1</td><td>IC,ATI,M56D,GRAPHXCTLR,880BGA,LF</td><td>U8400</td><td>CRITICAL</td><td></td></tr><tr><td>359S0101</td><td>1</td><td>IC,CY28445-5,CLK GEN,68PIN QFP</td><td>U3301</td><td>CRITICAL</td><td></td></tr><tr><td>338S0270</td><td>1</td><td>IC,88E8053,GIGABIT ENET XCVR,64P QFN,MND</td><td>U4101</td><td>CRITICAL</td><td></td></tr><tr><td>(335S0382) 341S1797</td><td>1</td><td>IC,ENET LAN ROM</td><td>U4102</td><td>CRITICAL</td><td></td></tr><tr><td>338S0279</td><td>1</td><td>IC,FW32306,1394A LINK,TQFP</td><td>U4400</td><td>CRITICAL</td><td></td></tr></table>																PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL		338S0328	1	IC,945PM,NORTHBRIDGE	U1200	CRITICAL		343S0385	1	IC,SB,652BGA	U2100	CRITICAL		338S0345	1	IC,ATI,M56D,GRAPHXCTLR,880BGA,LF	U8400	CRITICAL		359S0101	1	IC,CY28445-5,CLK GEN,68PIN QFP	U3301	CRITICAL		338S0270	1	IC,88E8053,GIGABIT ENET XCVR,64P QFN,MND	U4101	CRITICAL		(335S0382) 341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL		338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL								
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CPU 2 OF 2-PWR/GND

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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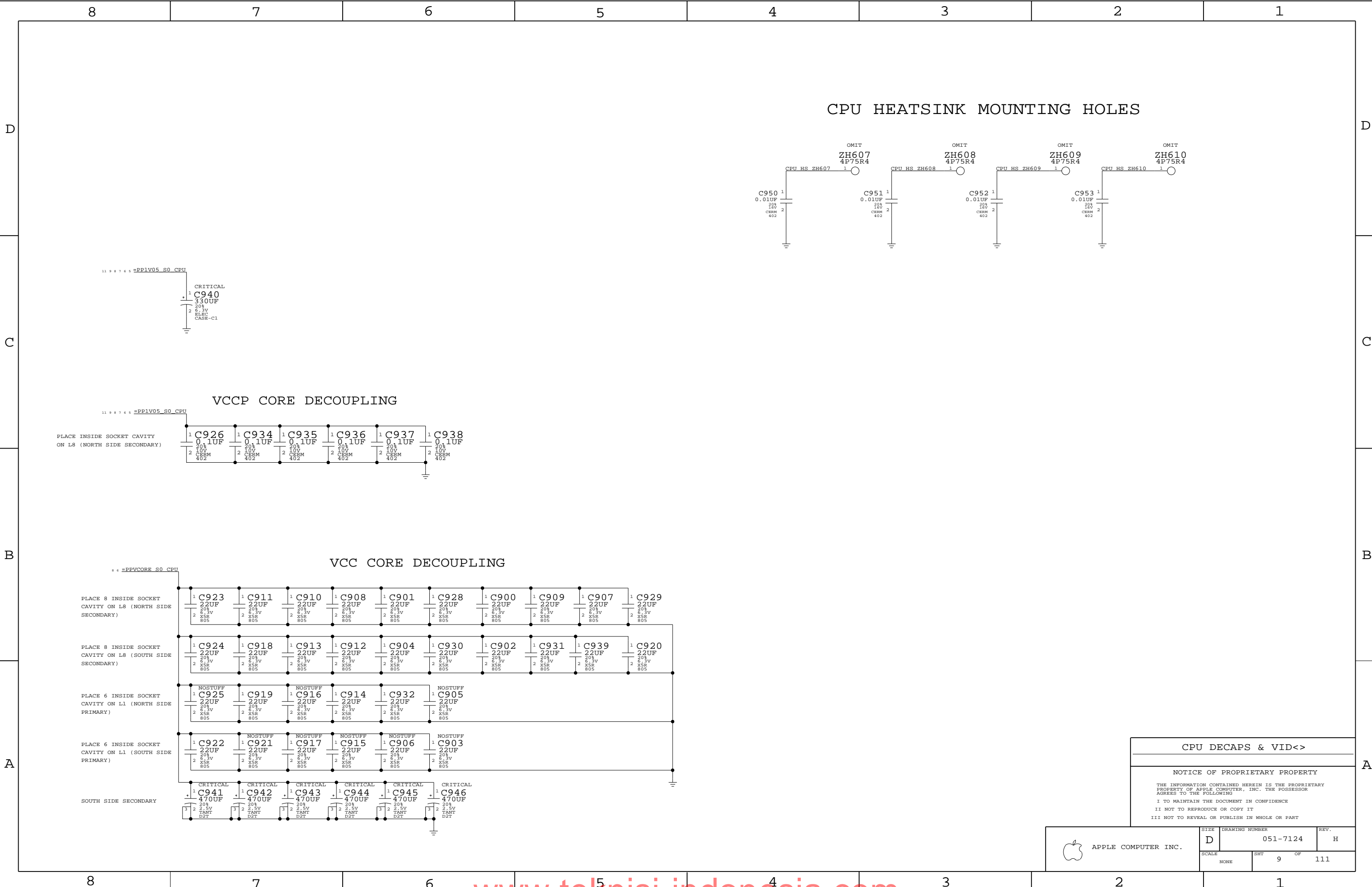
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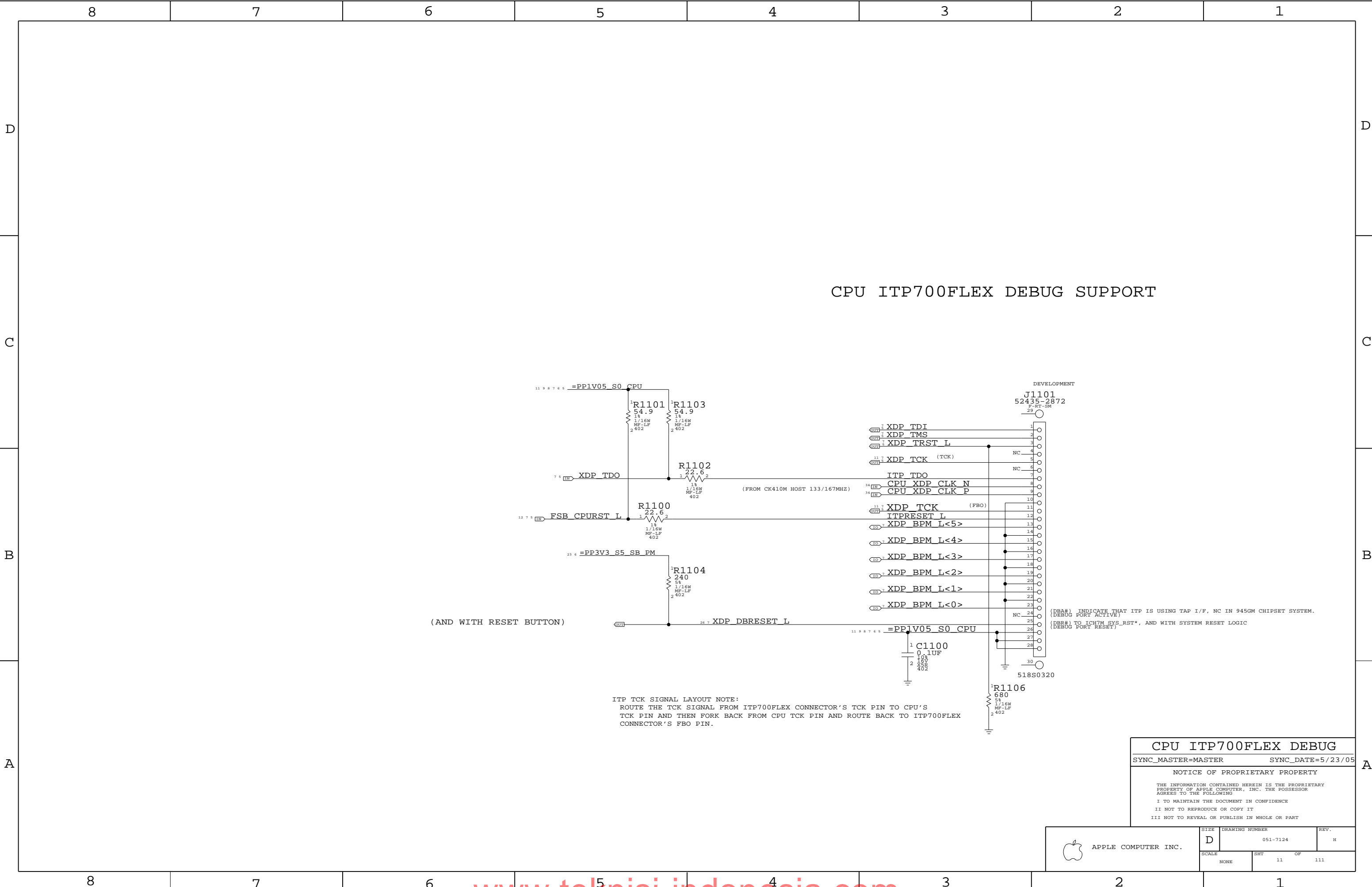
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CPU DECAPS & VID<>		
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	OF
NONE		9	111



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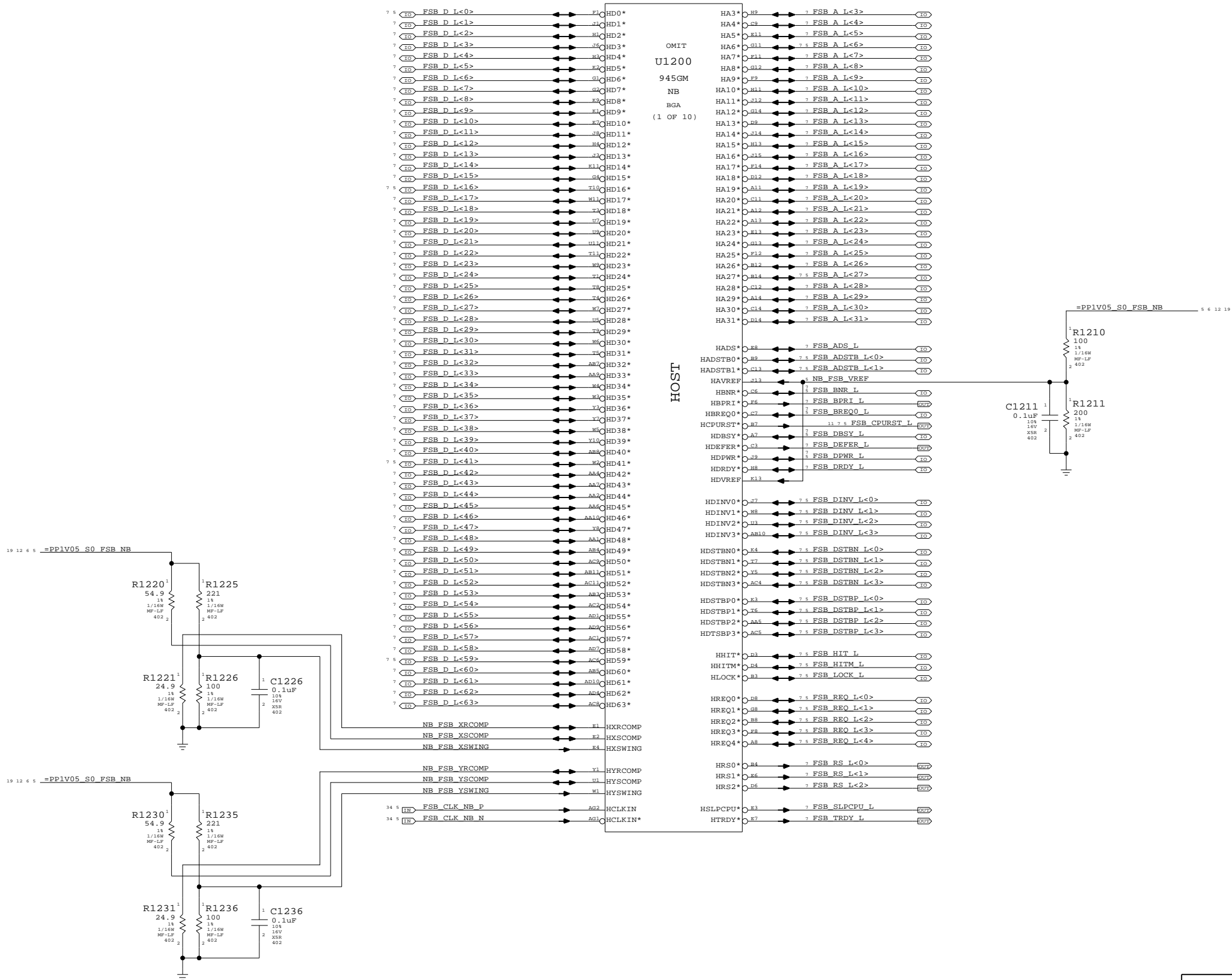
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NB CPU Interface

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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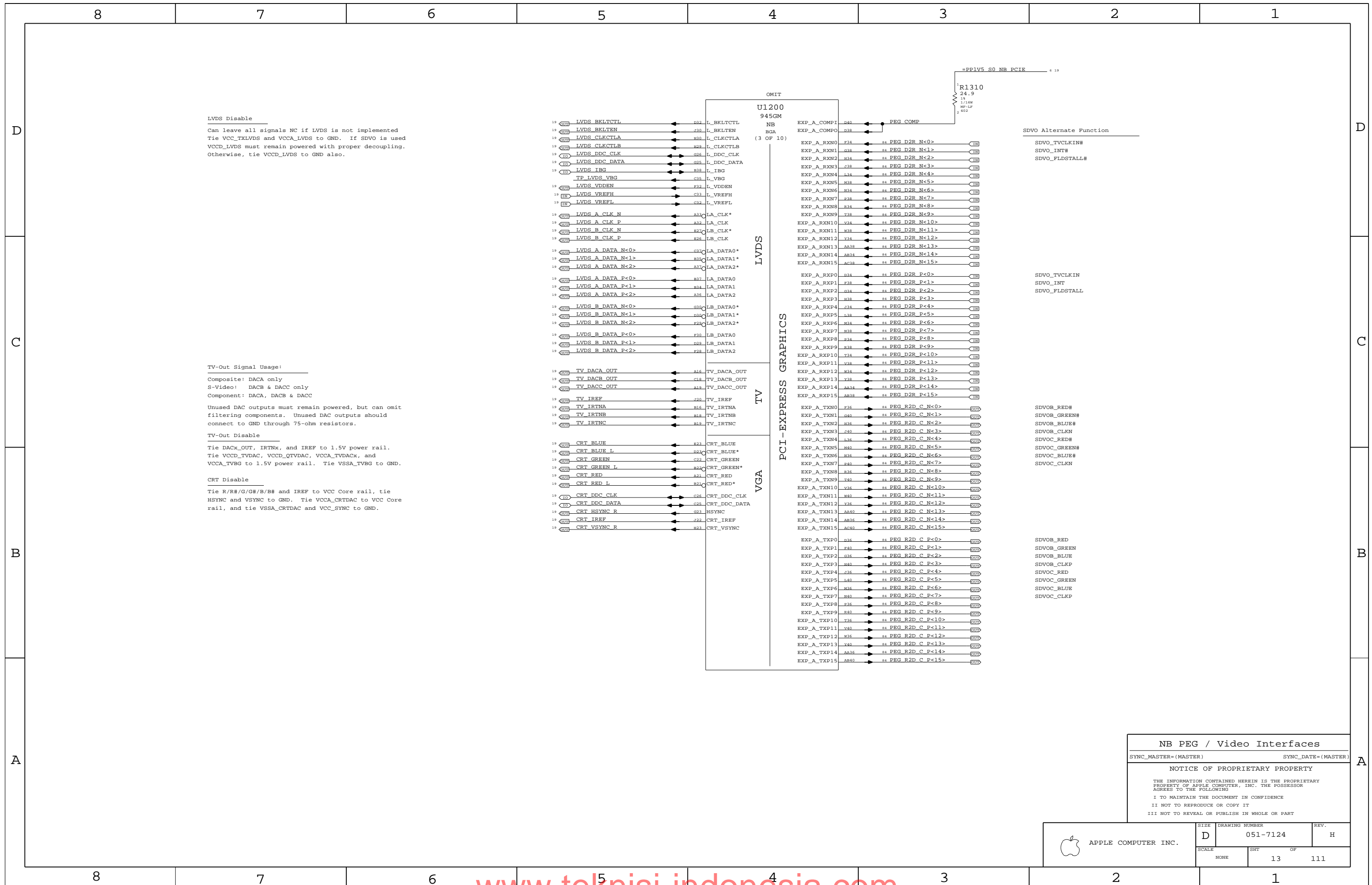
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

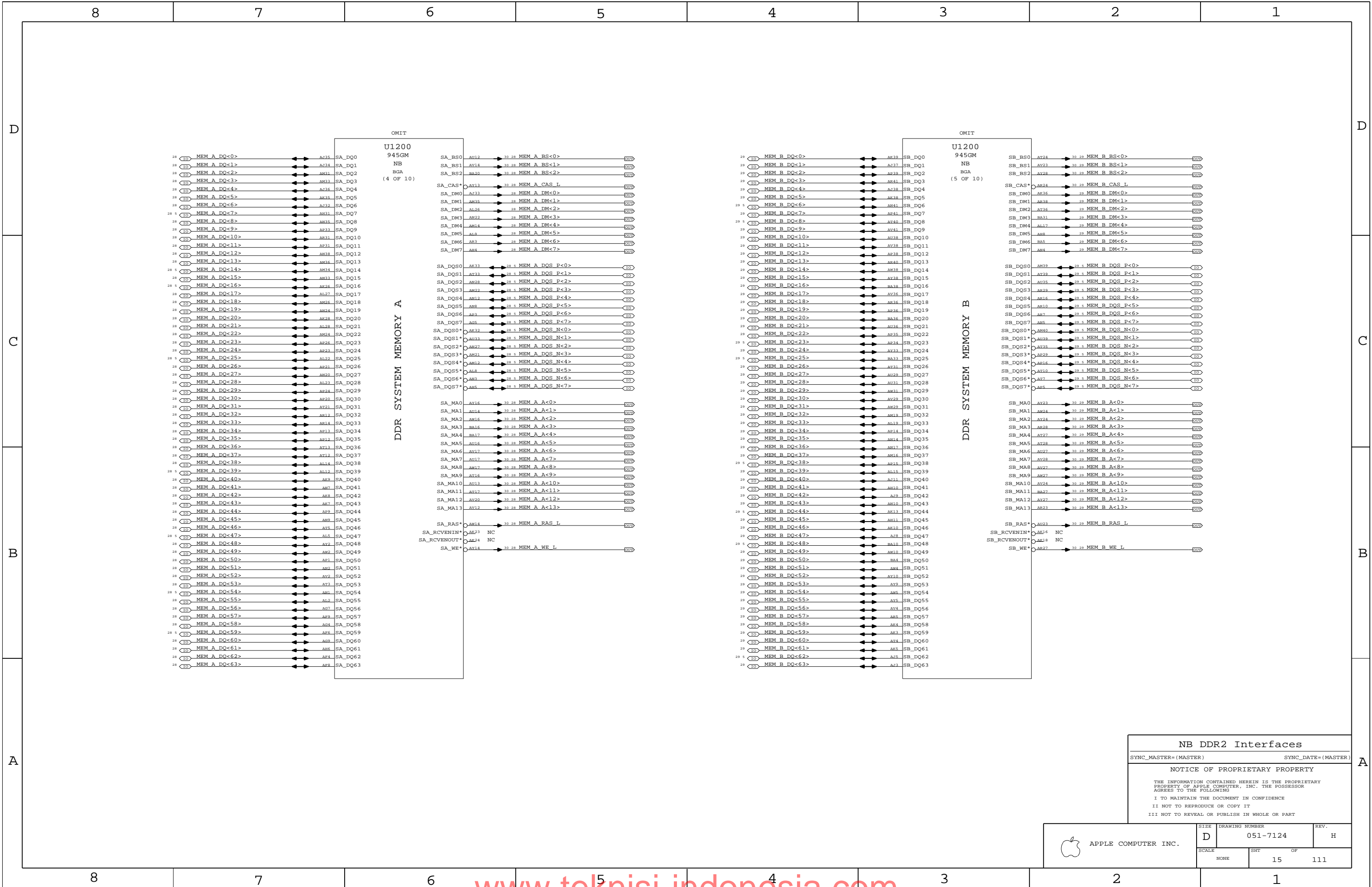
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

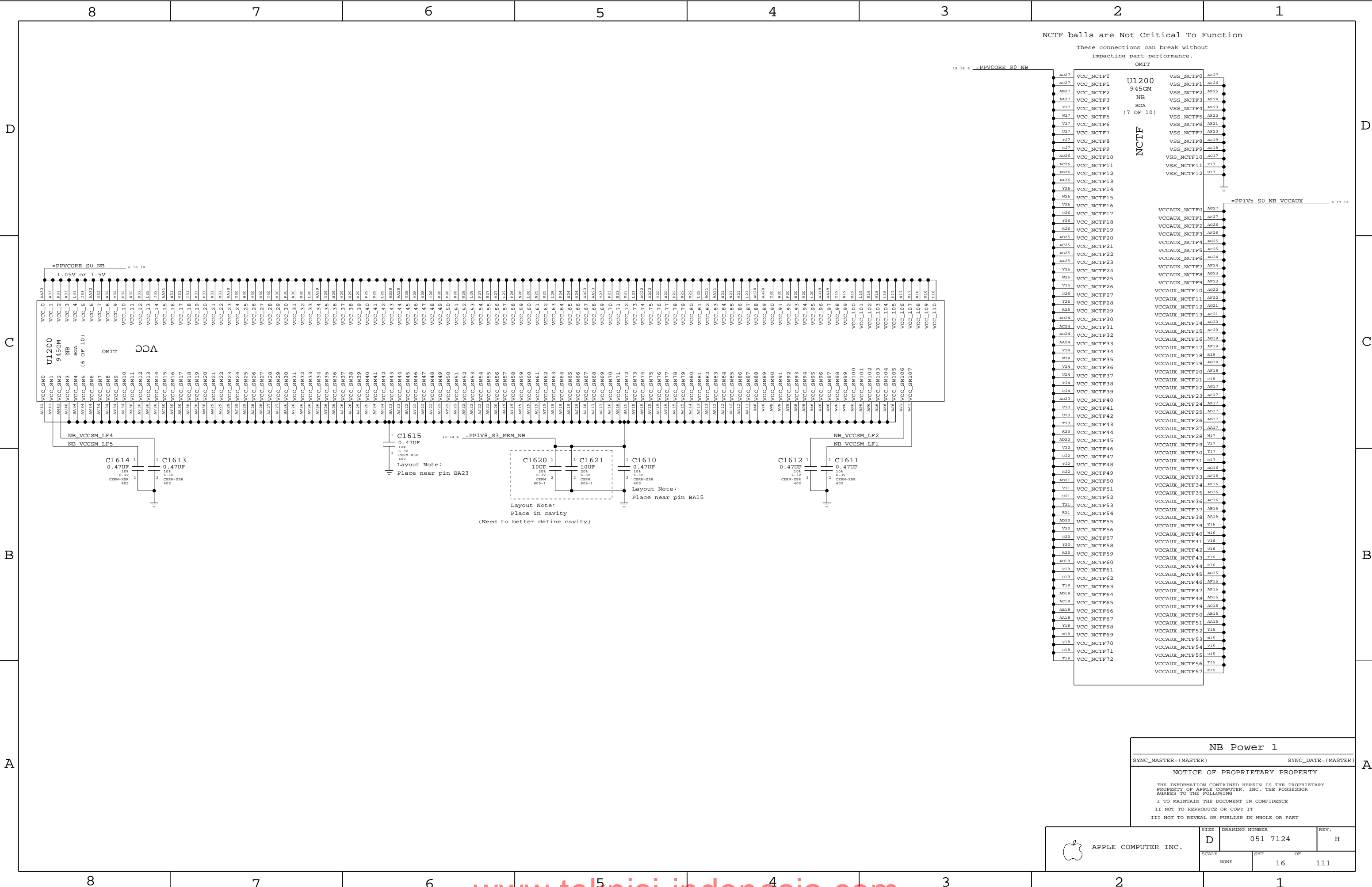
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	D	051-7124	H
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NONE		12	111







NB Power 1

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

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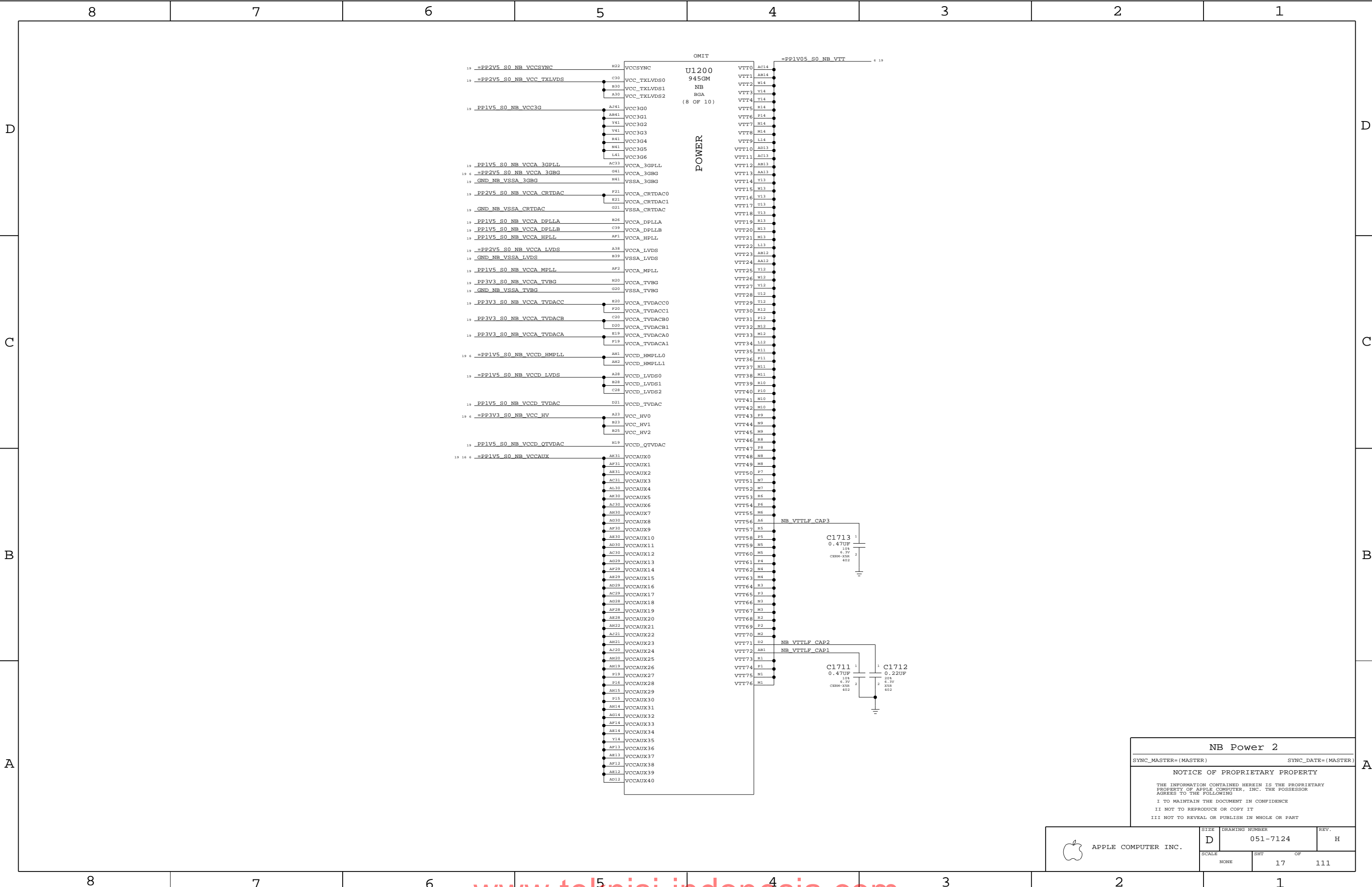
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	D	051-7124	H
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NONE		16	111



NB Power 2

SYNC_MASTER=(MASTER)SYNC_DATE=(MASTER)

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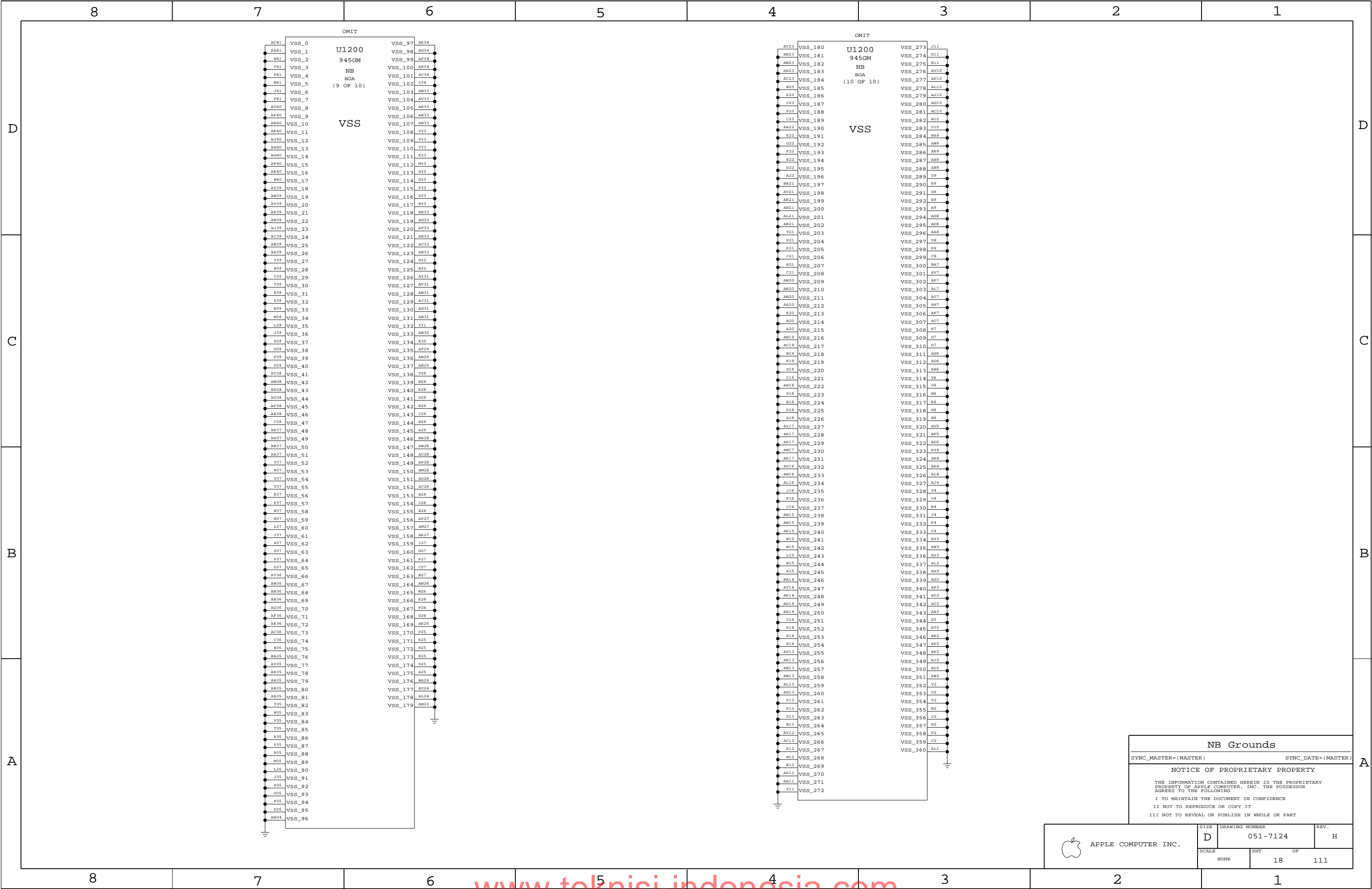
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NB Grounds

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)


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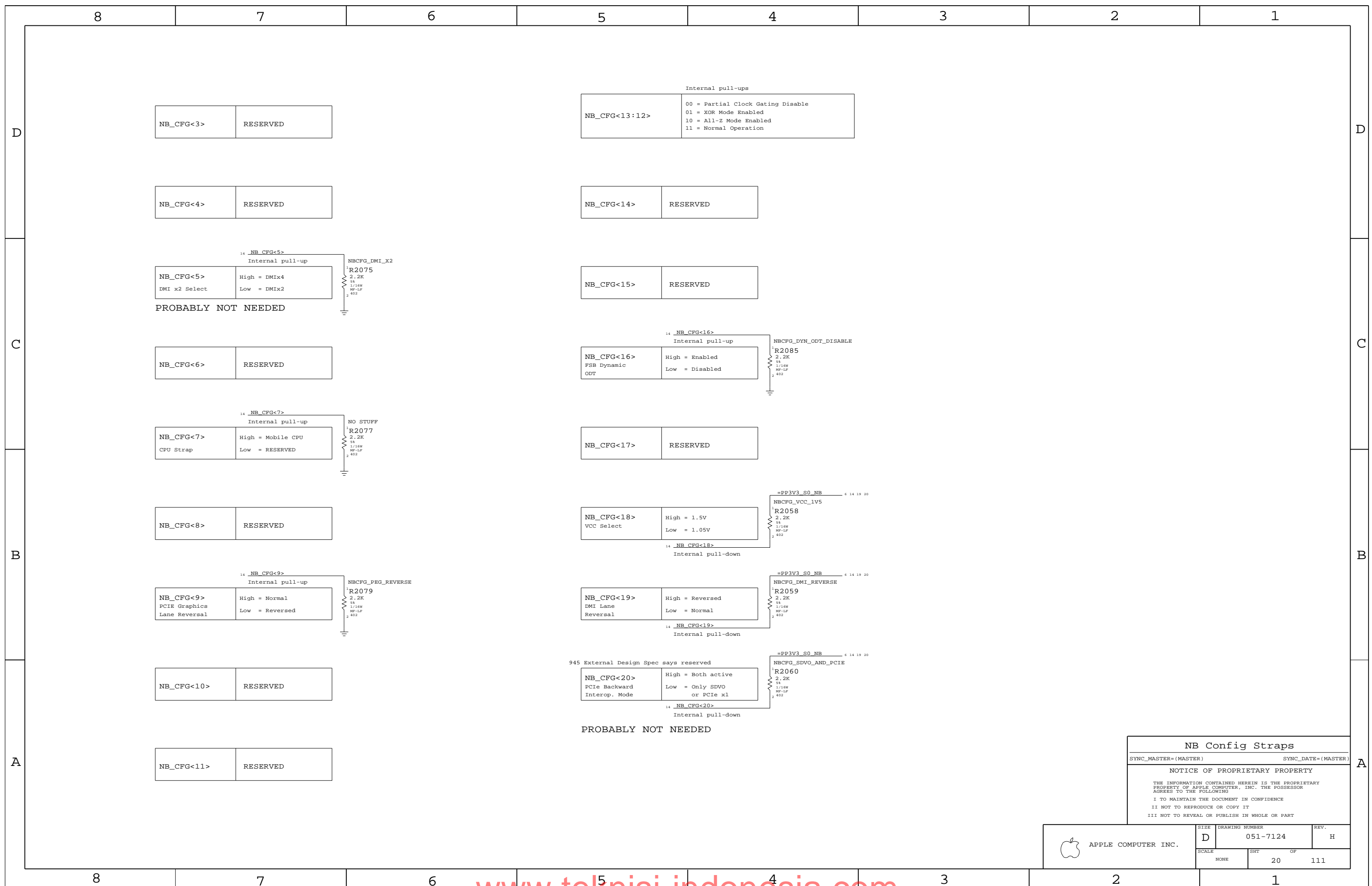
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

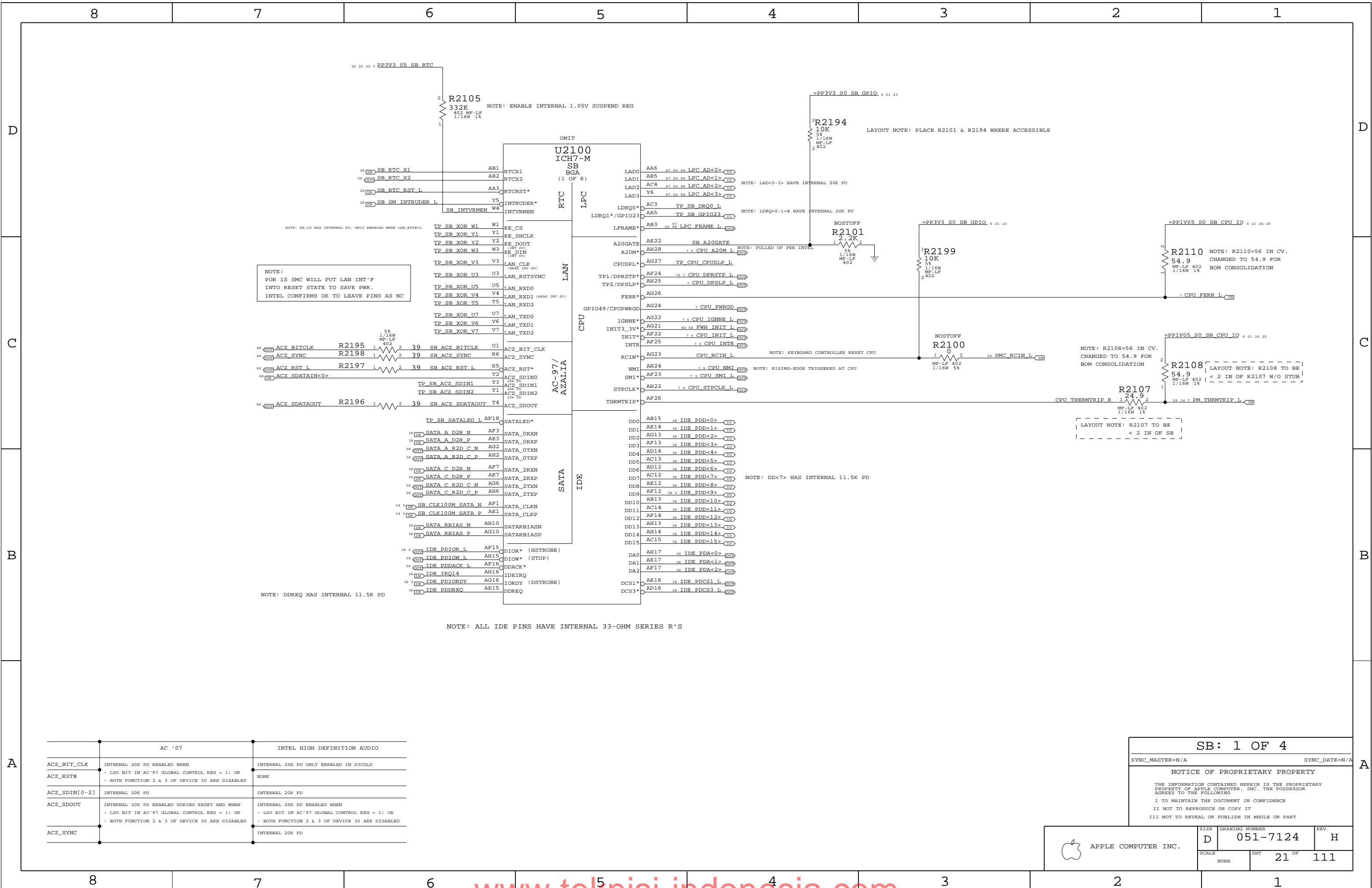
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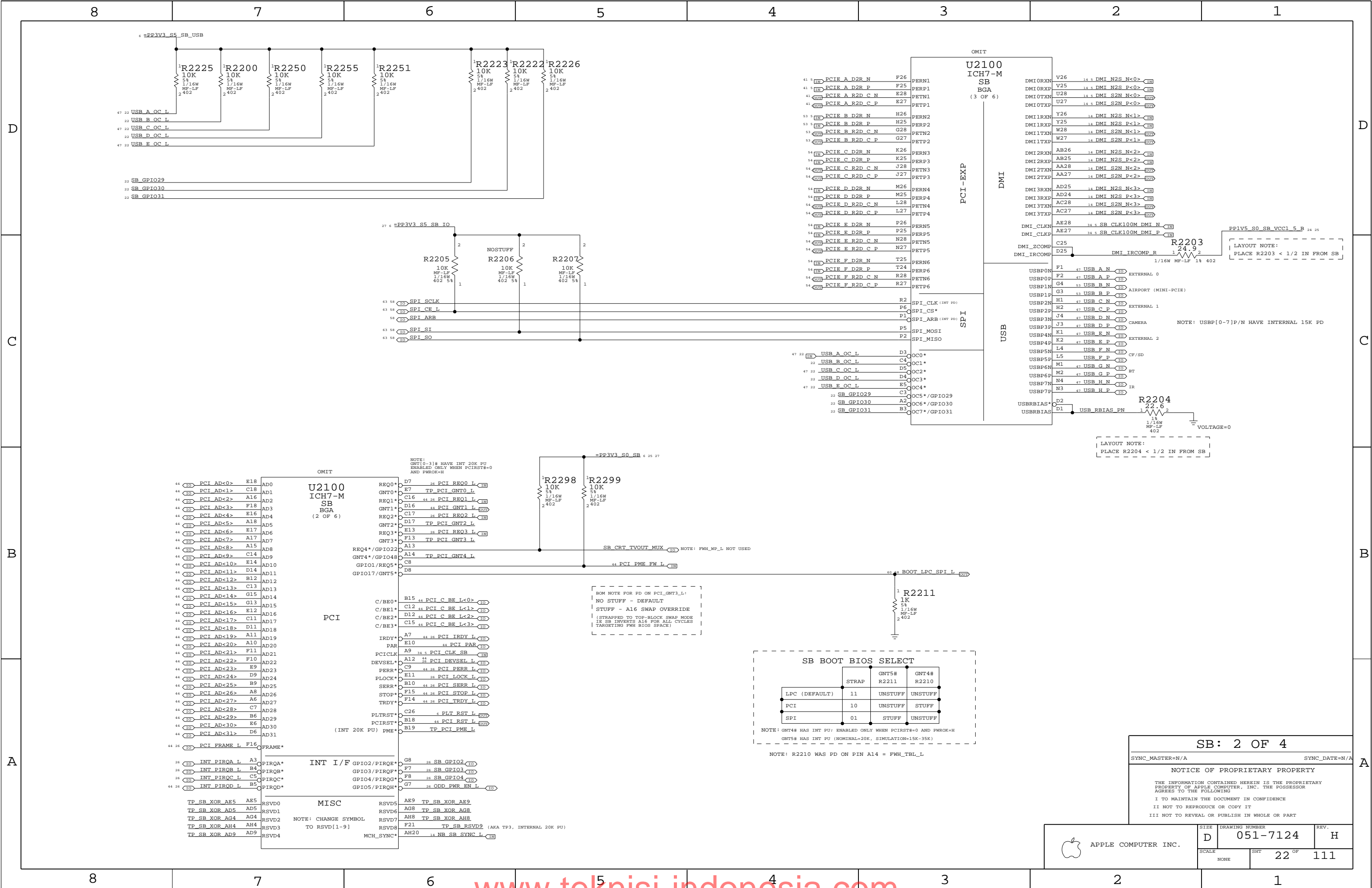
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

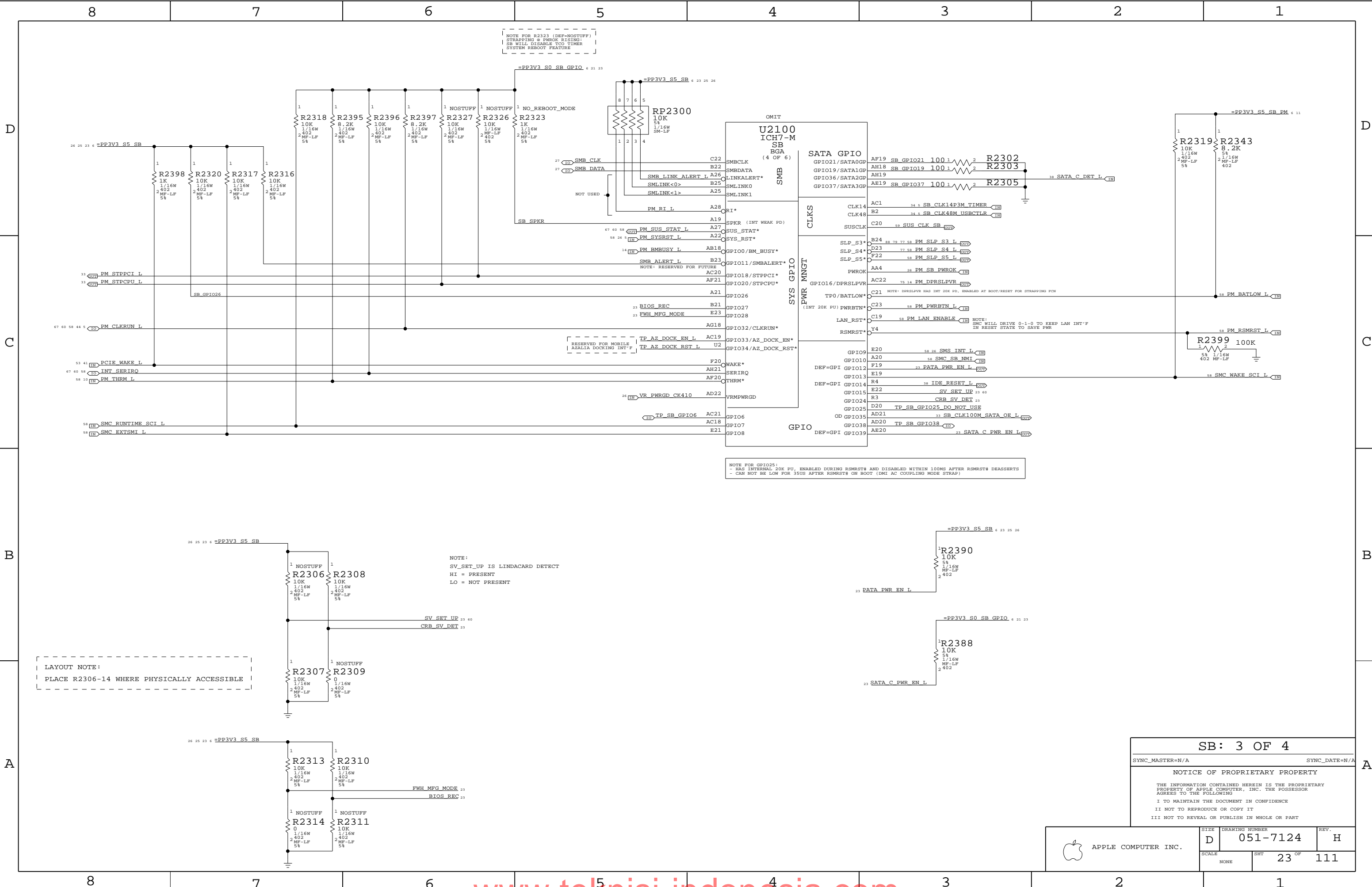
 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 18	OF 111

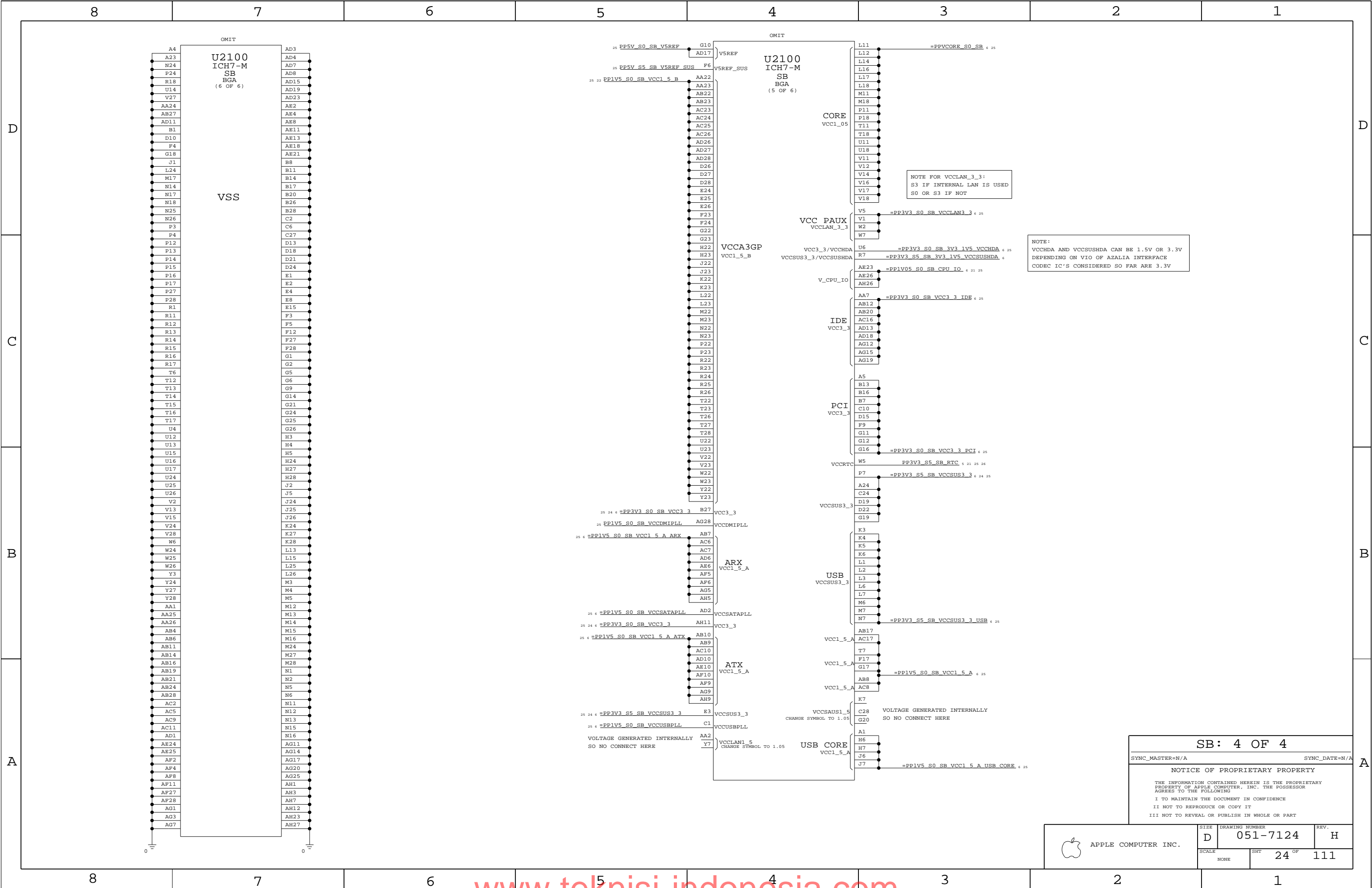


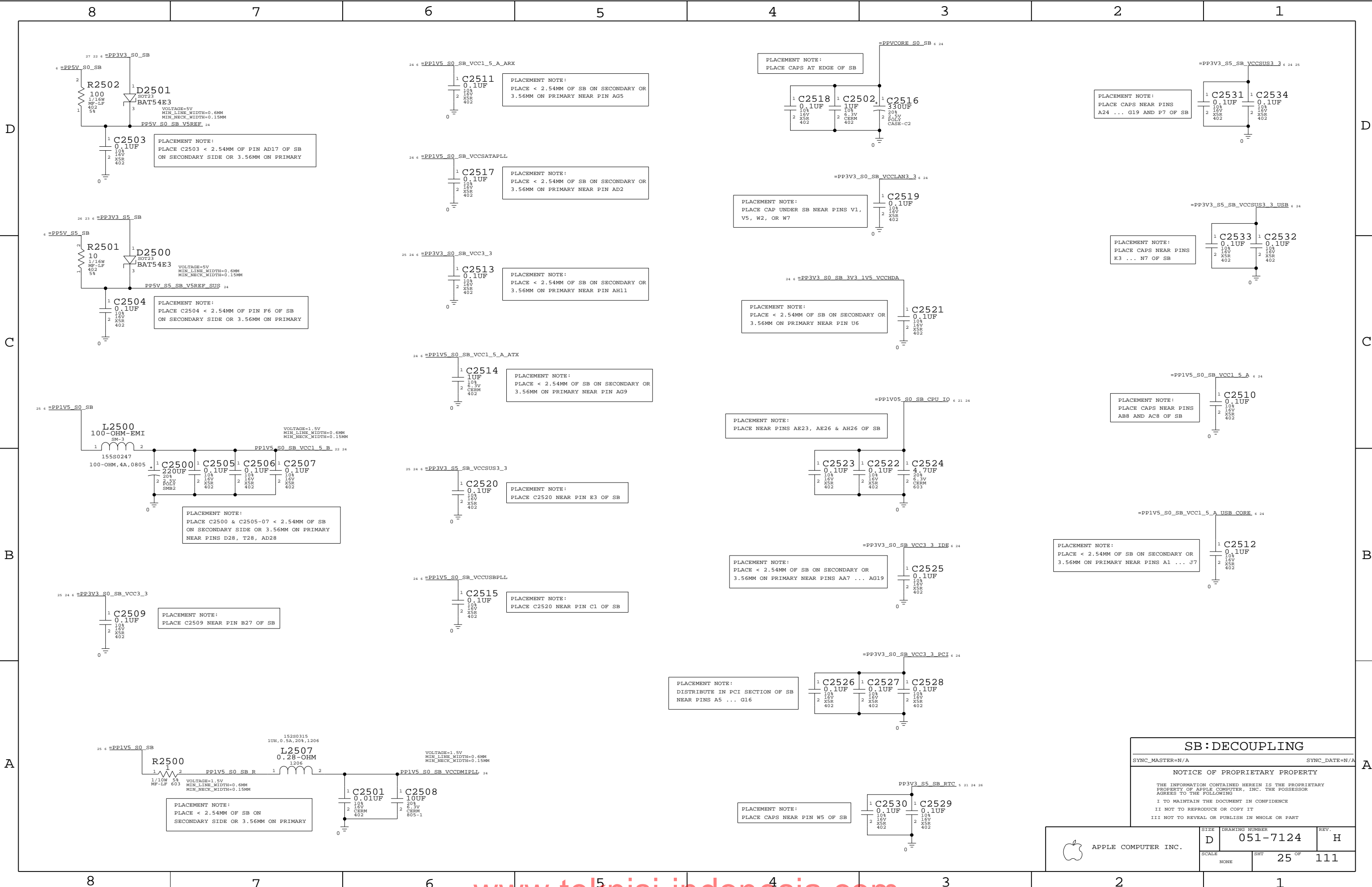


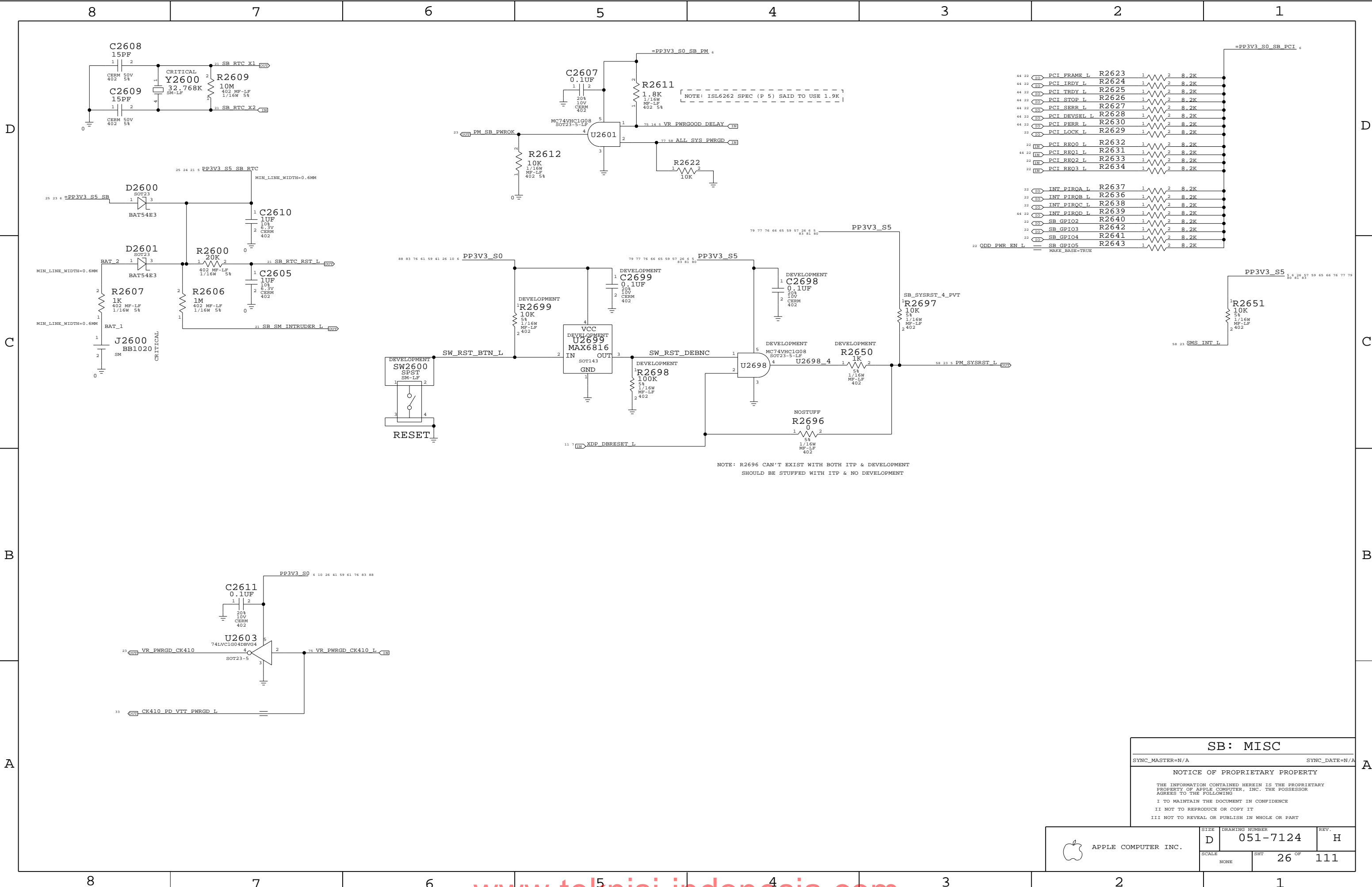












SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

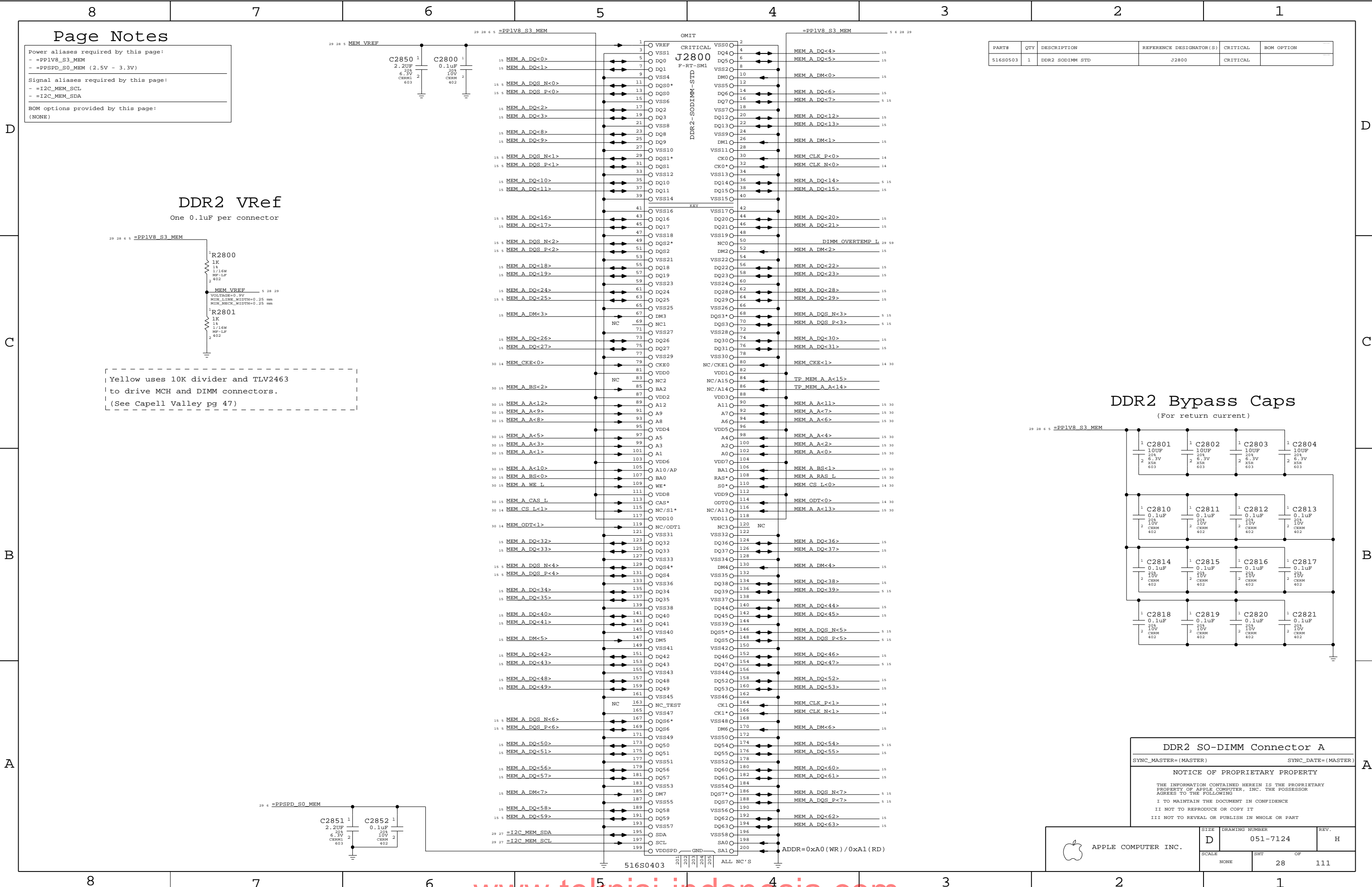
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	D	051-7124	H
SCALE	NONE	SHT	26 OF 111



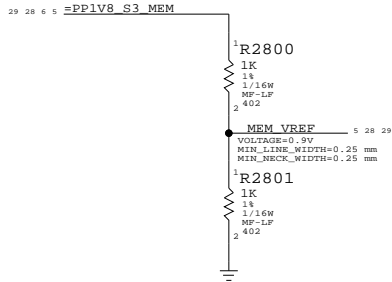
Page Notes

Power aliases required by this page:
- =PPIV8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:
(NONE)

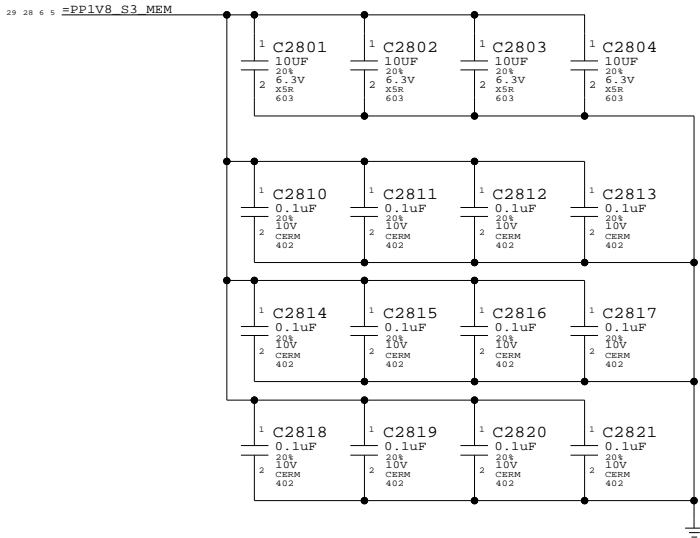
DDR2 VRef
One 0.1uF per connector



Yellow uses 10K divider and TLV2463 to drive MCH and DIMM connectors.
(See Capell Valley pg 47)

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
516S0503	1	DDR2 SODIMM STD	J2800	CRITICAL	

DDR2 Bypass Caps
(For return current)



DDR2 SO-DIMM Connector A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

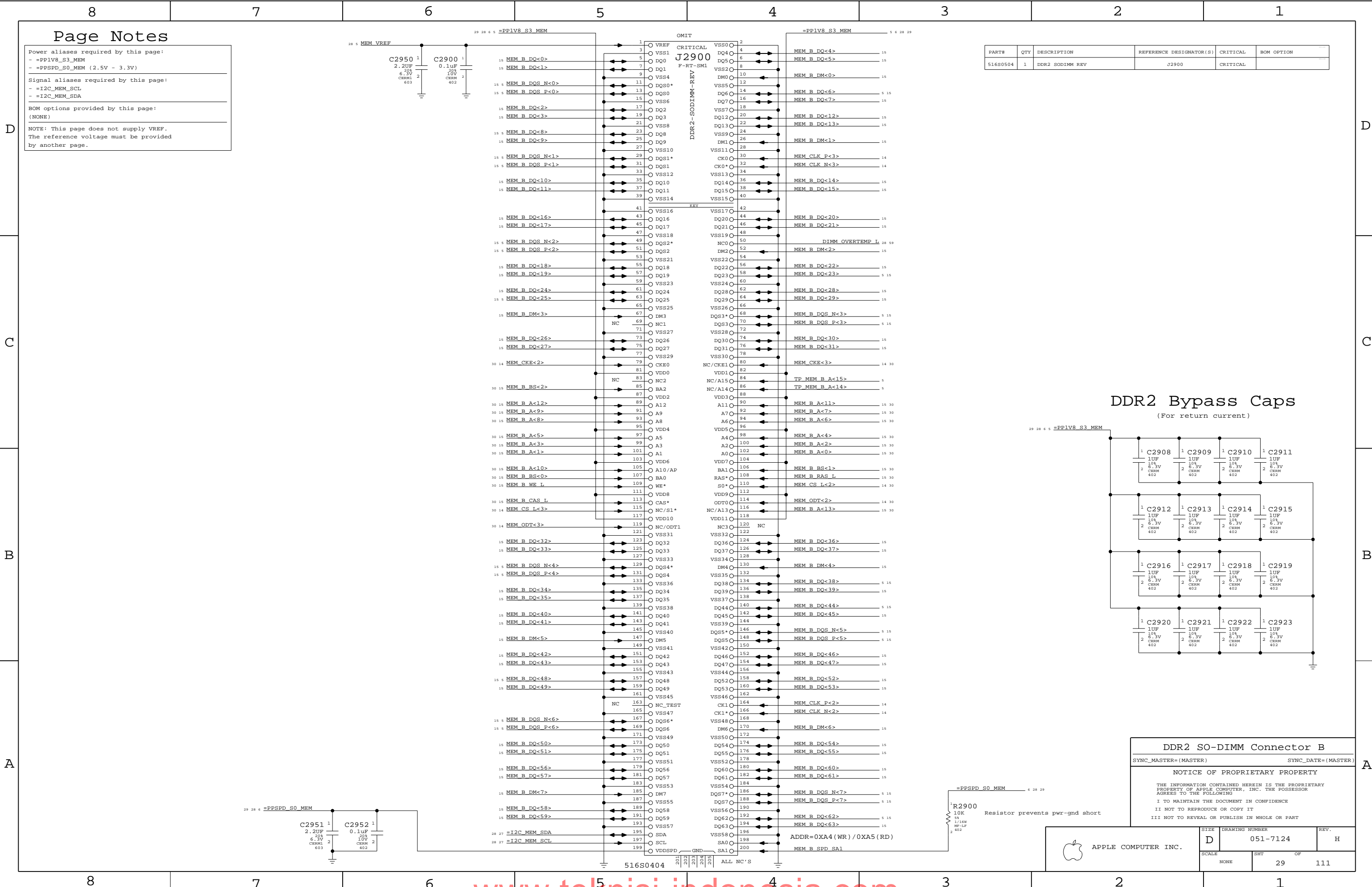
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 28	OF 111



Page Notes

Power aliases required by this page:
- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

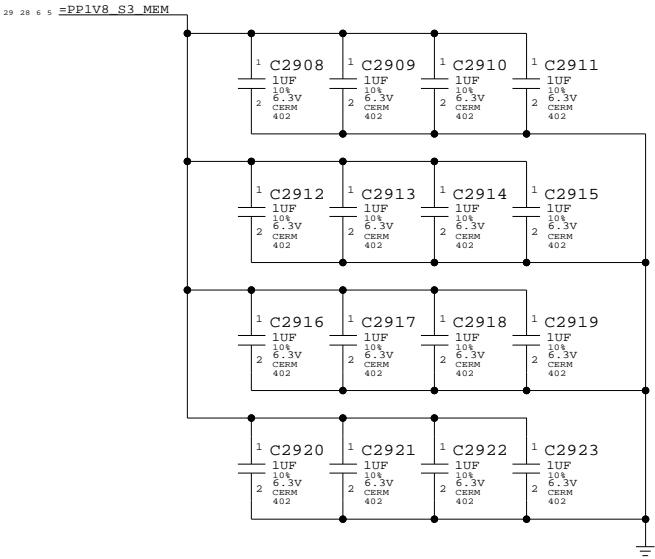
Signal aliases required by this page:
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:
(NONE)


NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
516S0504	1	DDR2 SODIMM REV	J2900	CRITICAL	

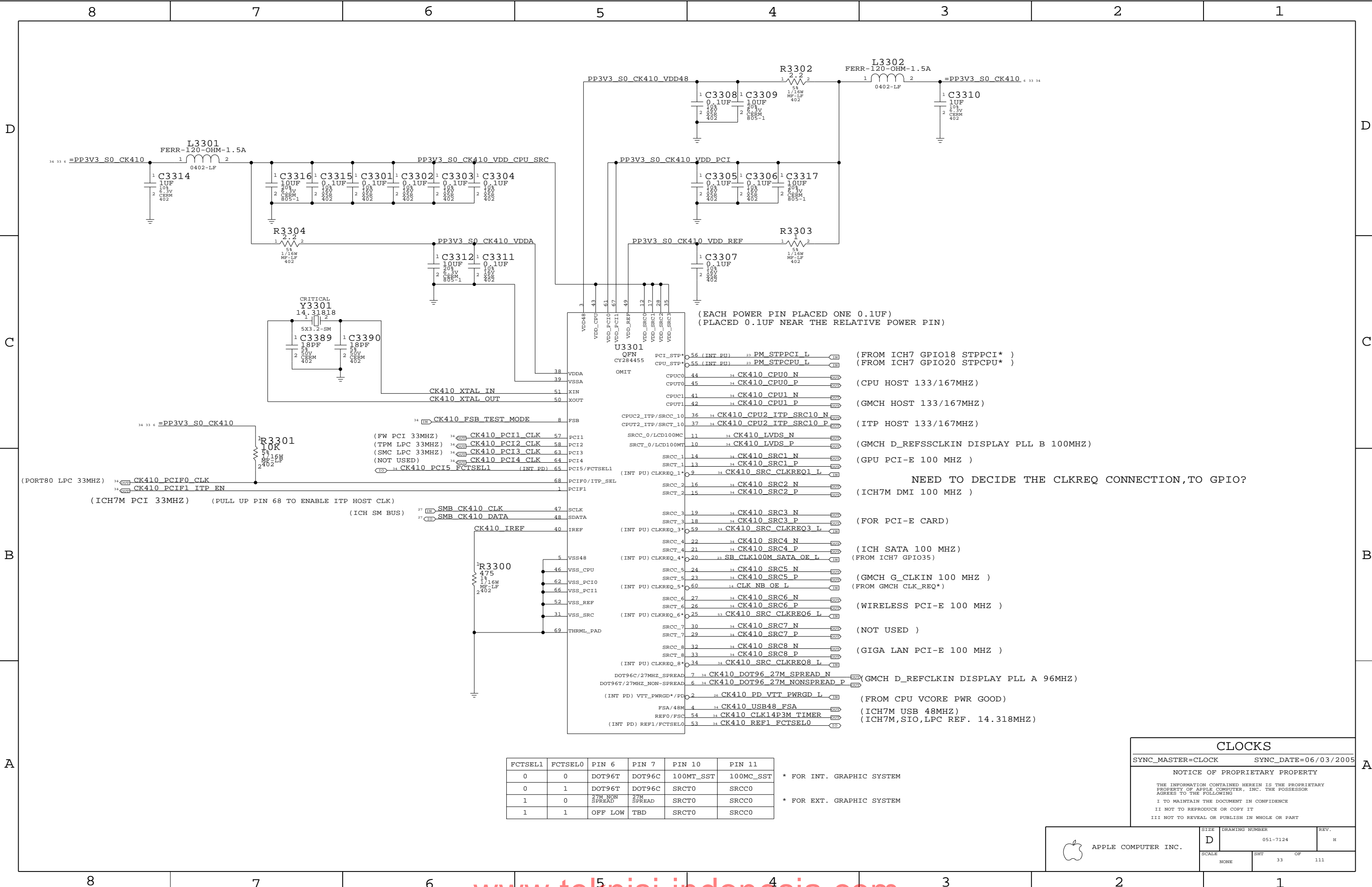
DDR2 Bypass Caps
(For return current)



DDR2 SO-DIMM Connector B	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT OF 29 111	





FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M NON SPREAD	27M SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

* FOR INT. GRAPHIC SYSTEM

* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC_MASTER=CLOCK

SYNC_DATE=06/03/2005

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SIZE D

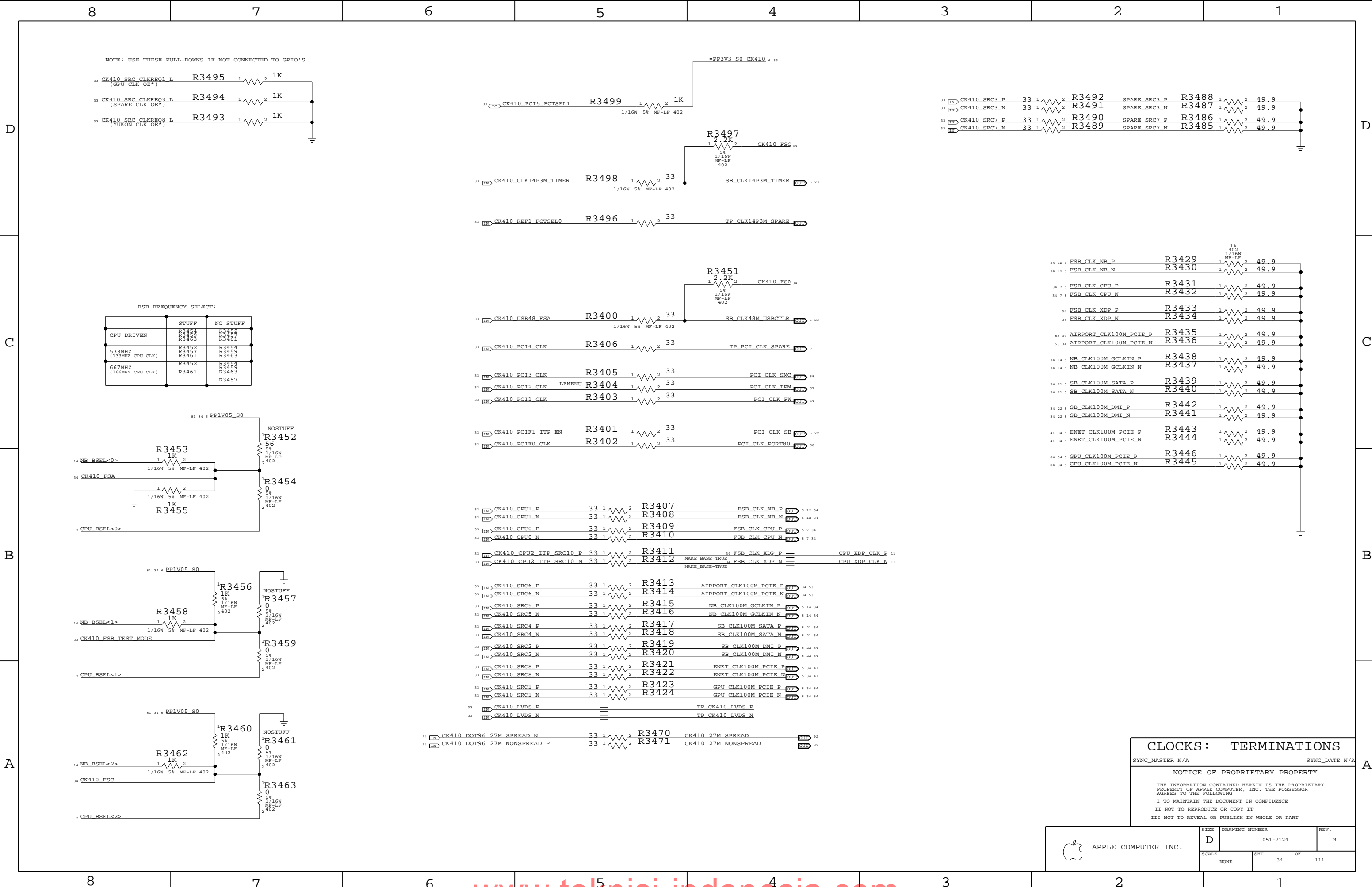
DRAWING NUMBER 051-7124

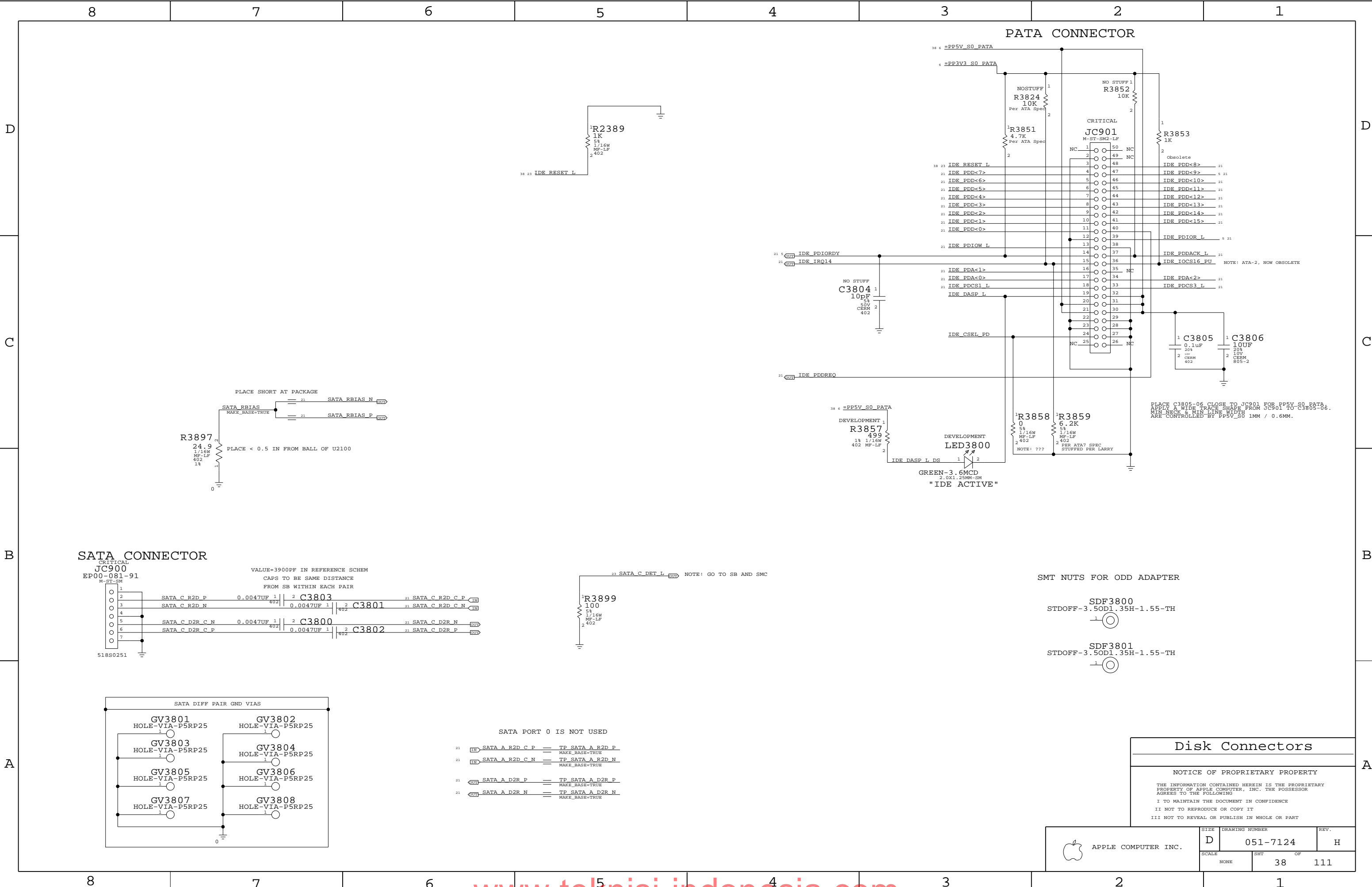
REV. H

SCALE NONE

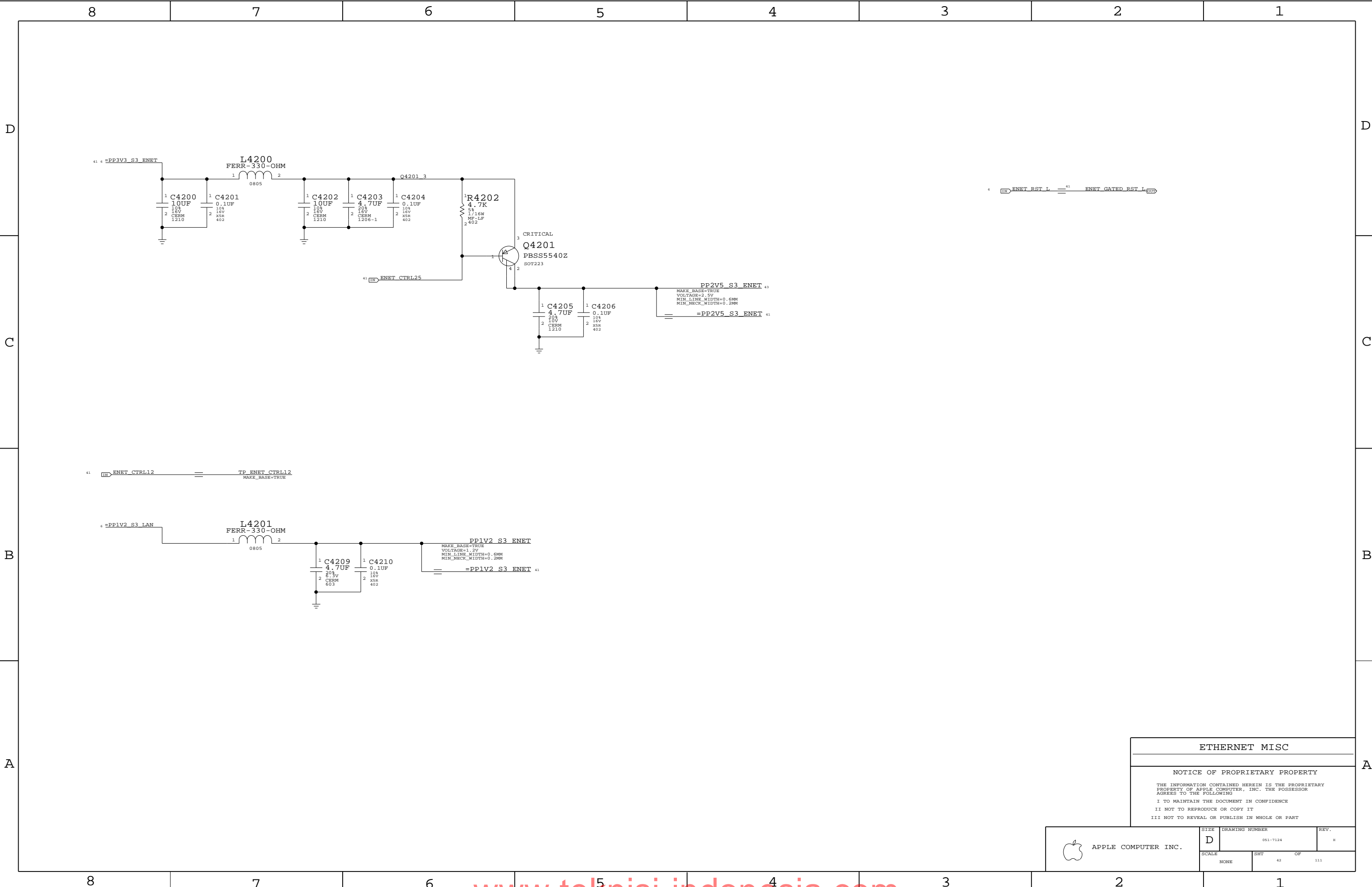
SHT 33

OF 111

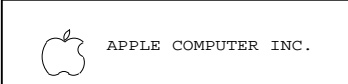


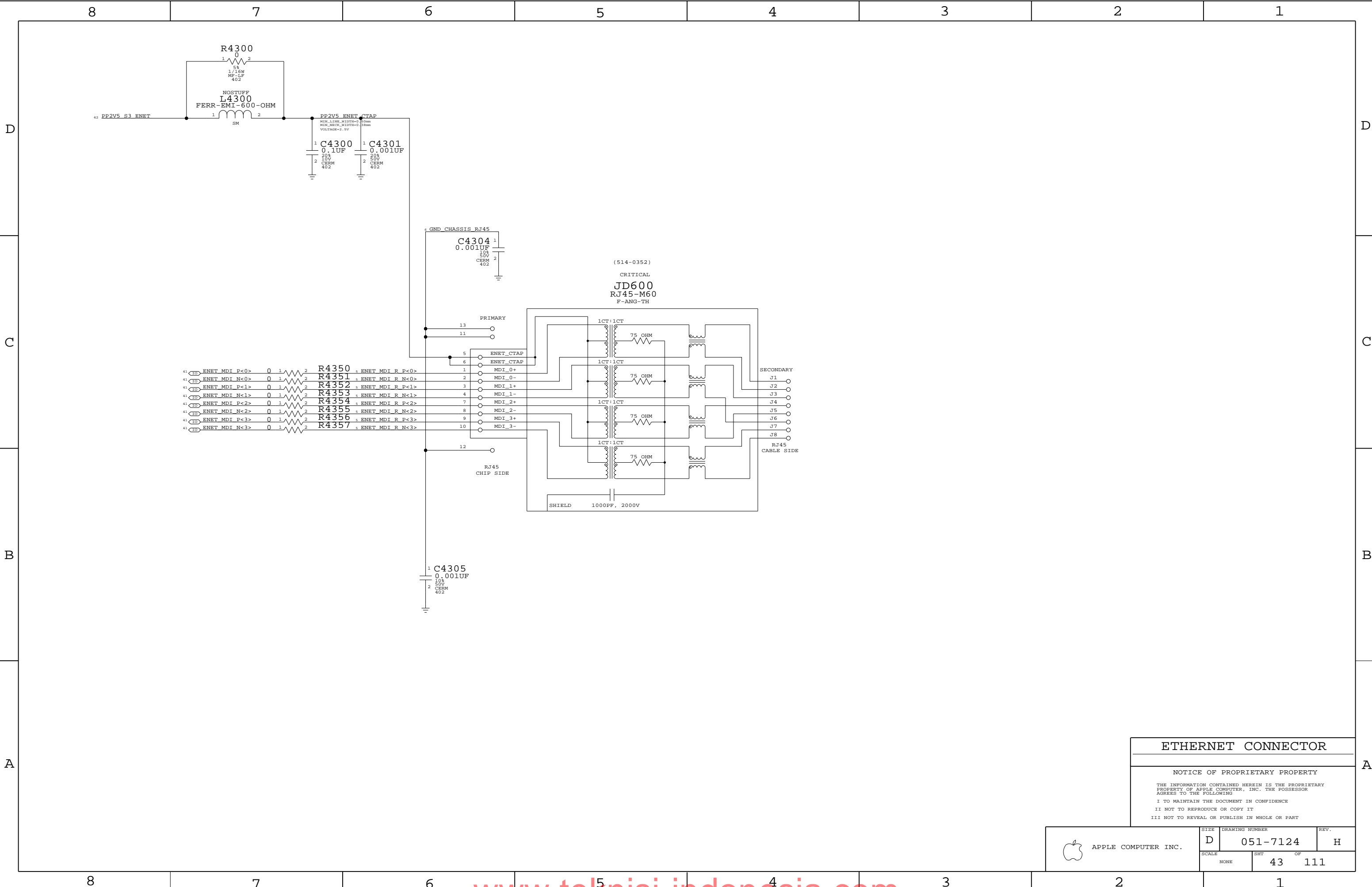






ETHERNET MISC		
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SIZE D	DRAWING NUMBER 051-7124	REV. H
SCALE NONE	SHT 42	OF 111





ETHERNET CONNECTOR

NOTICE OF PROPRIETARY PROPERTY

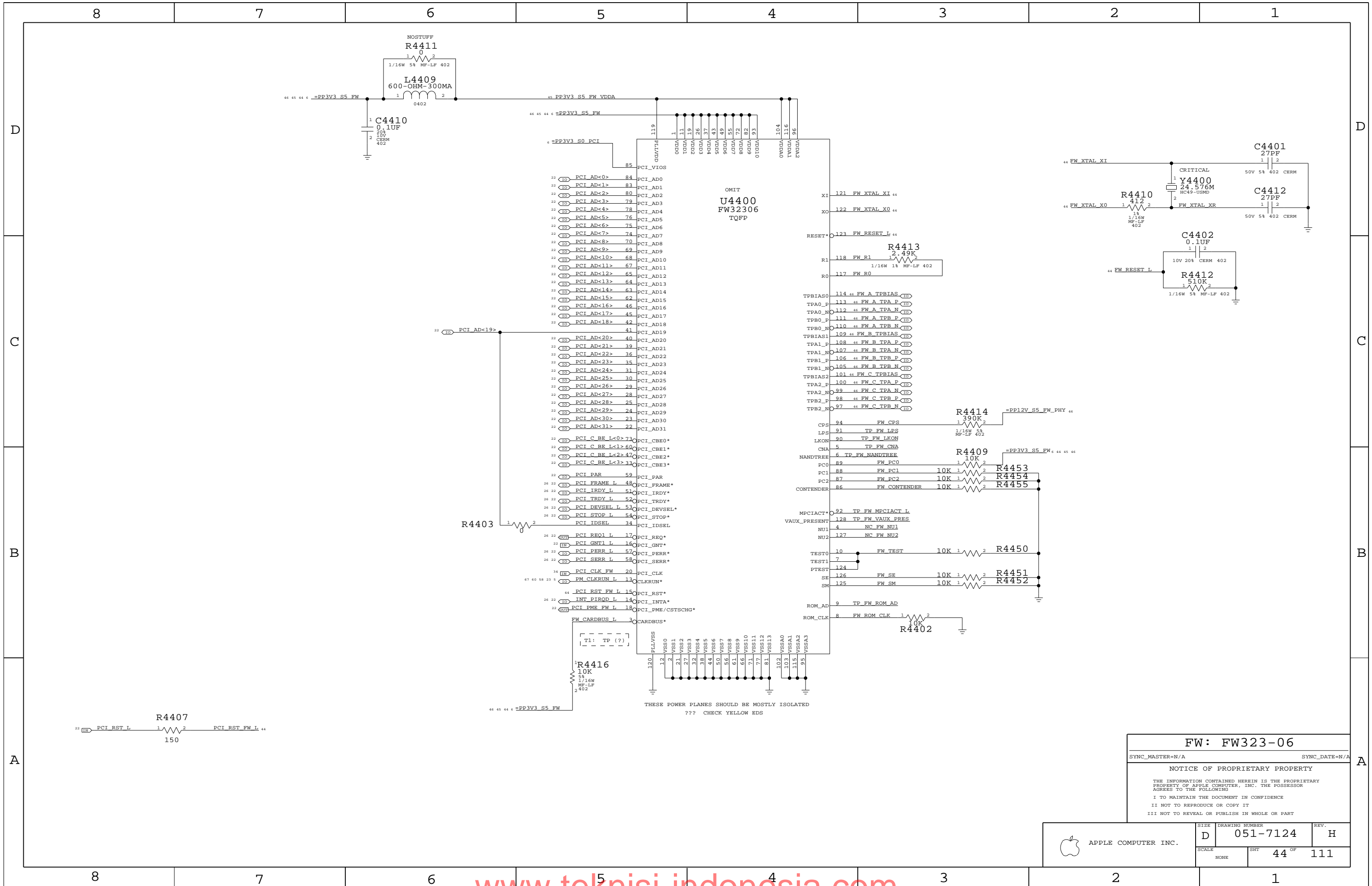
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

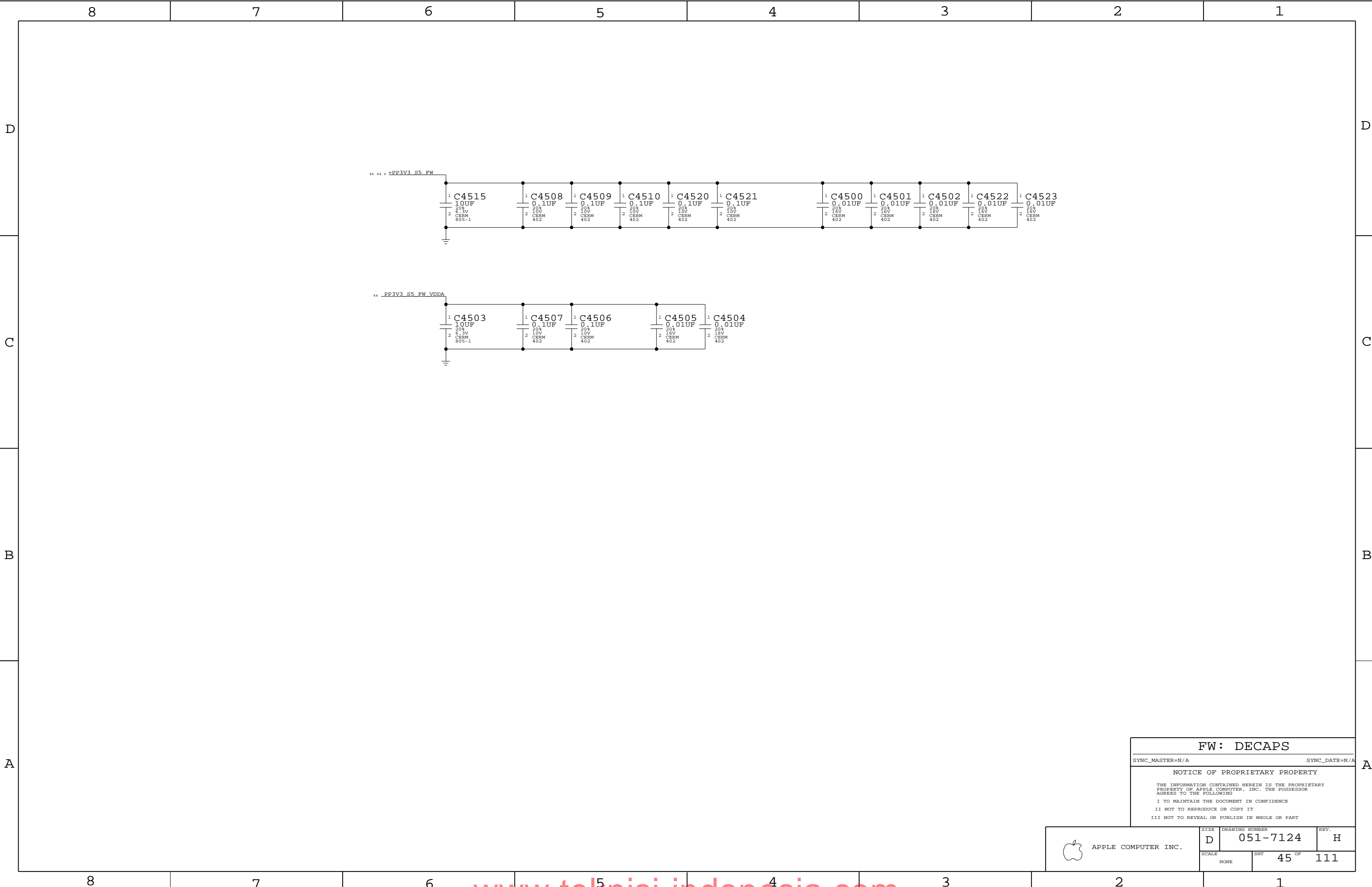
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-7124		H
SCALE		SHT	OF	
NONE		43	111	





FW: DECAPS

SYNC_MASTER=N/A SYNC_DATE=N/A


NOTICE OF PROPRIETARY PROPERTY

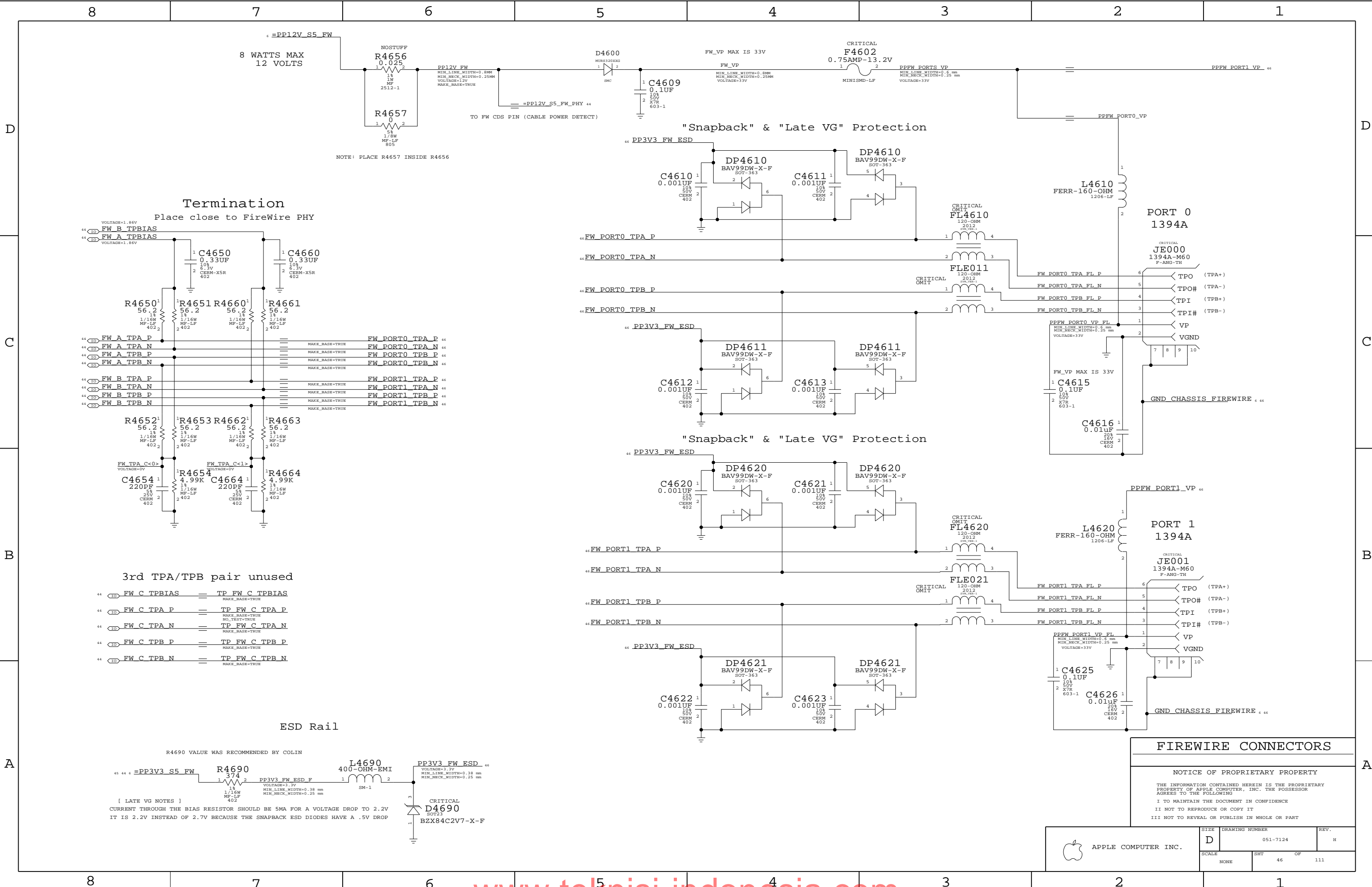
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

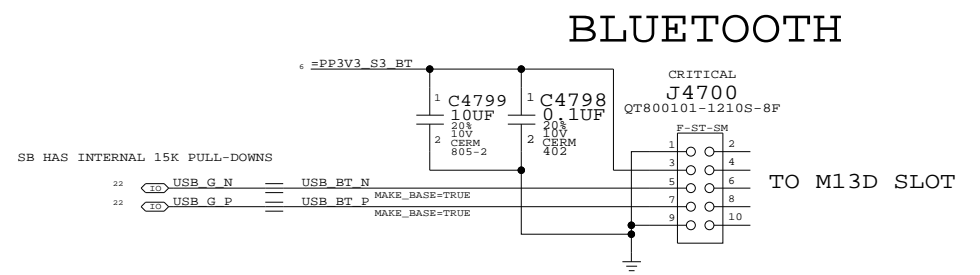
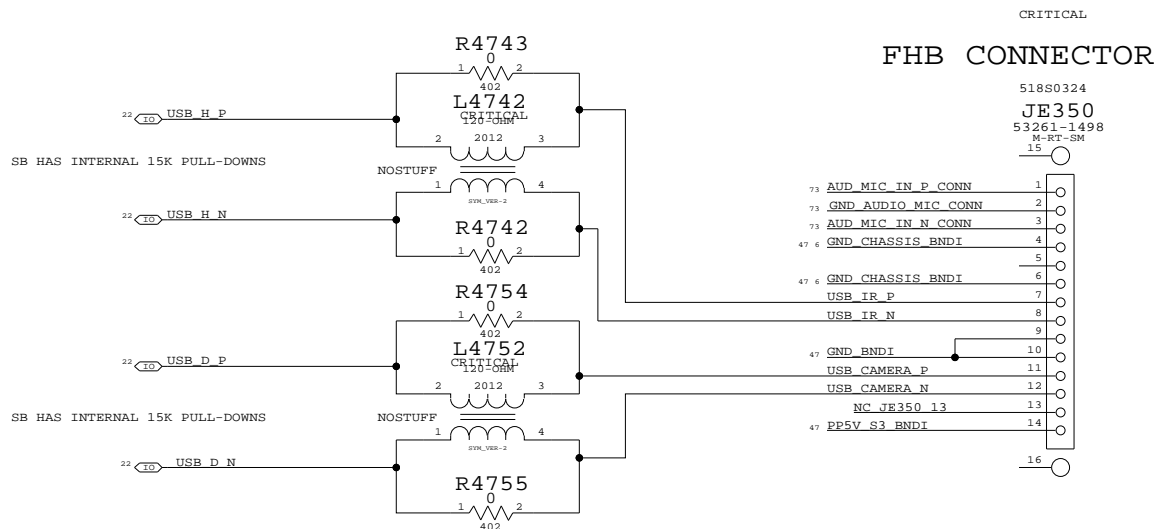
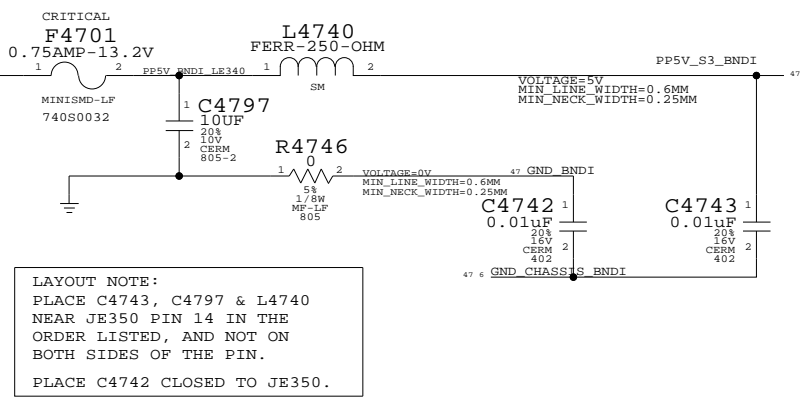
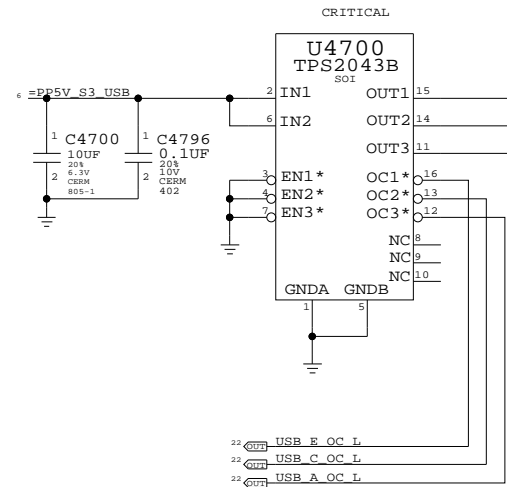
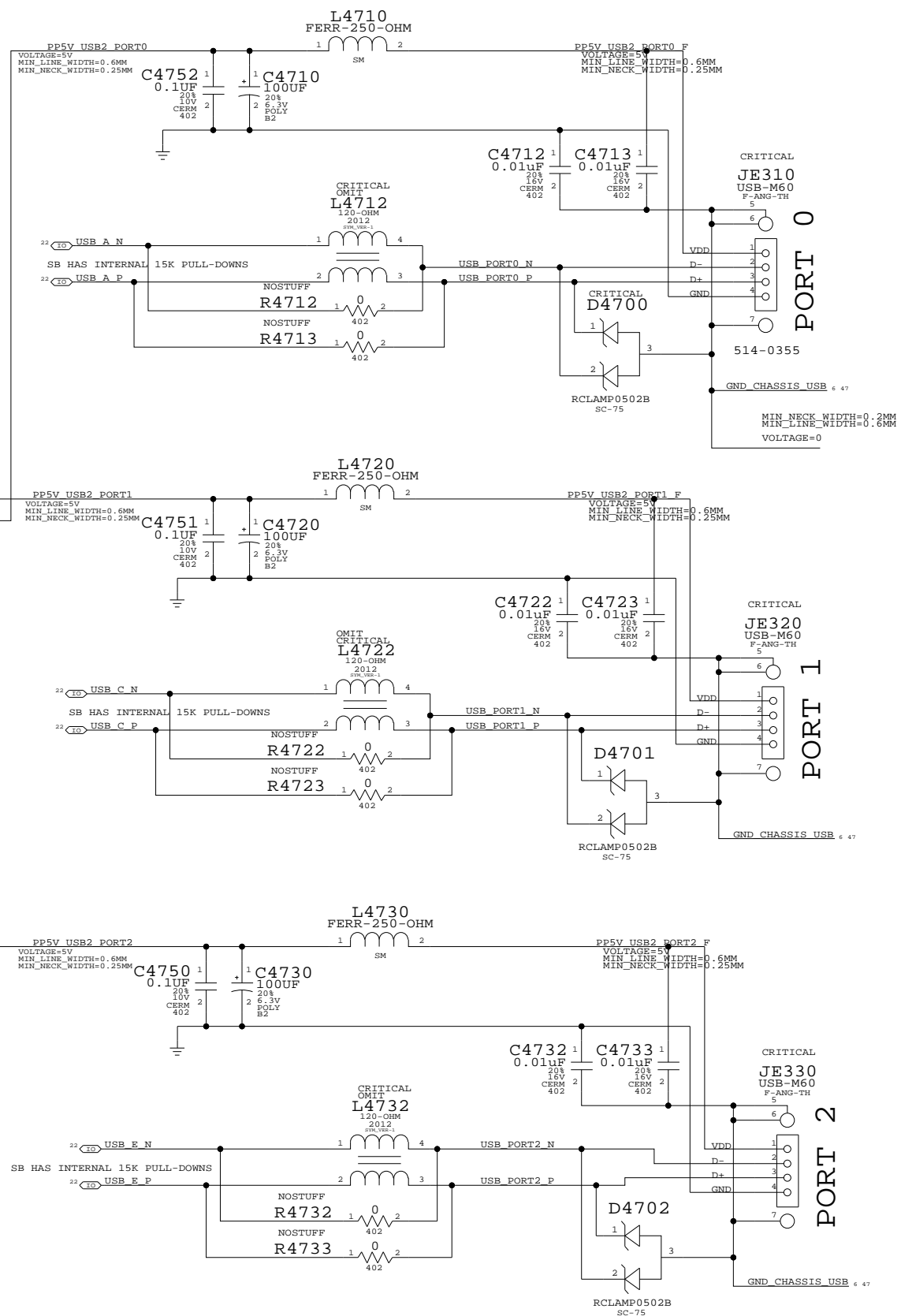
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 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 45 OF	111



External USB Ports



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0355	3	USB RECEPTACLE,4P,RIBLESS	JE310,JE320,JE330	CRITICAL	20_INCH_LCD

NOTE: STANDOFFS FOR J4700
SDF4700
STDOFF-40D4.5H-1.35-TH
SDF4701
STDOFF-40D4.5H-1.35-TH

USB Device Interfaces

NOTICE OF PROPRIETARY PROPERTY

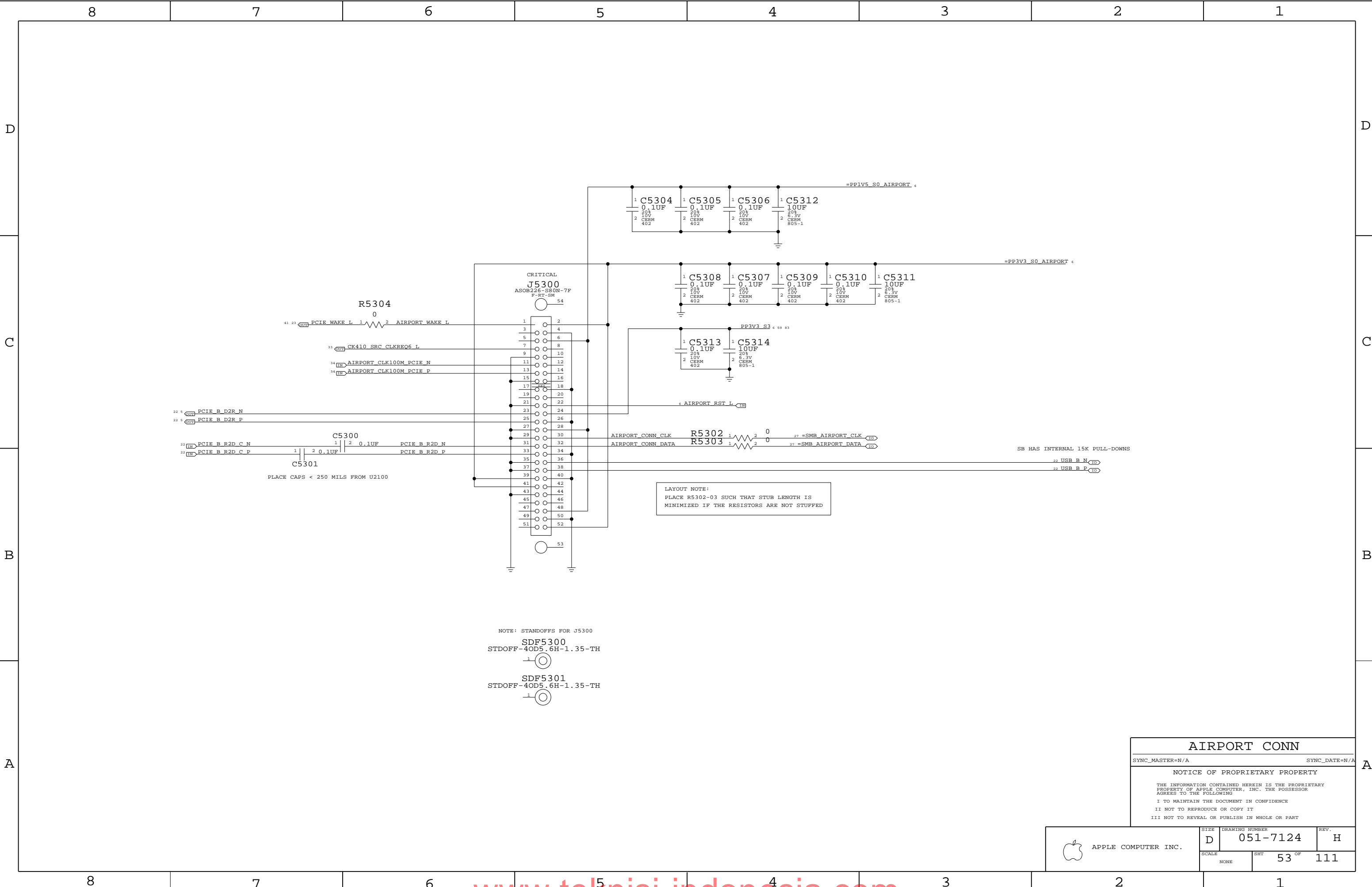
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APPLE COMPUTER INC.	SIZE	D	DRAWING NUMBER	051-7124	REV.	H
	SCALE	NONE	SHT	47	OF	111



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SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

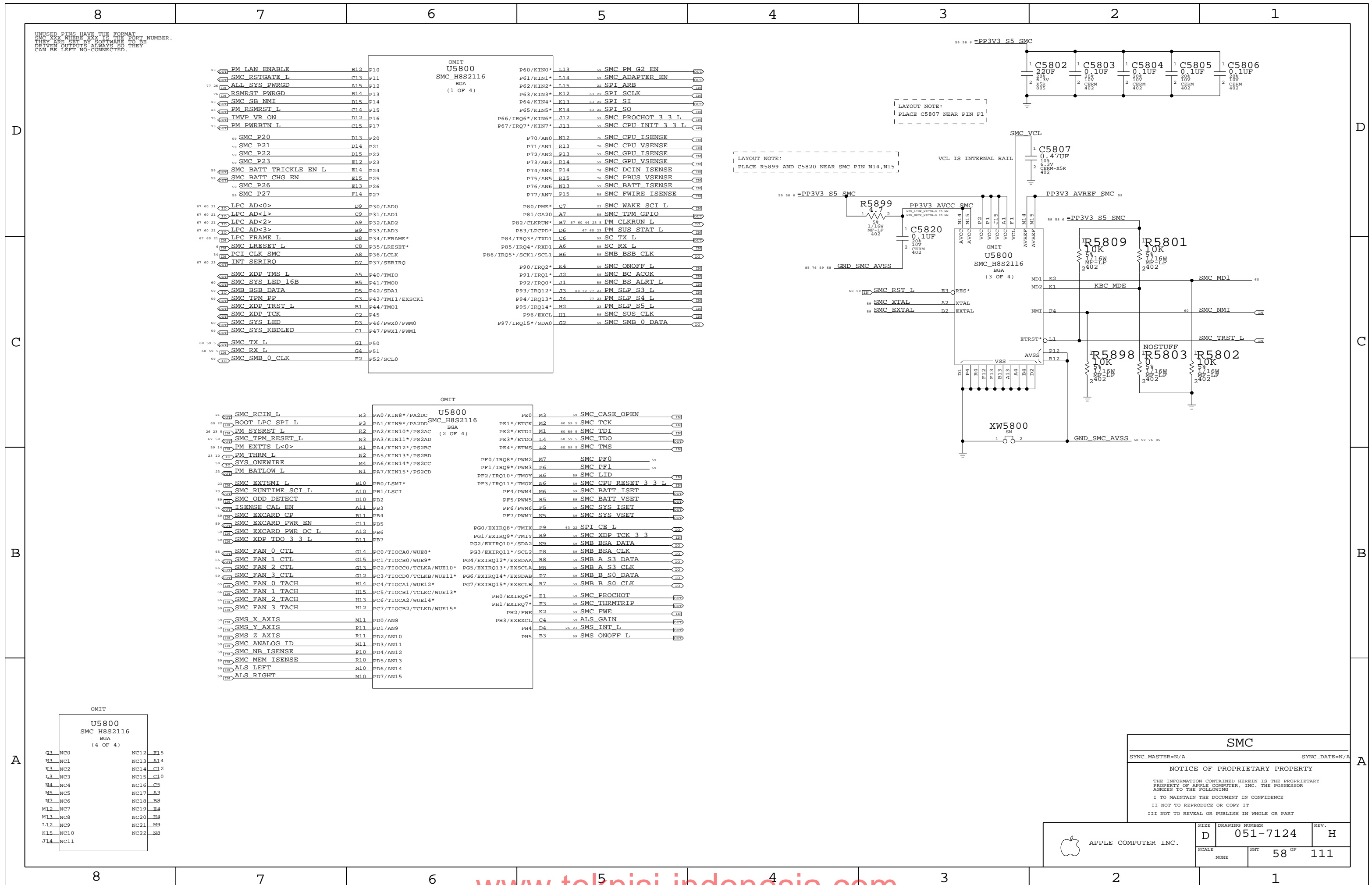
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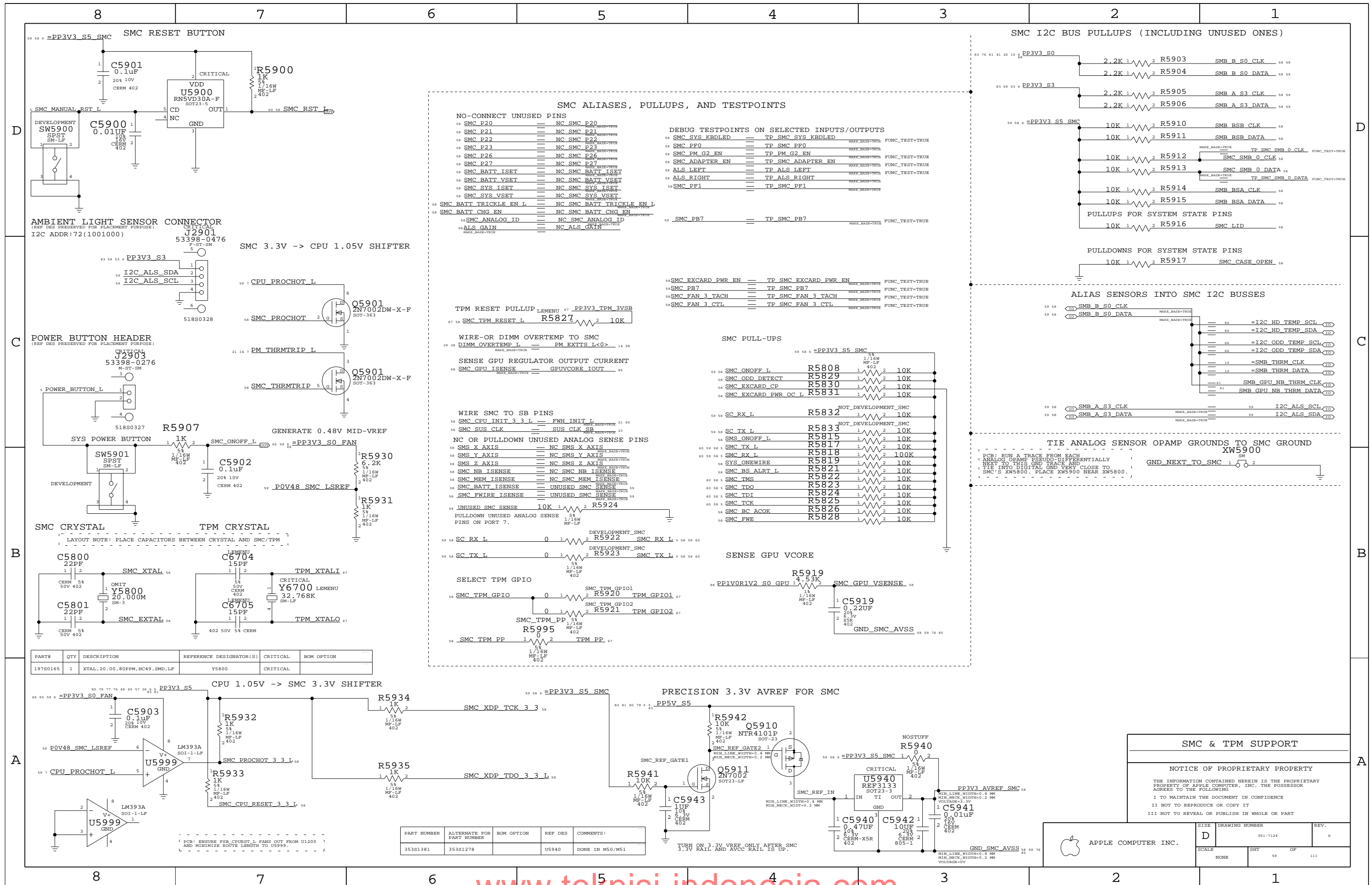
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	OF
NONE		53	111





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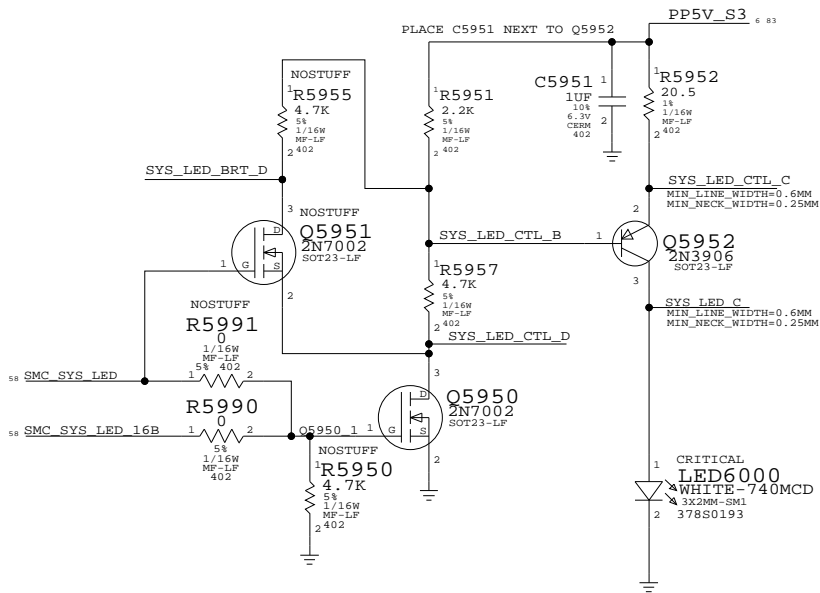
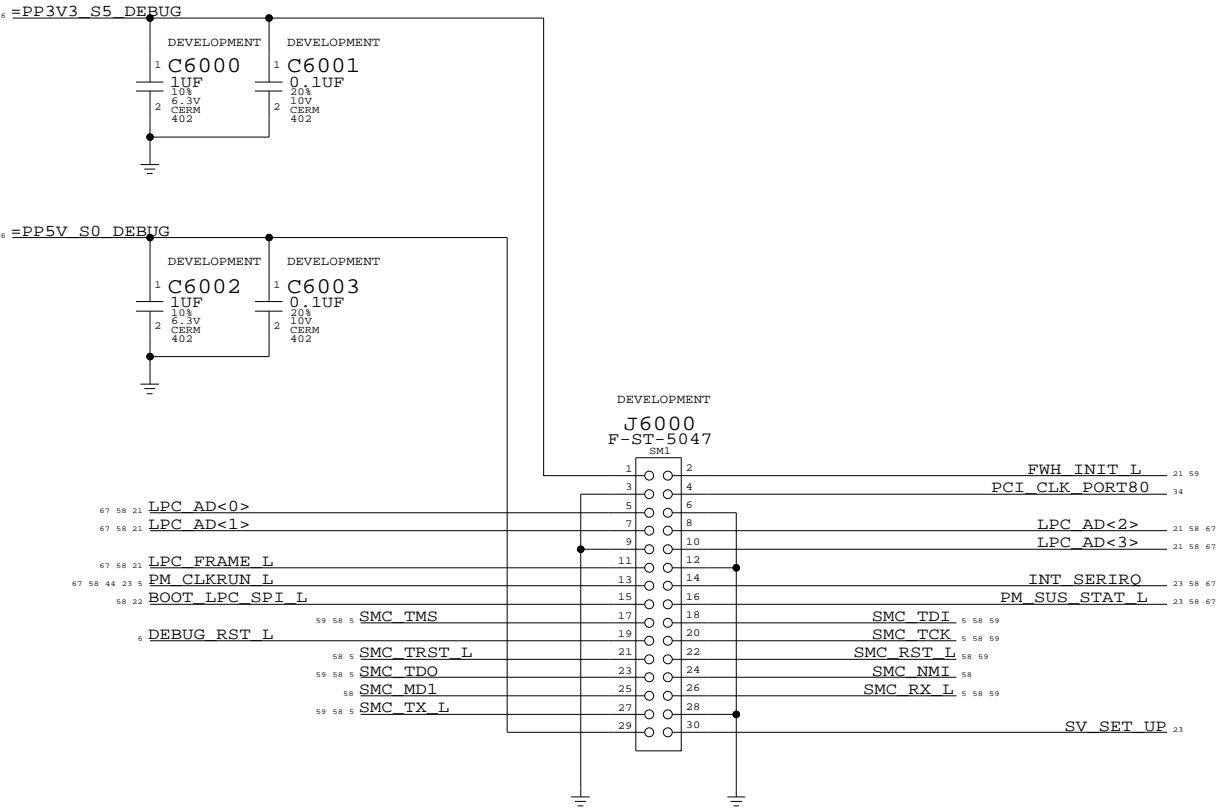
D

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8 7 6 5 4 3 2 1



LPC+ CONN

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

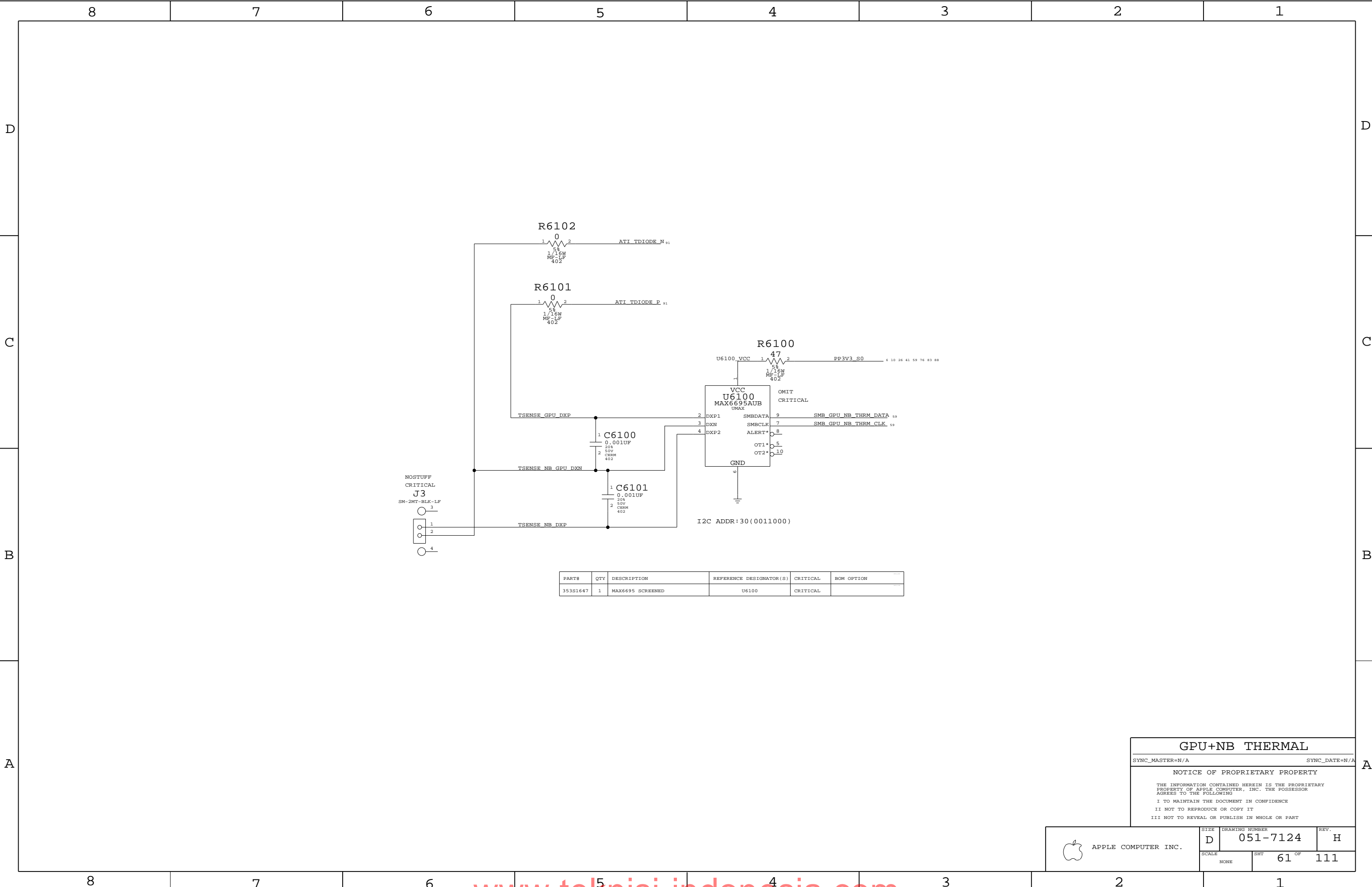
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	60 OF 111
NONE			



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
353S1647	1	MAX6695 SCREENED	U6100	CRITICAL	

GPU+NB THERMAL

SYNC_MASTER=N/A

SYNC_DATE=N/A

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APPLE COMPUTER INC.

SIZE

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DRAWING NUMBER

051-7124

REV.

H

SCALE

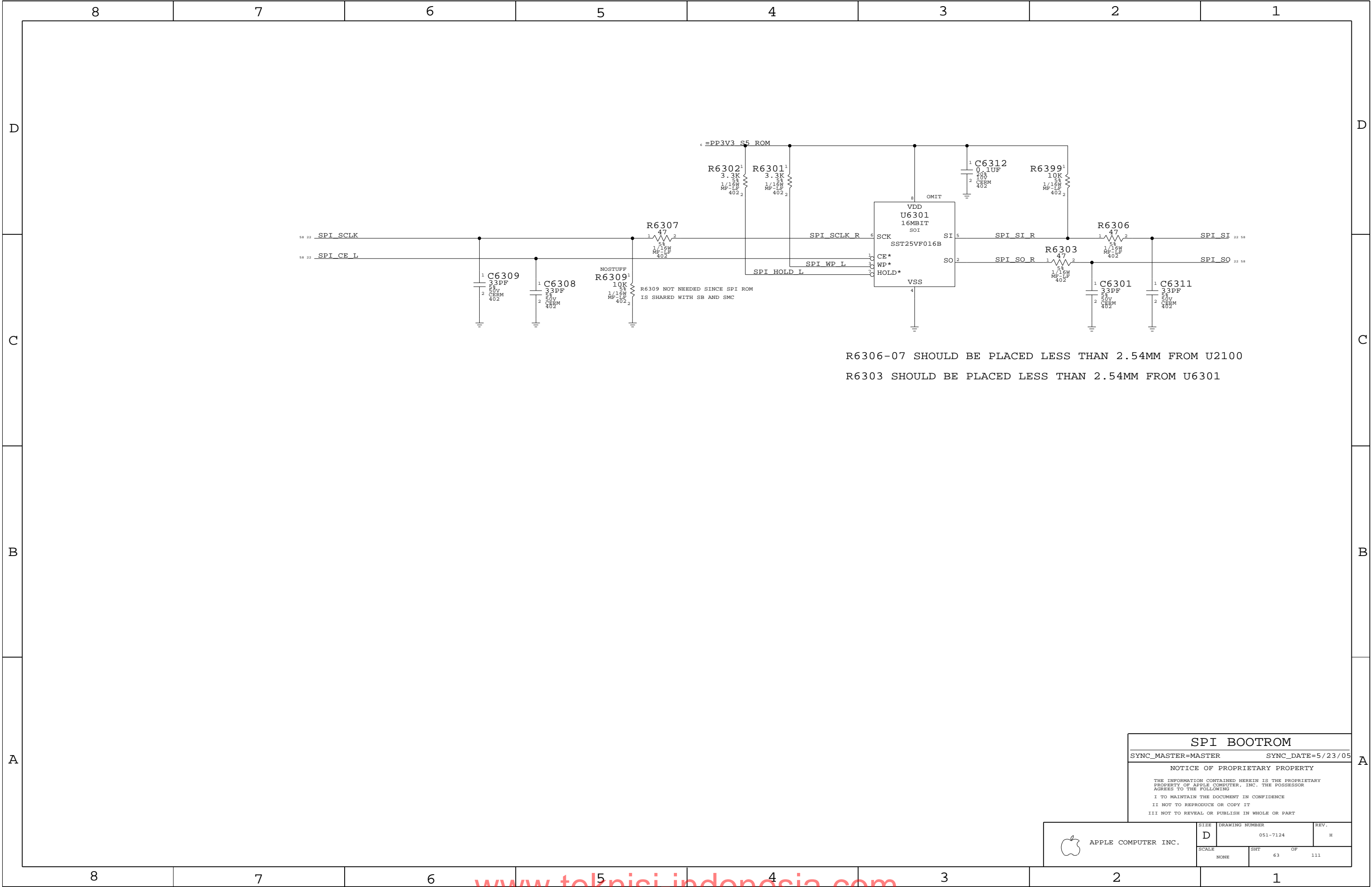
NONE

SHT

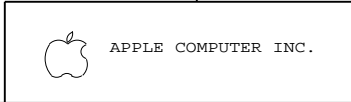
61

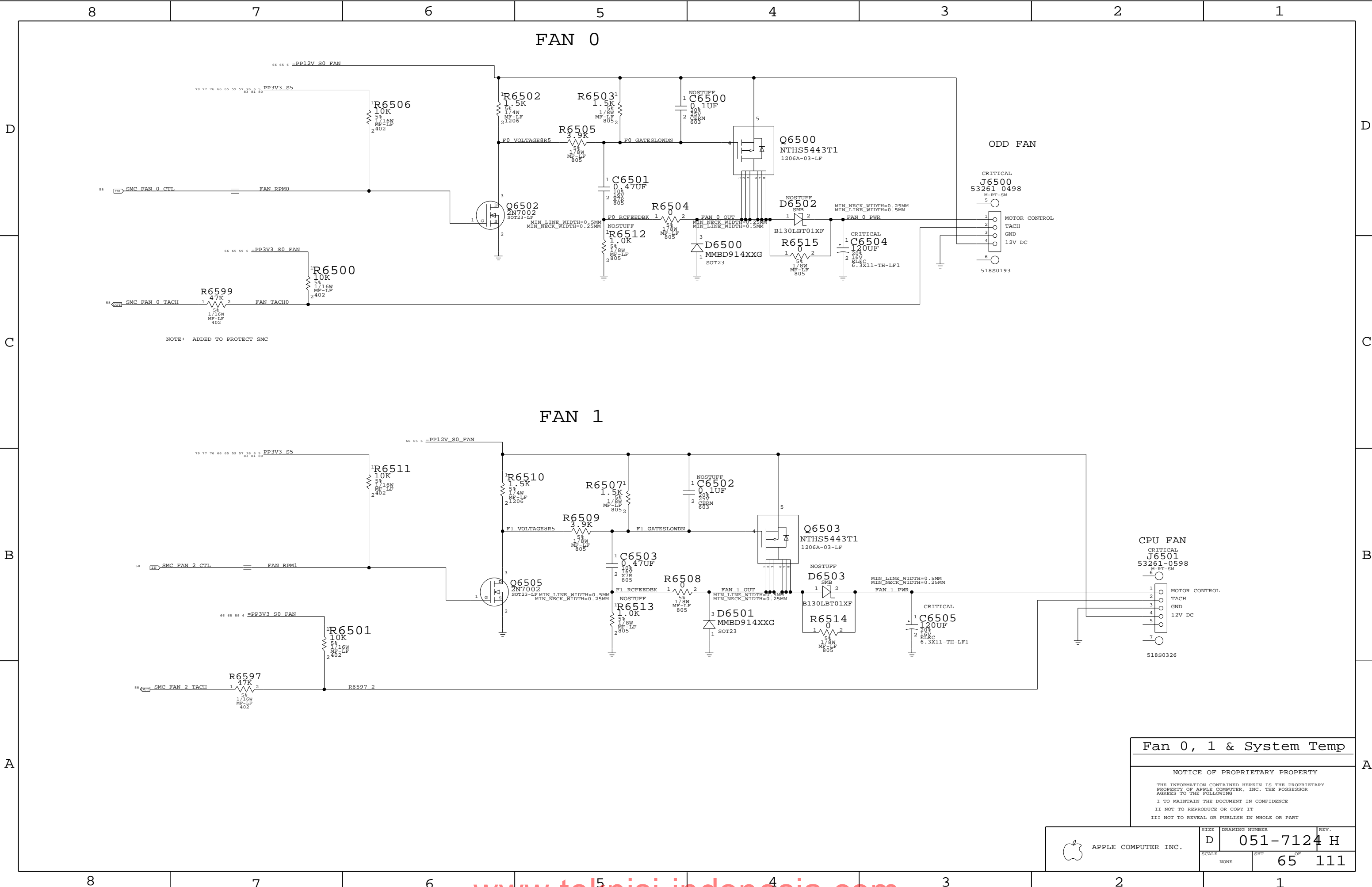
OF

111



SPI BOOTROM		
SYNC_MASTER=MASTER		SYNC_DATE=5/23/05
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SCALE	SHT	OF
NONE	63	111





Fan 0, 1 & System Temp

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	D	051-7124	H
SCALE	SHT		REV.
	NONE		65 OF 111

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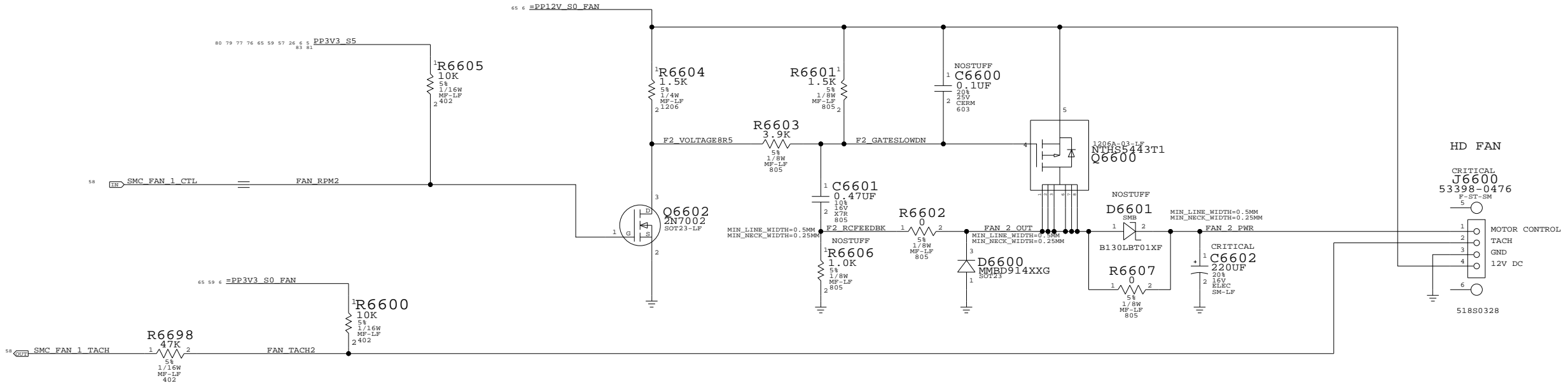
B

A

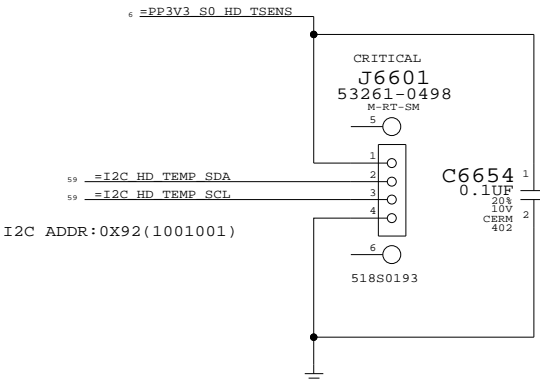
8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

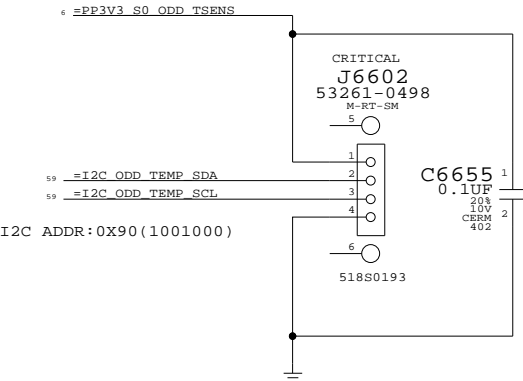
FAN 2



HD TEMP SENSOR



ODD TEMP SENSOR



Fan 2 & HD Temp

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	SCALE NONE	SHT 66 OF 111	

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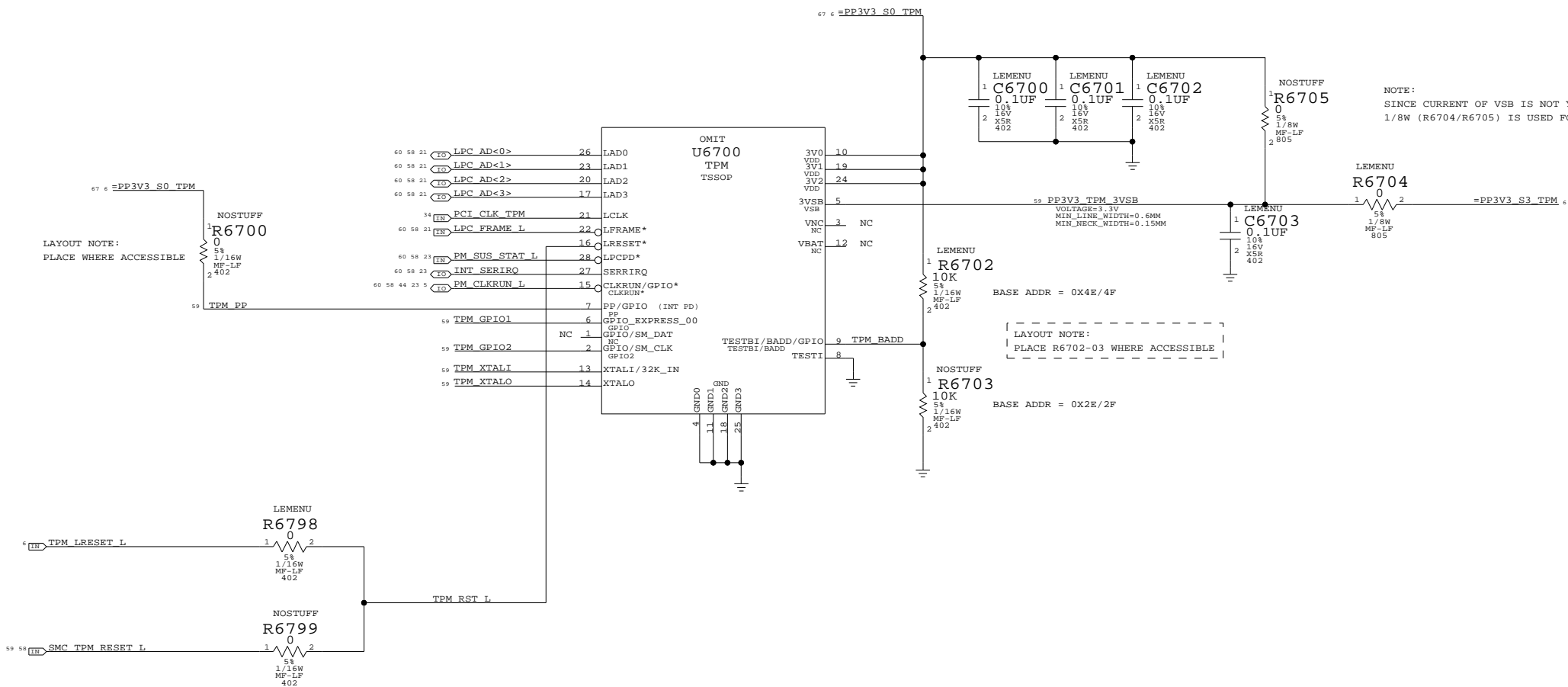
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NOTE:
SINCE CURRENT OF VSB IS NOT YET ON SPEC,
1/8W (R6704/R6705) IS USED FOR NOW

TPM	
SYNC_MASTER=N/A	SYNC_DATE=N/A
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 67 OF 111	

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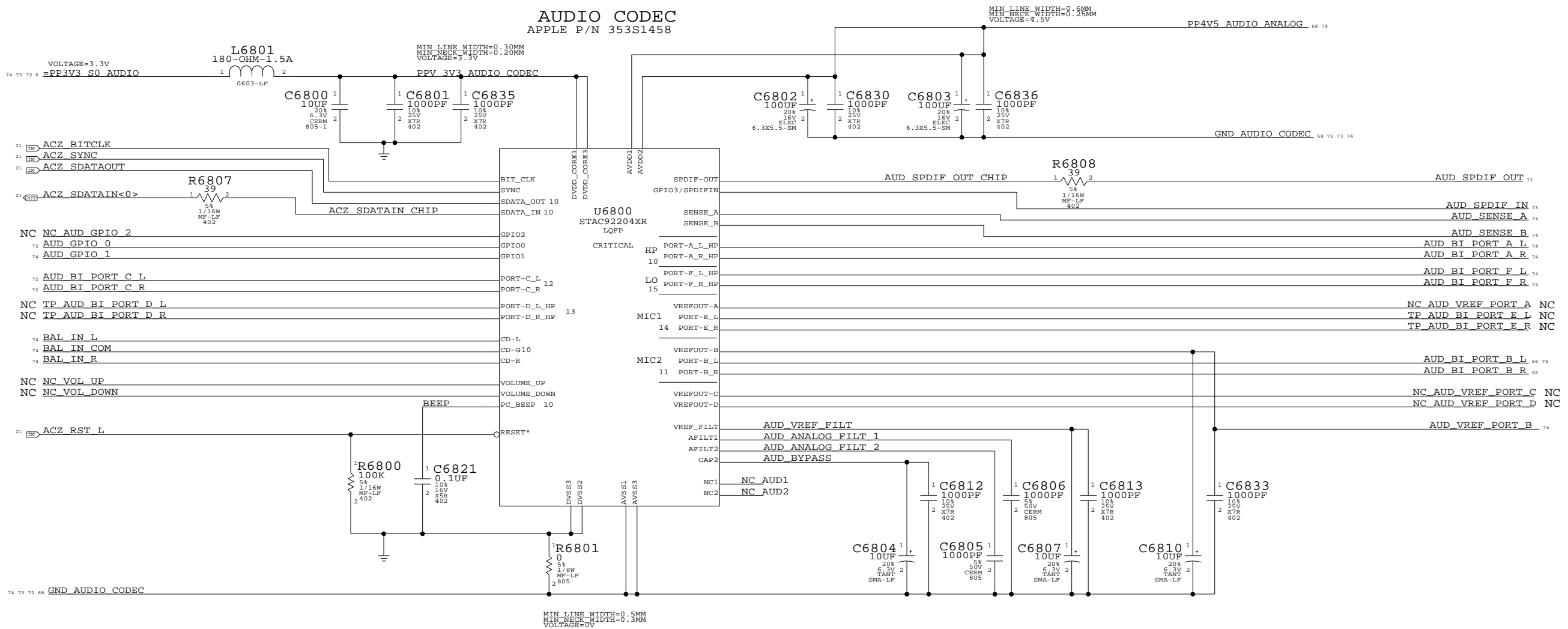
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8 7 6 5 4 3 2 1

AUDIO CODEC
APPLE P/N 353S1458



PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0091	126S0092		C6802, C6803	FACTORY SHORTAGE
126S0091	126S0092		C7403, C7404	FACTORY SHORTAGE
353S1345	353S1458		U6800	FACTORY SHORTAGE

MIN LINE WIDTH=0.6MM
MIN NECK WIDTH=0.25MM
VOLTAGE=5V
=PP4V5_S0_AUDIO_ANALOG

L6802
180-OHM-1.5A
0603-LF

AUDIO: CODEC

SYNC_MASTER=AUDIO SYNC_DATE=02/23/2006

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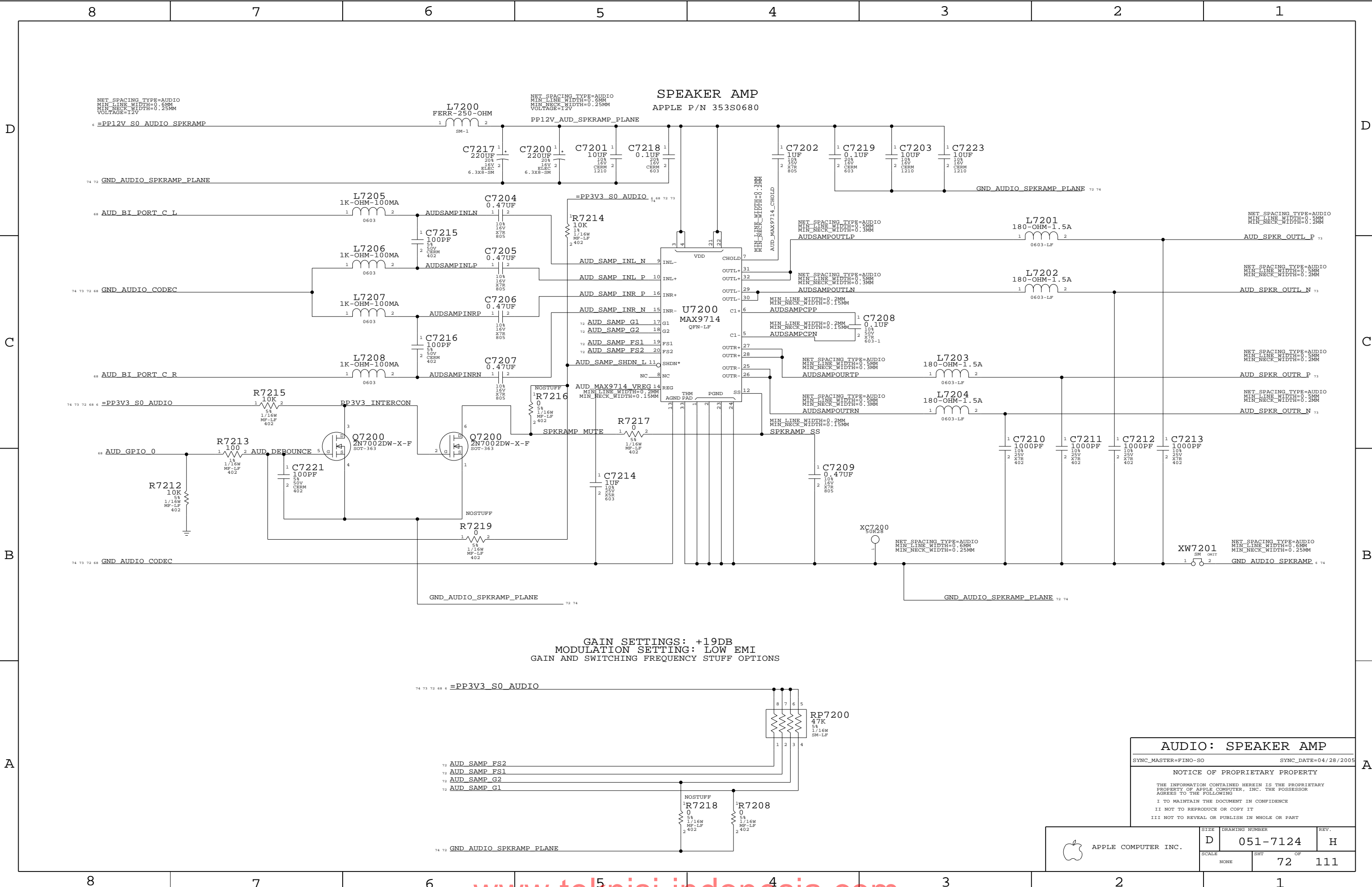
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-7124 REV. H

SCALE NONE SHT 68 OF 111

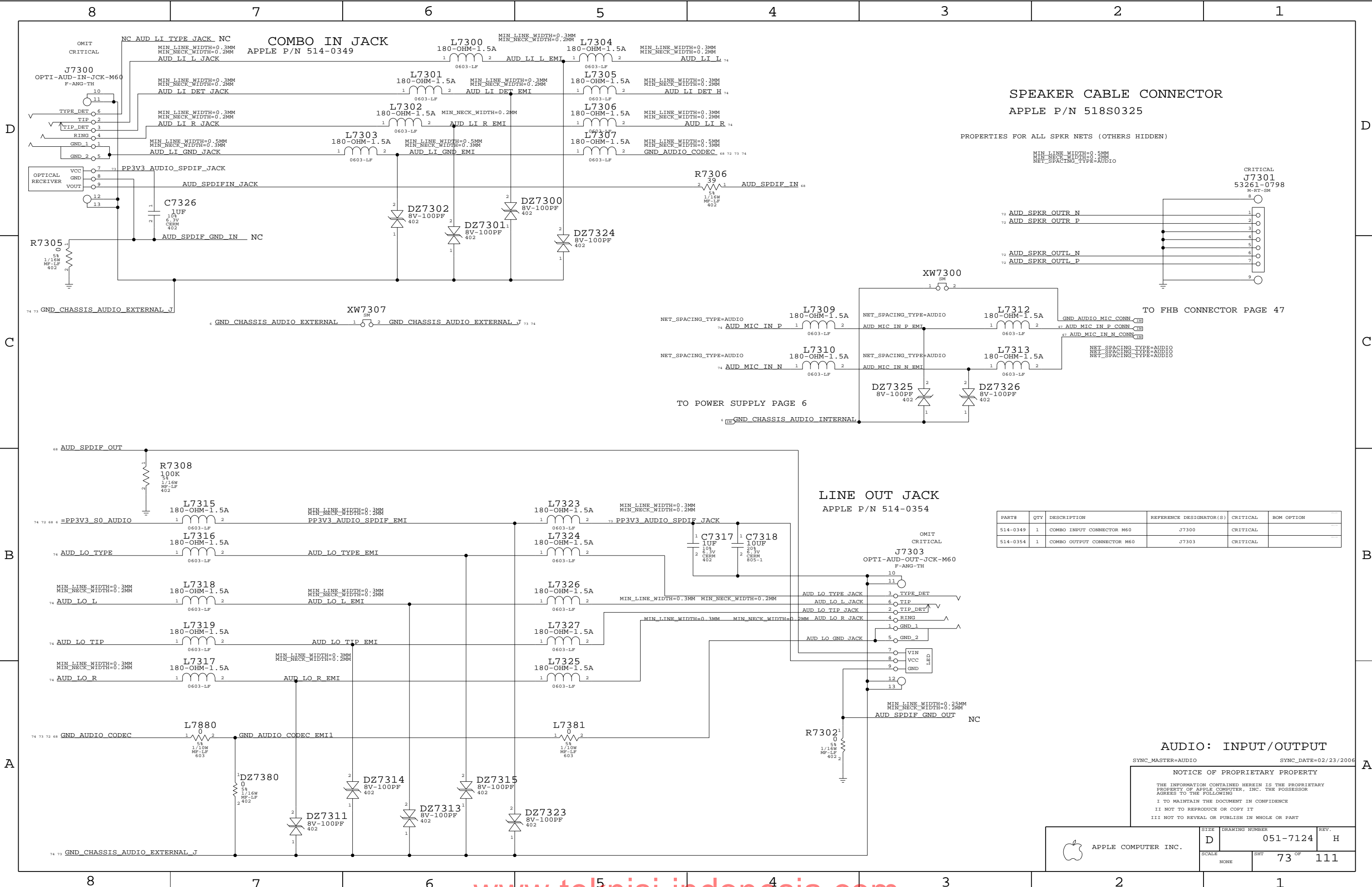
8 7 6 5 4 3 2 1



GAIN SETTINGS: +19DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

AUDIO: SPEAKER AMP
SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005
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	D	051-7124	H
SCALE	SHT		OF
	NONE		72 111



SPEAKER CABLE CONNECTOR
APPLE P/N 518S0325

PROPERTIES FOR ALL SPKR NETS (OTHERS HIDDEN)

MIN LINE WIDTH=0.5MM
MIN NECK WIDTH=0.2MM
NET SPACING TYPE=AUDIO

CRITICAL
J7301
53261-0798
M-ET-SM

AUD_SPKR_OUTR_N
AUD_SPKR_OUTR_P
AUD_SPKR_OUTL_N
AUD_SPKR_OUTL_P

TO FHB CONNECTOR PAGE 47

TO POWER SUPPLY PAGE 6

LINE OUT JACK
APPLE P/N 514-0354

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0349	1	COMBO INPUT CONNECTOR M60	J7300	CRITICAL	
514-0354	1	COMBO OUTPUT CONNECTOR M60	J7303	CRITICAL	

AUDIO: INPUT/OUTPUT

SYNC_MASTER=AUDIO SYNC_DATE=02/23/2006

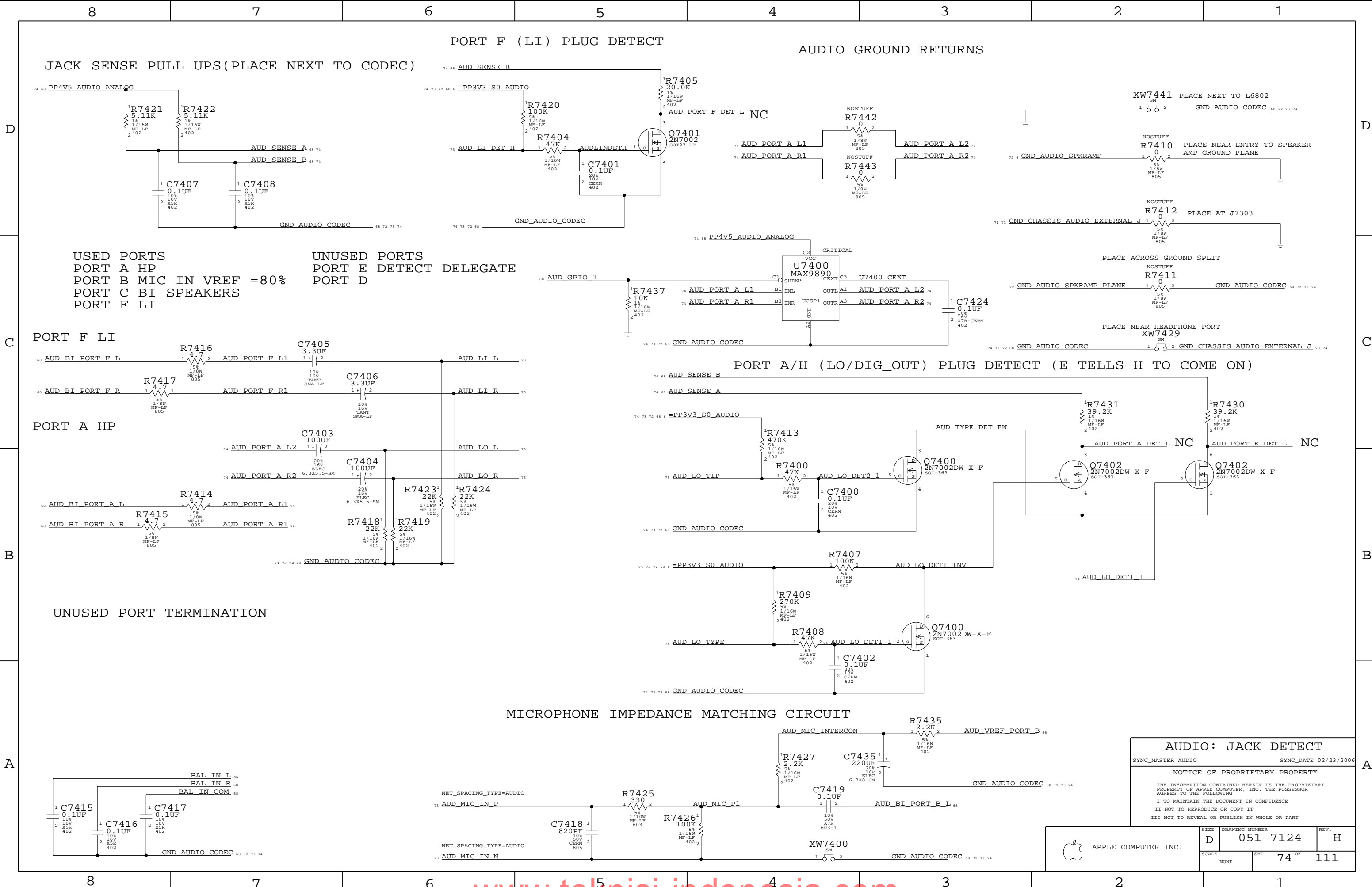
NOTICE OF PROPRIETARY PROPERTY

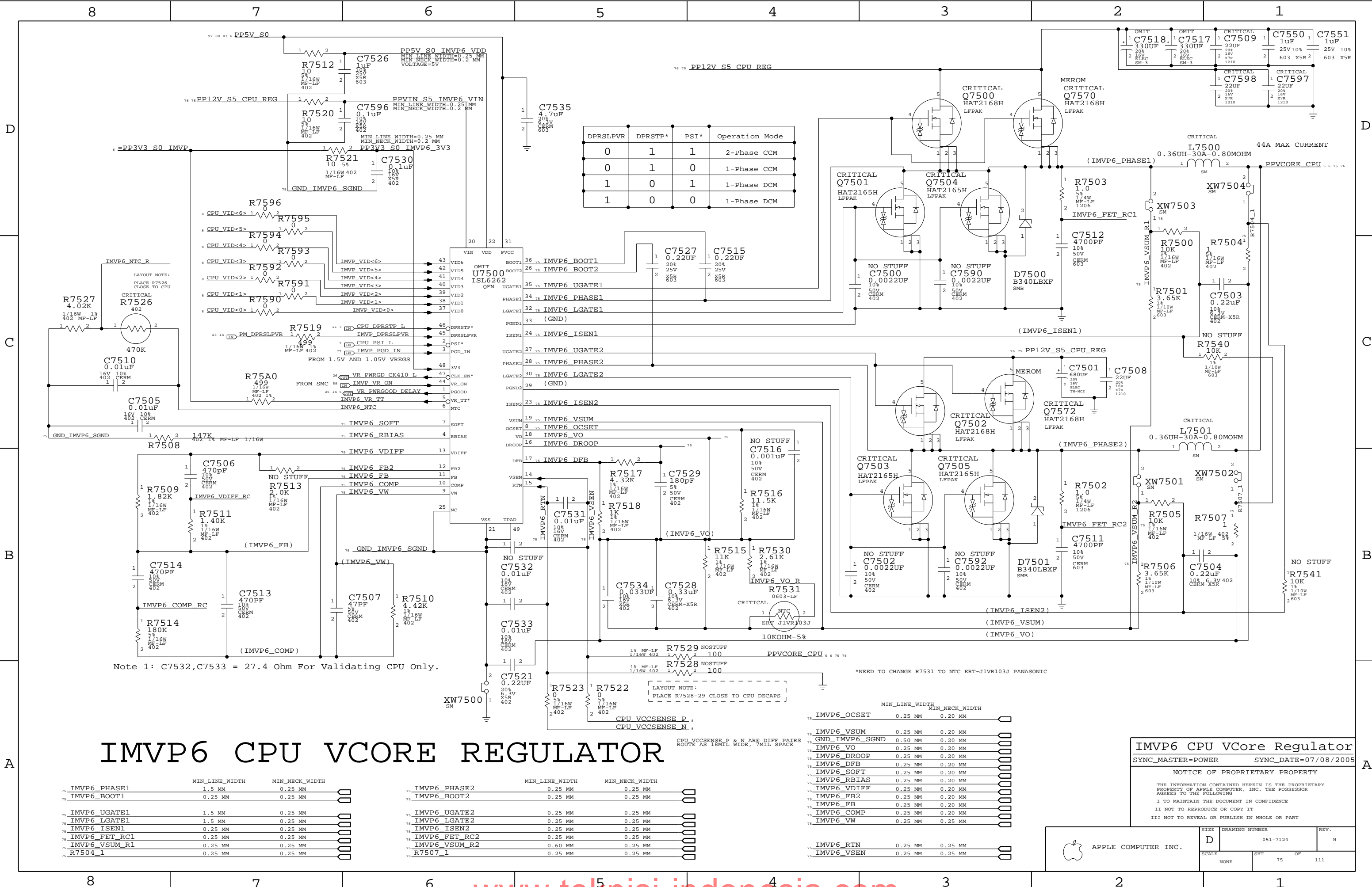
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	REV.
NONE	73 OF 111	





IMVP6 CPU VCore REGULATOR

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_PHASE1	1.5 MM	0.25 MM
IMVP6_BOOT1	0.25 MM	0.25 MM
IMVP6_UGATE1	1.5 MM	0.25 MM
IMVP6_LGATE1	1.5 MM	0.25 MM
IMVP6_ISEN1	0.25 MM	0.25 MM
IMVP6_FET_RC1	0.25 MM	0.25 MM
IMVP6_VSUM_R1	0.25 MM	0.25 MM
R7504_1	0.25 MM	0.25 MM

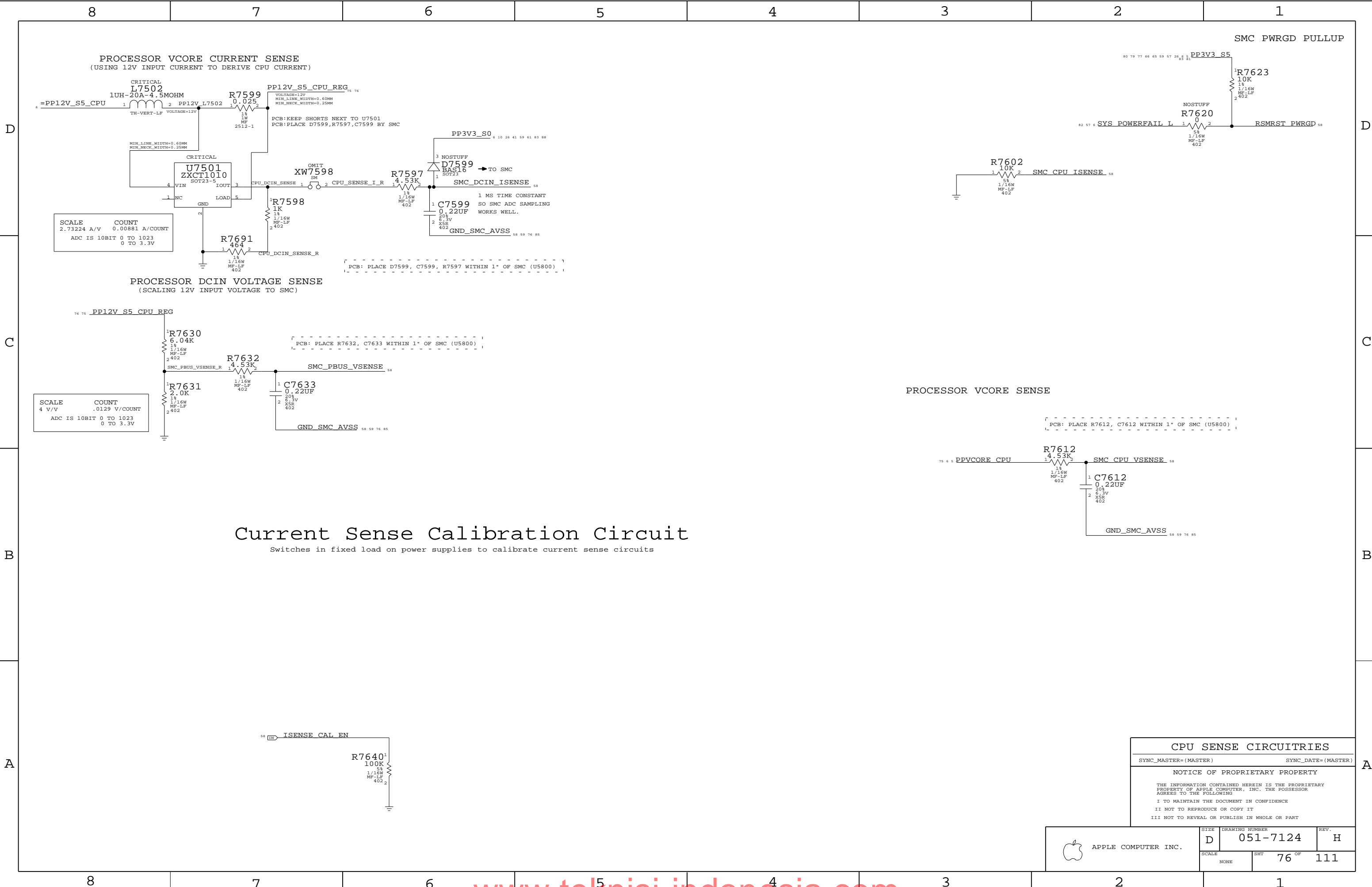
	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_PHASE2	0.25 MM	0.25 MM
IMVP6_BOOT2	0.25 MM	0.25 MM
IMVP6_UGATE2	0.25 MM	0.25 MM
IMVP6_LGATE2	0.25 MM	0.25 MM
IMVP6_ISEN2	0.25 MM	0.25 MM
IMVP6_FET_RC2	0.25 MM	0.25 MM
IMVP6_VSUM_R2	0.60 MM	0.25 MM
R7507_1	0.25 MM	0.25 MM

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_OCSET	0.25 MM	0.20 MM
IMVP6_VSUM	0.25 MM	0.20 MM
GND_IMVP6_SGND	0.50 MM	0.20 MM
IMVP6_VO	0.25 MM	0.20 MM
IMVP6_DROOP	0.25 MM	0.20 MM
IMVP6_DFB	0.25 MM	0.20 MM
IMVP6_SOFT	0.25 MM	0.20 MM
IMVP6_RBIAS	0.25 MM	0.20 MM
IMVP6_VDIFF	0.25 MM	0.20 MM
IMVP6_FB2	0.25 MM	0.20 MM
IMVP6_FB	0.25 MM	0.20 MM
IMVP6_COMP	0.25 MM	0.20 MM
IMVP6_VW	0.25 MM	0.25 MM
IMVP6_RTIN	0.25 MM	0.25 MM
IMVP6_VSEN	0.25 MM	0.25 MM

IMVP6 CPU VCore Regulator
SYNC_MASTER=POWER SYNC_DATE=07/08/2005

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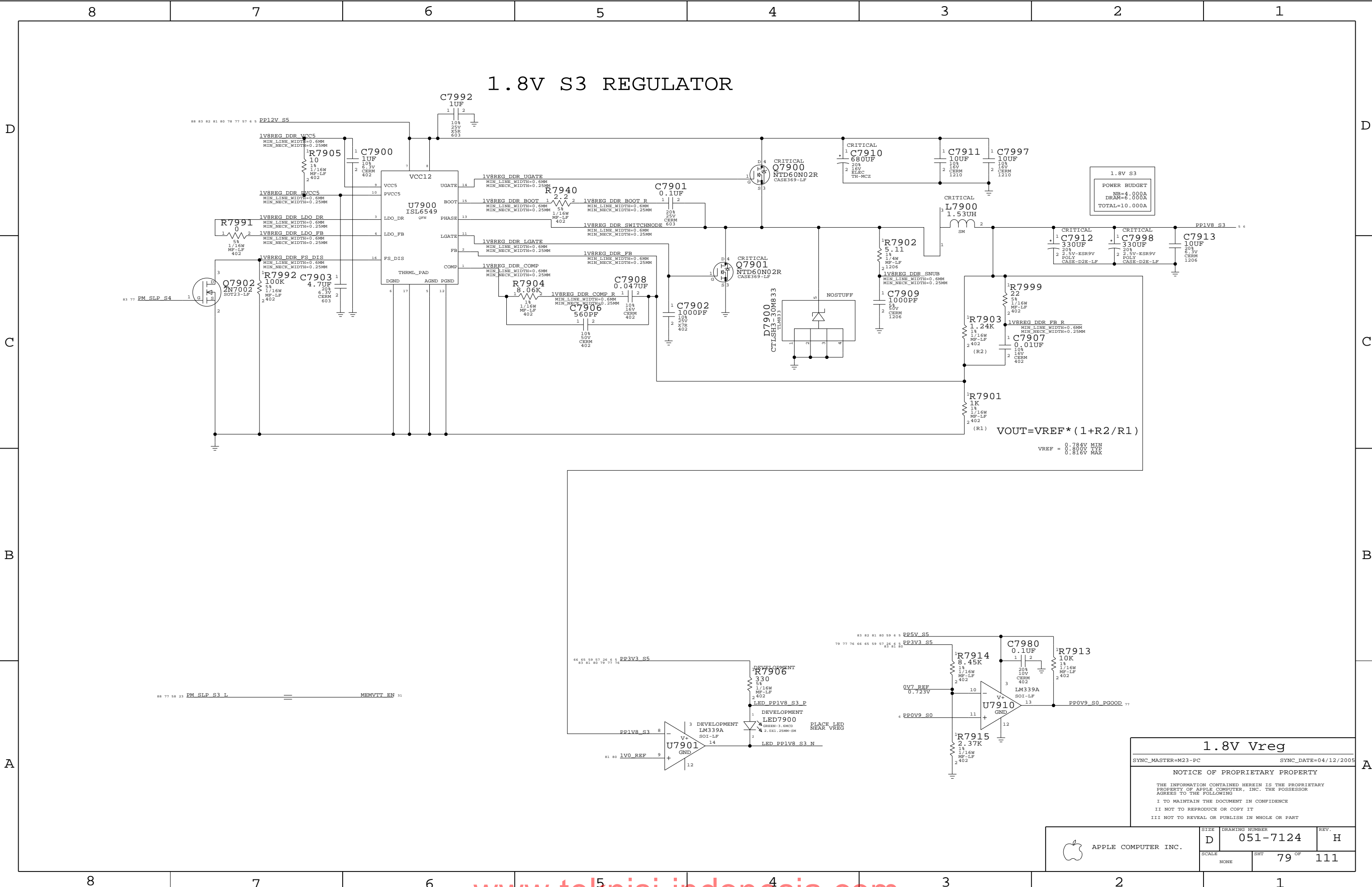
SIZE	D	DRAWING NUMBER	051-7124	REV.	H
SCALE	NONE	SHT	75	OF	111



Current Sense Calibration Circuit

Switches in fixed load on power supplies to calibrate current sense circuits

CPU SENSE CIRCUITRIES		
SYNC_MASTER= (MASTER)		SYNC_DATE= (MASTER)
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124
	SCALE NONE	REV. H
		SHT 76 OF 111



1.8V Vreg

SYNC_MASTER=M23-PC SYNC_DATE=04/12/2005

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT		REV.
	NONE		79 OF 111

D

C

B

A

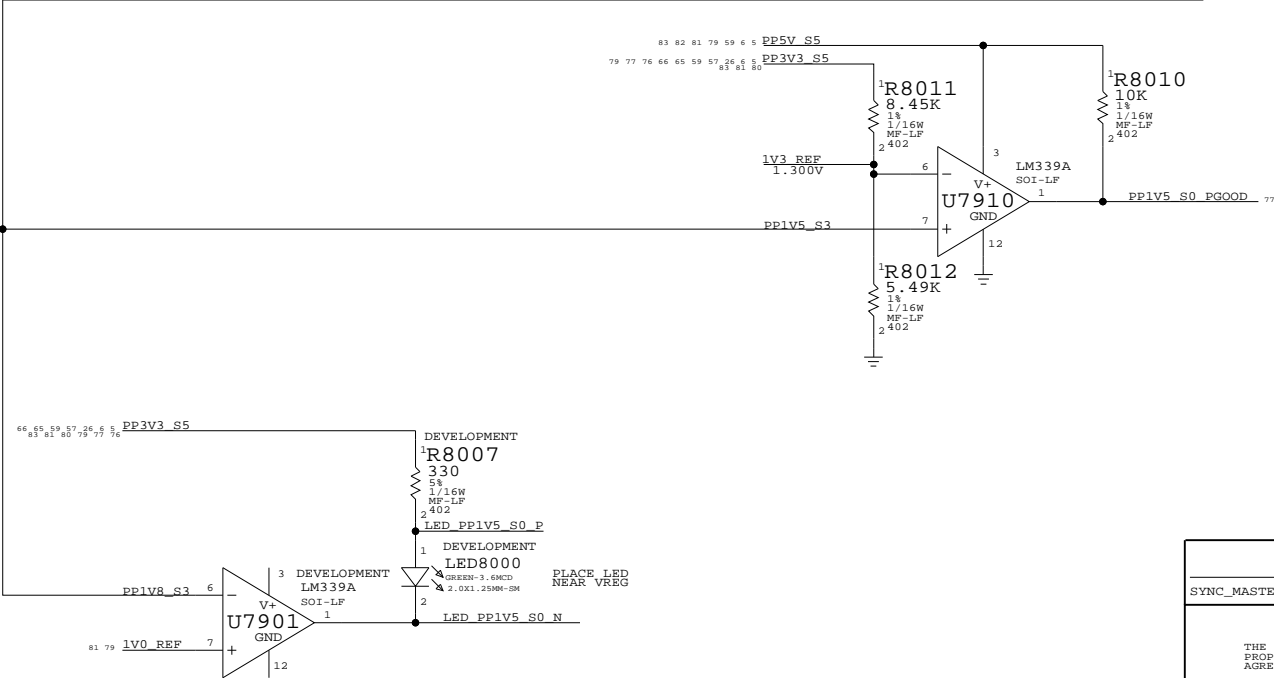
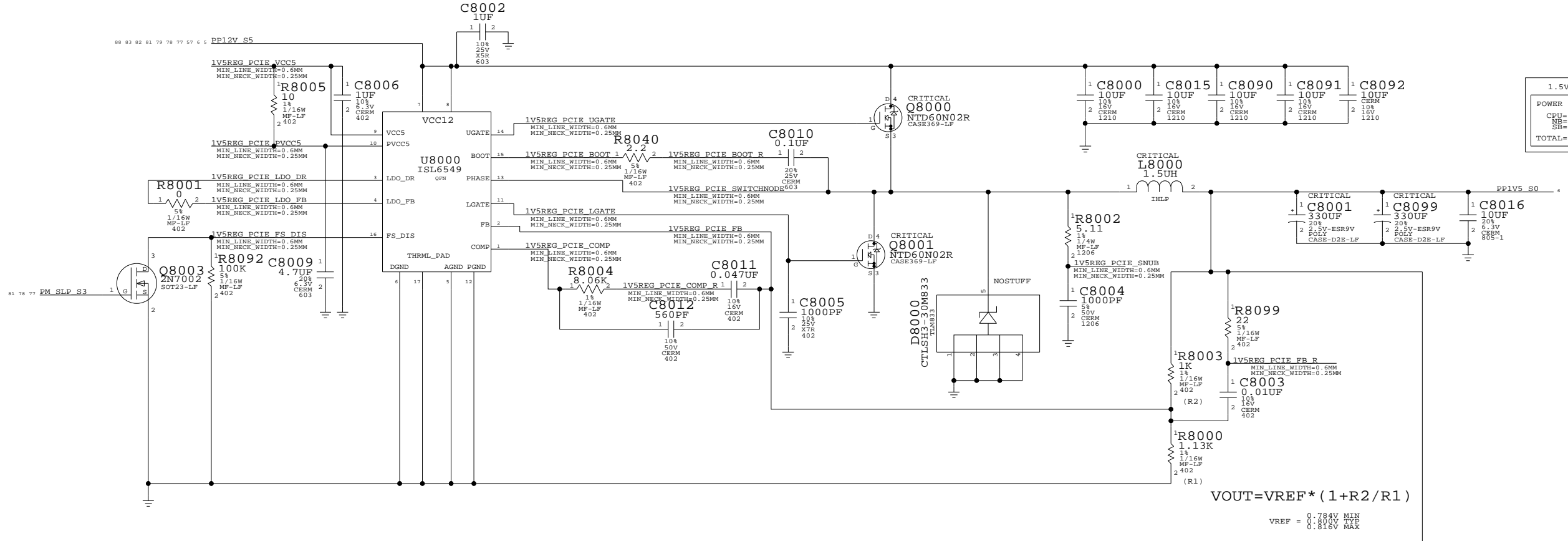
D

C

B

A

1.5V S0 REGULATOR



1.5V Vreg

SYNC_MASTER=FINO-PC SYNC_DATE=05/18/2005

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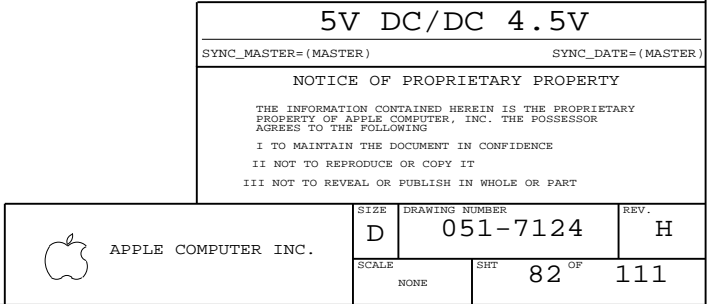
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

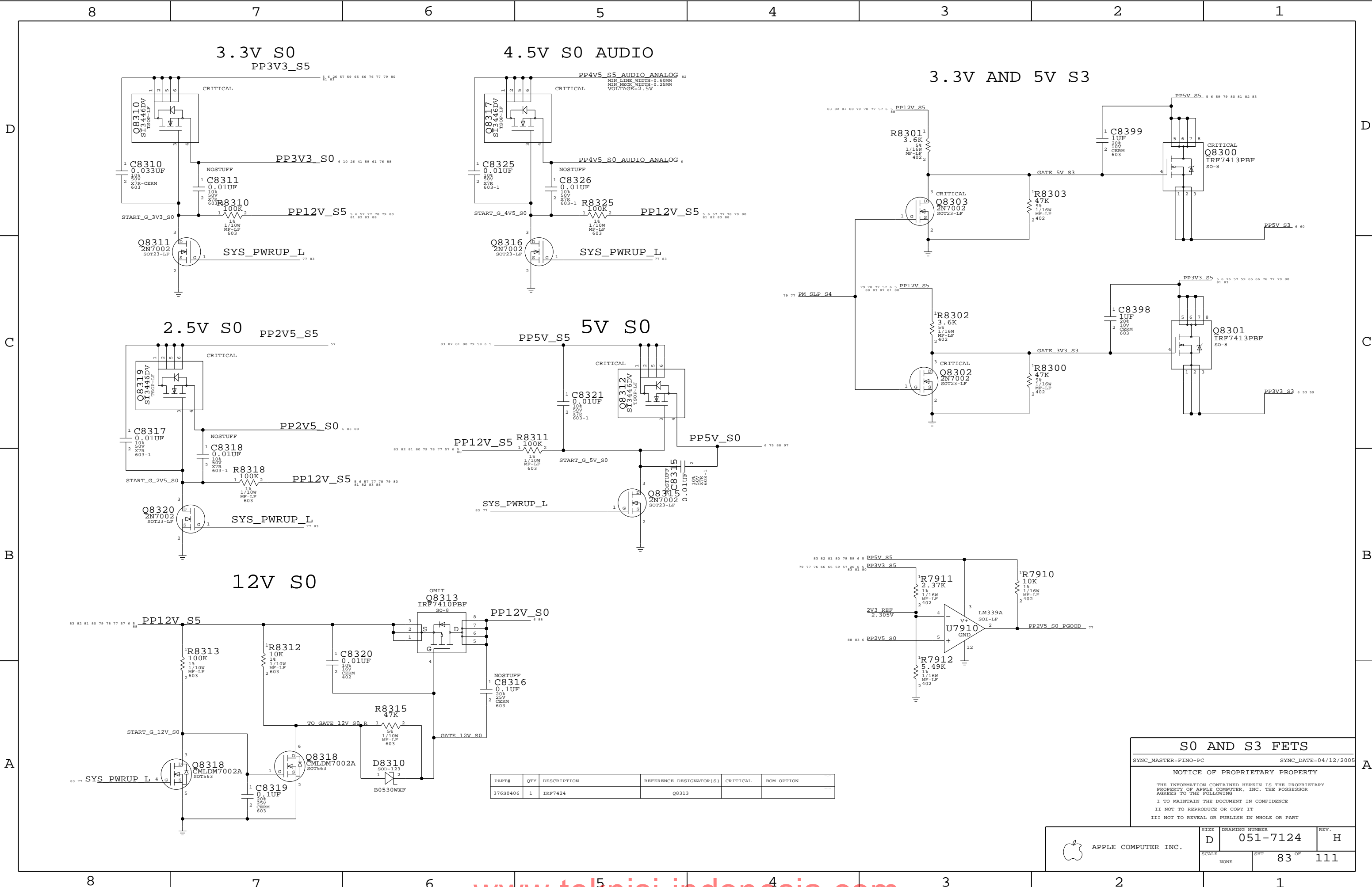
II NOT TO REPRODUCE OR COPY IT

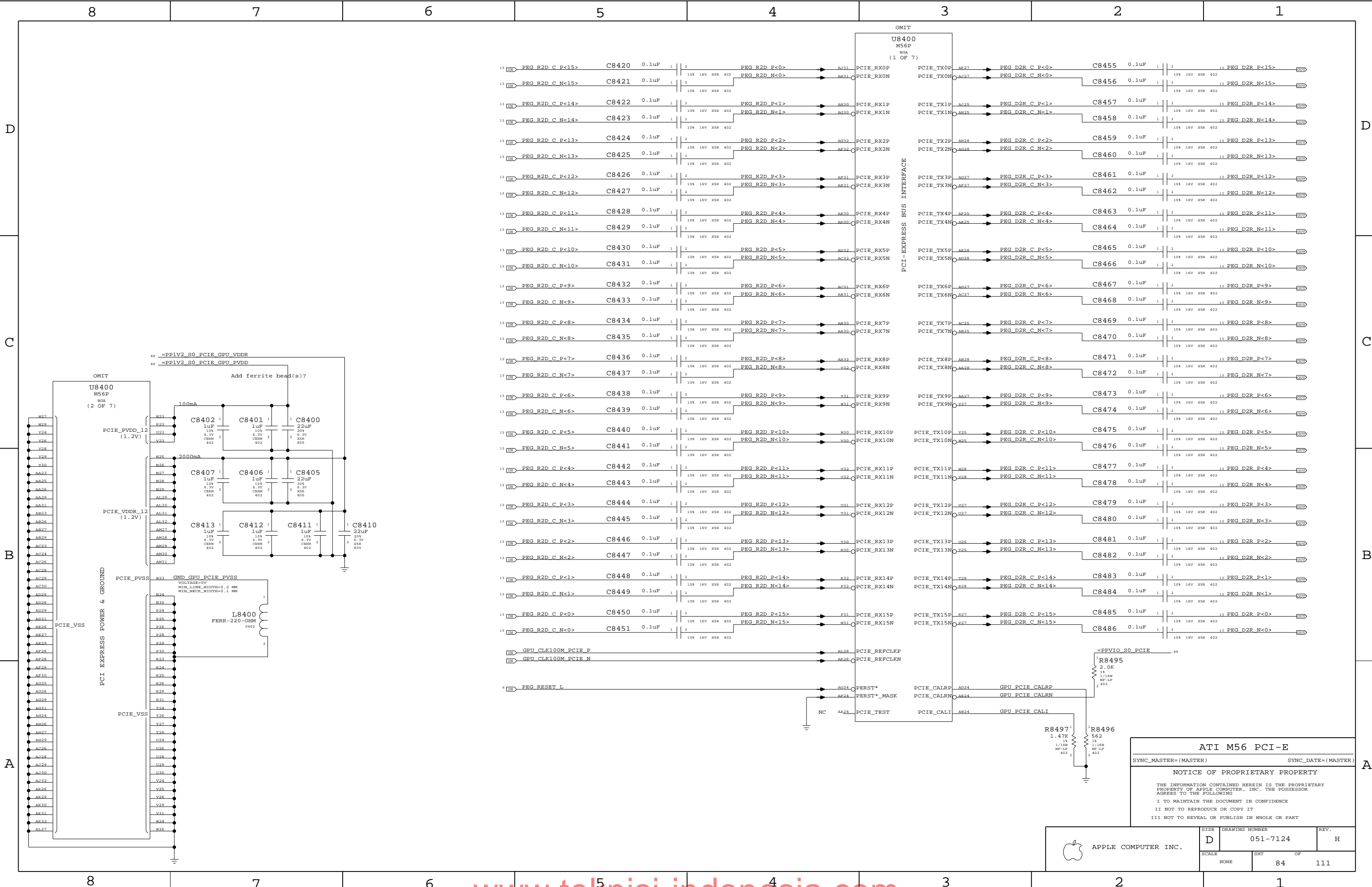
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	80 OF 111
NONE			

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ATI M56 PCI-E

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

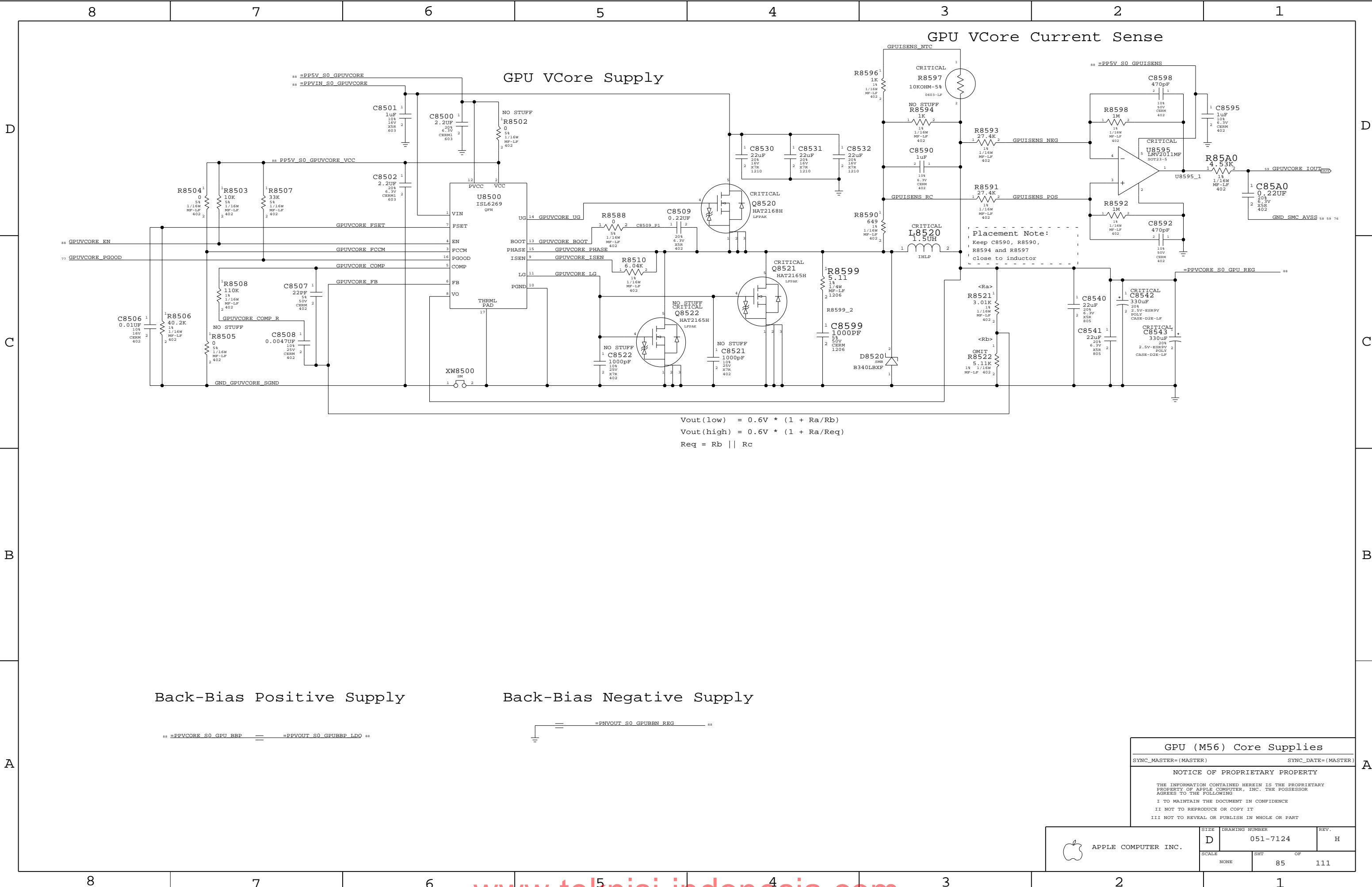
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT		OF
	NONE		111



Back-Bias Positive Supply

Back-Bias Negative Supply

GPU (M56) Core Supplies

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

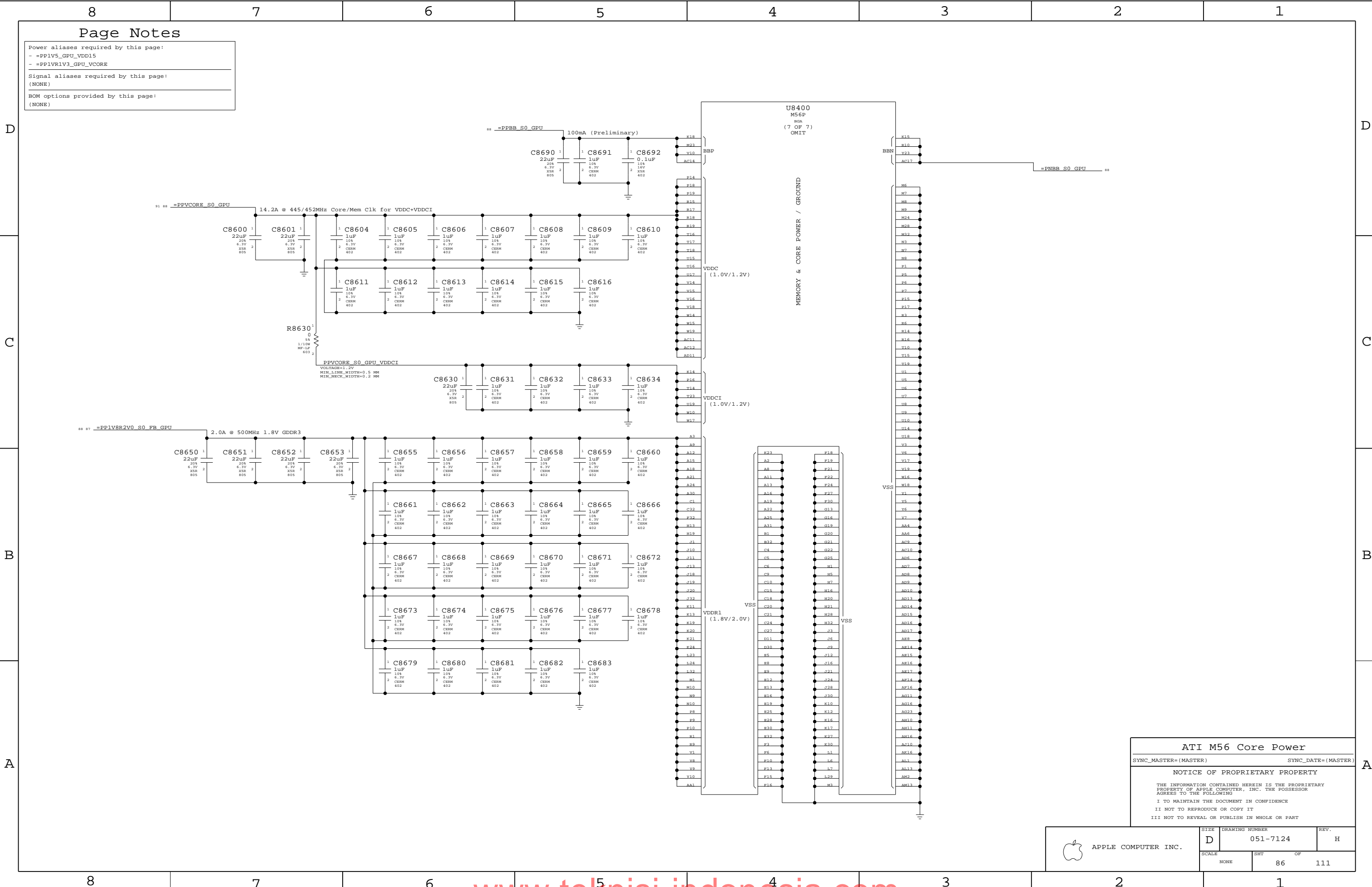
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT	OF	
NONE	85	111	



Page Notes

Power aliases required by this page:

- =PP1V5_GPU_VDD15
- =PP1VR1V3_GPU_VCORE

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)

ATI M56 Core Power

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT		OF
	NONE		86 111

Page Notes

Power aliases required by this page:

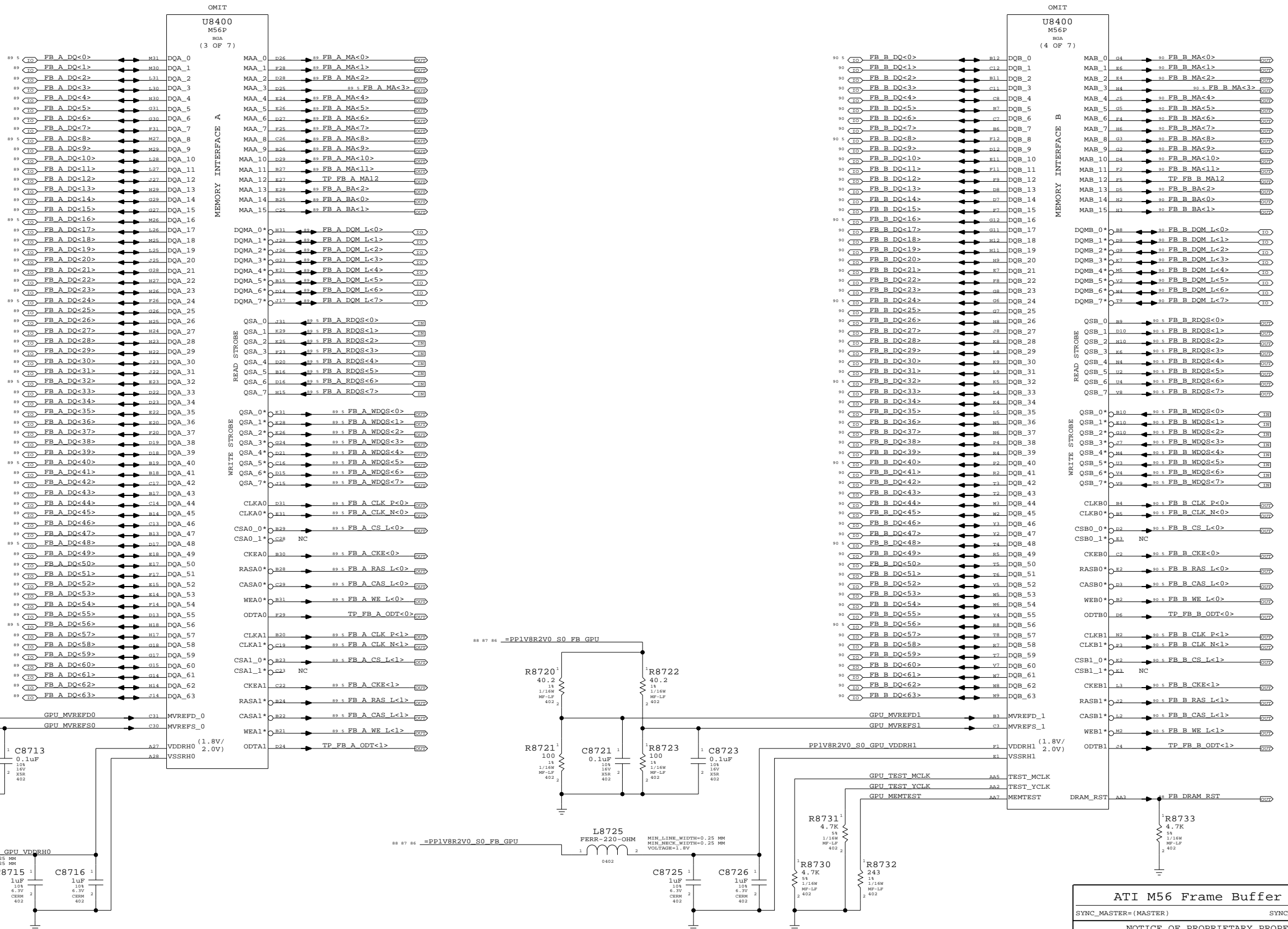
- =PP1V8R2V0_S0_FB_GPU

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)



ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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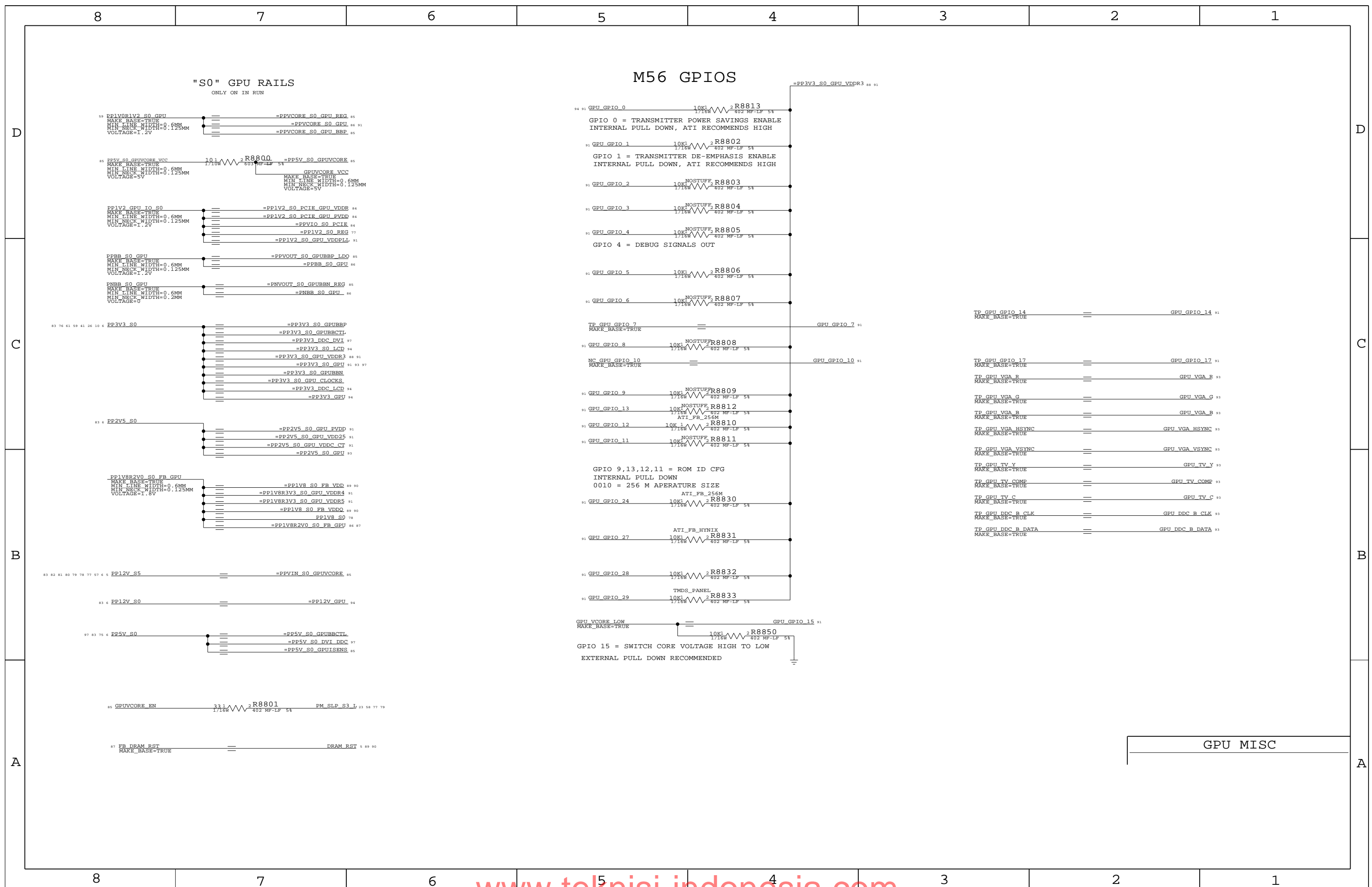
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	87	111



Power aliases required by this page:
 - =PP1V8_S0_FB_VDD
 - =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER)
 SYNC_DATE=(MASTER)

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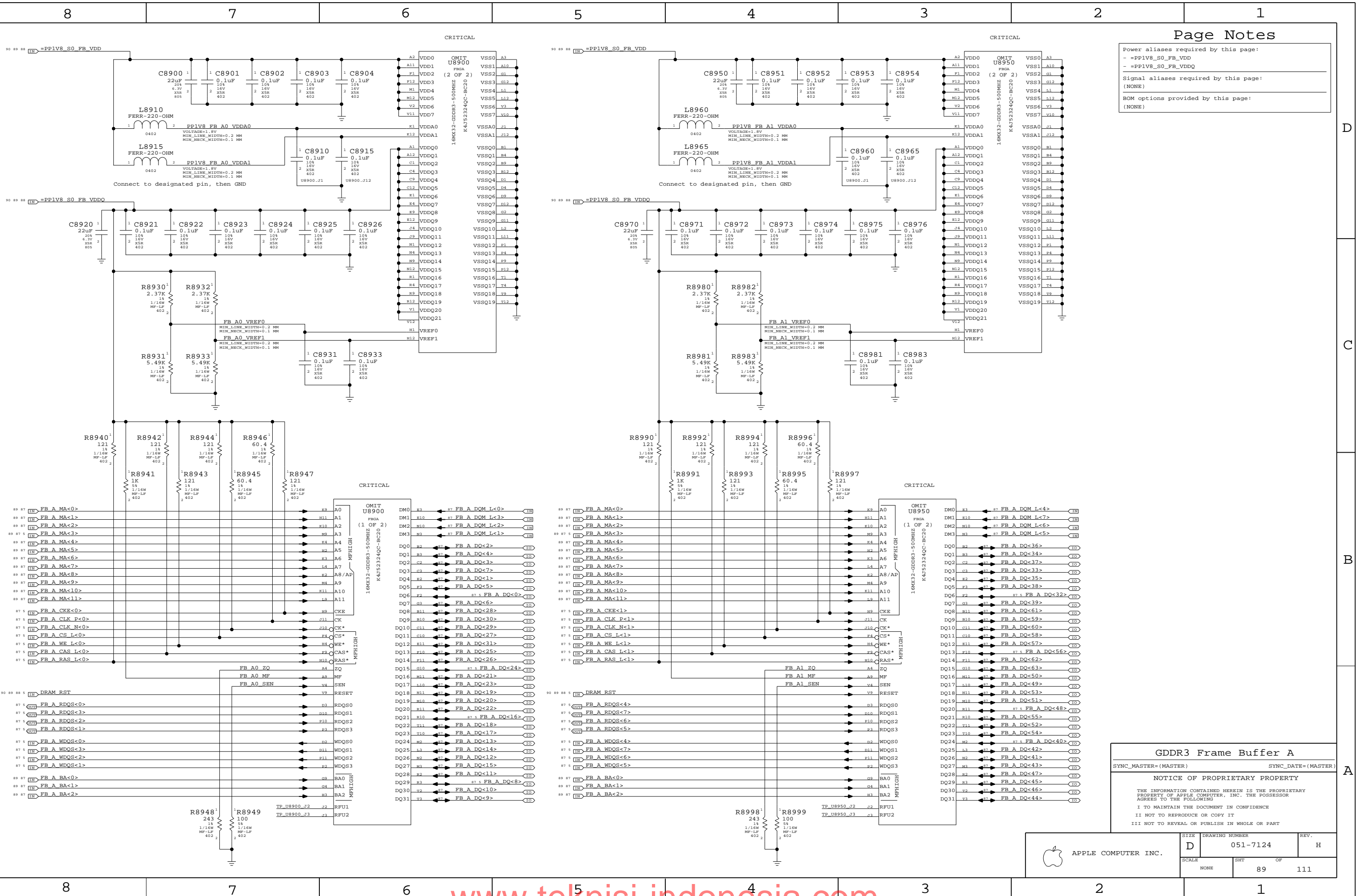
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	89	111



Power aliases required by this page:
 - =PP1V8_S0_FB_VDD
 - =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

GDWR3 Frame Buffer B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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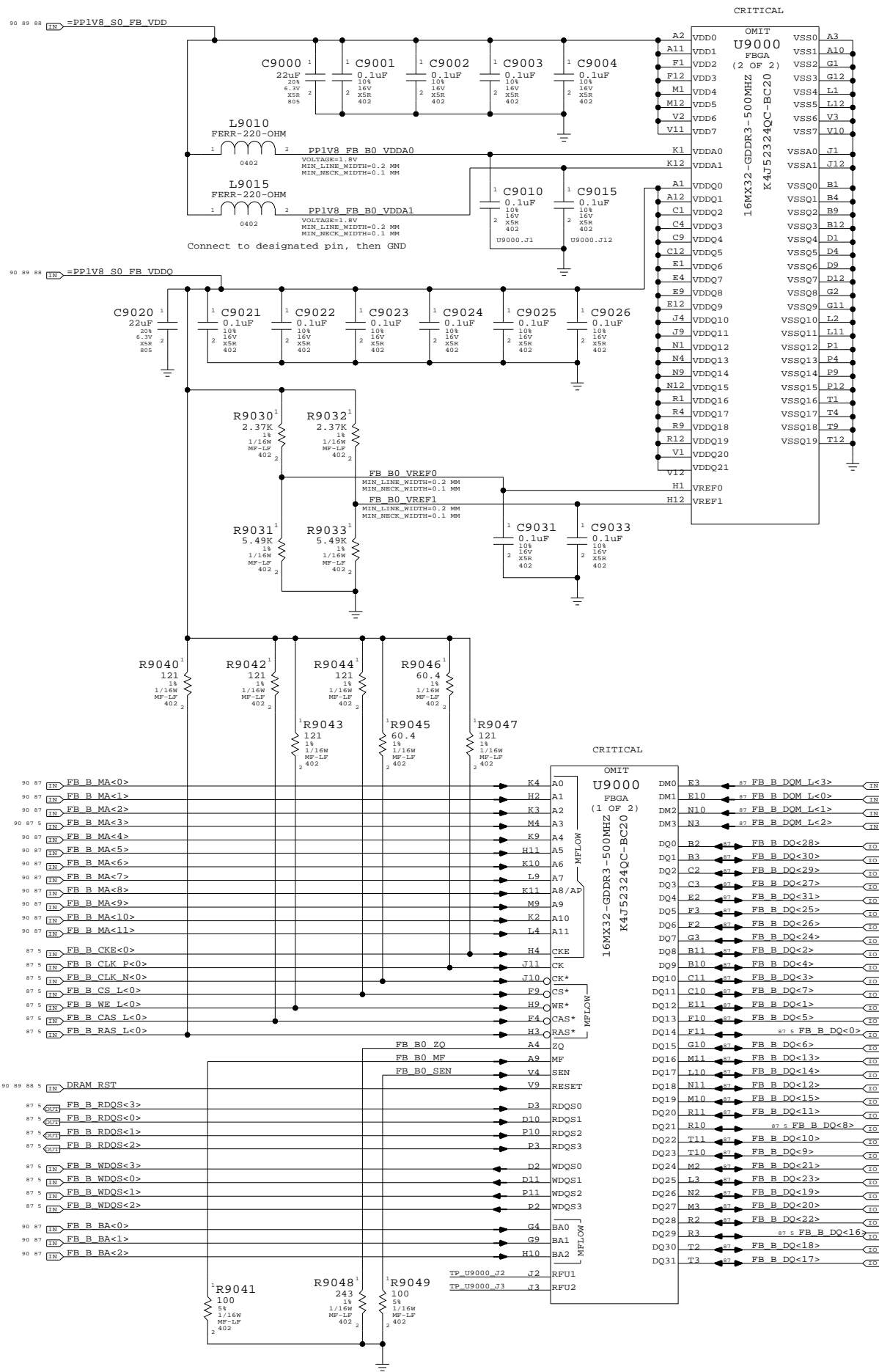
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SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	90	111



Power aliases required by this page:

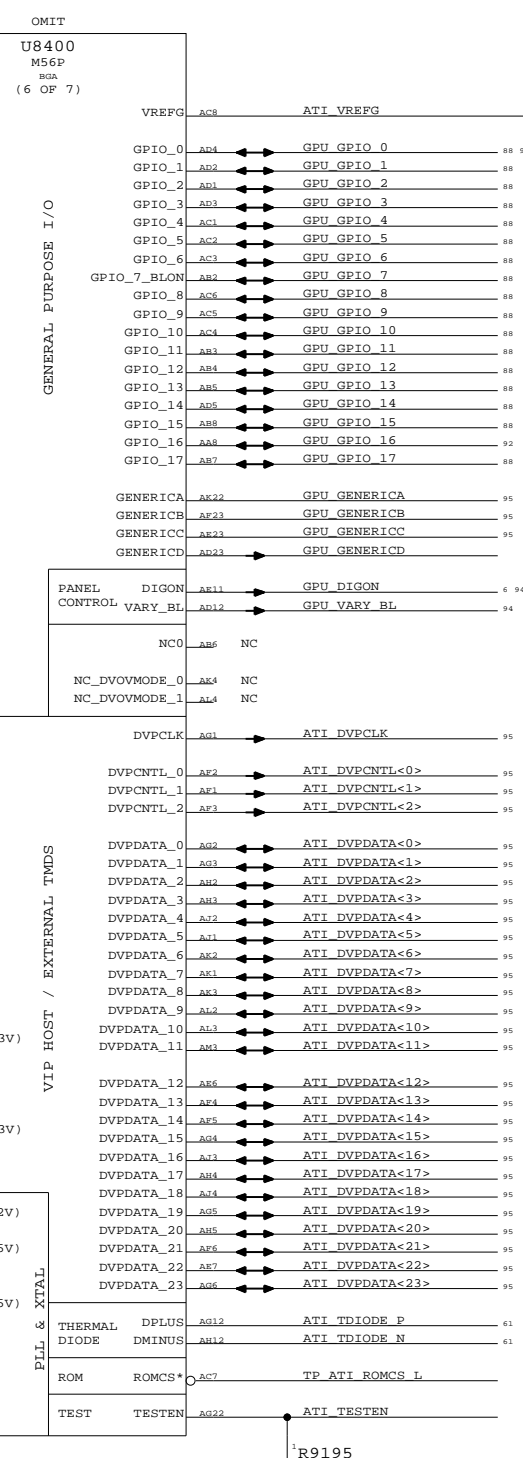
- =PP3V3_GPU_GPIOS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLL

Signal aliases required by this page:

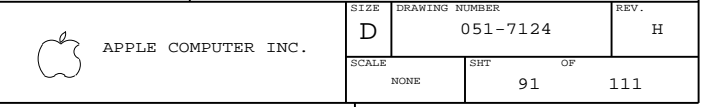
- =I2C_GPU_TMDS_SDA - I2C data line for external TMDS transmitters
- =I2C_GPU_TMDS_SCL - I2C clock line for external TMDS transmitters

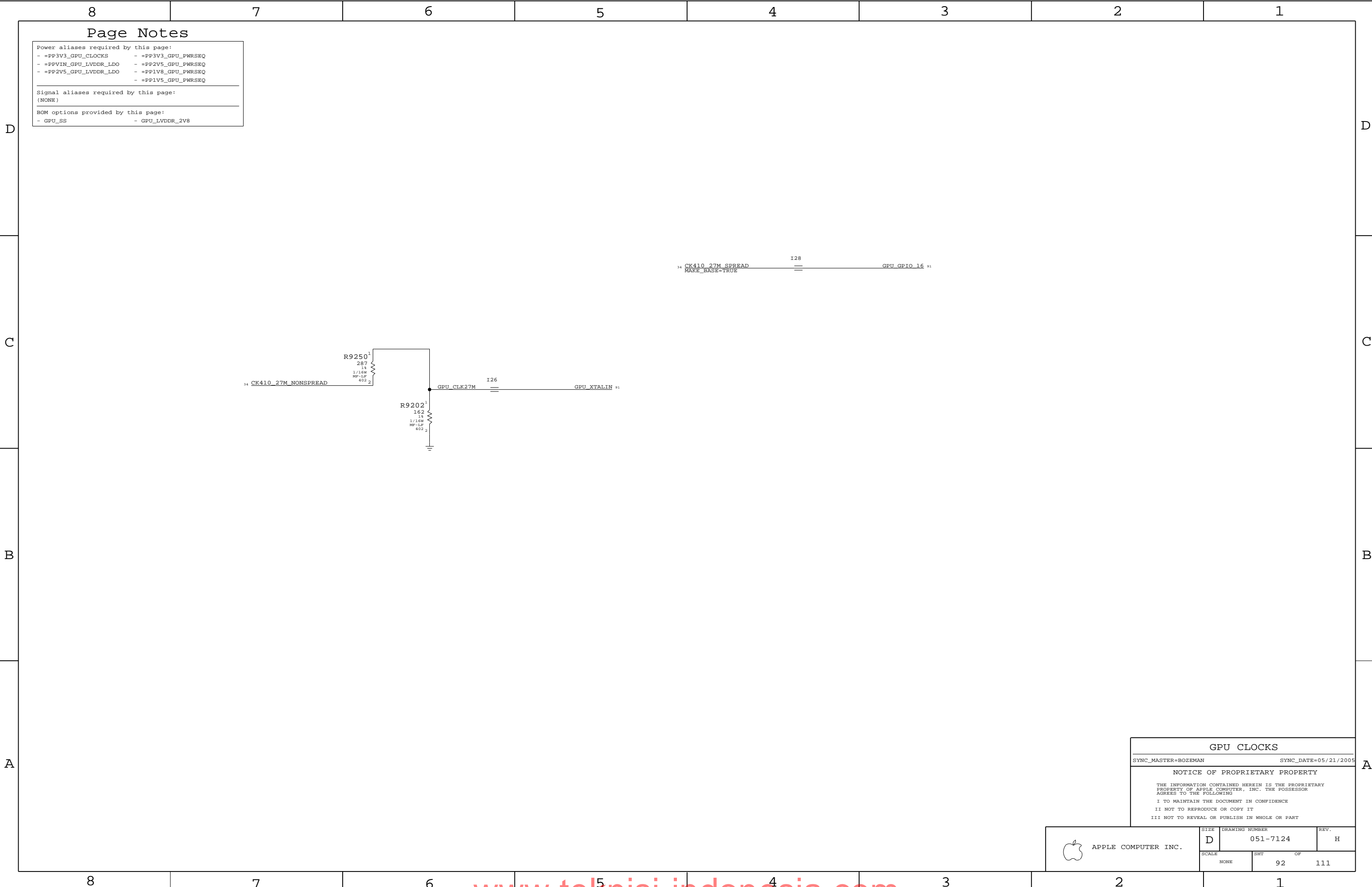
BOM options provided by this page:

(NONE)



ATI M56 GPIO/DVO/Misc	
SYNC_MASTER=(MASTER)	SYNC_DATA=(MASTER)
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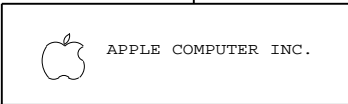




Page Notes

- Power aliases required by this page:
- =PP3V3_GPU_CLOCKS
 - =PP3V3_GPU_PWRSEQ
 - =PPVIN_GPU_LVDDR_LDO
 - =PP2V5_GPU_PWRSEQ
 - =PP2V5_GPU_LVDDR_LDO
 - =PP1V8_GPU_PWRSEQ
 - =PP1V5_GPU_PWRSEQ
- Signal aliases required by this page:
- (NONE)
- BOM options provided by this page:
- GPU_SS
 - GPU_LVDDR_2V8

GPU CLOCKS		
SYNC_MASTER=BOZEMAN		SYNC_DATE=05/21/2005
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SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	92	111

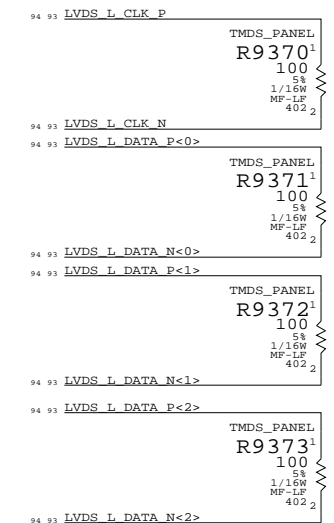
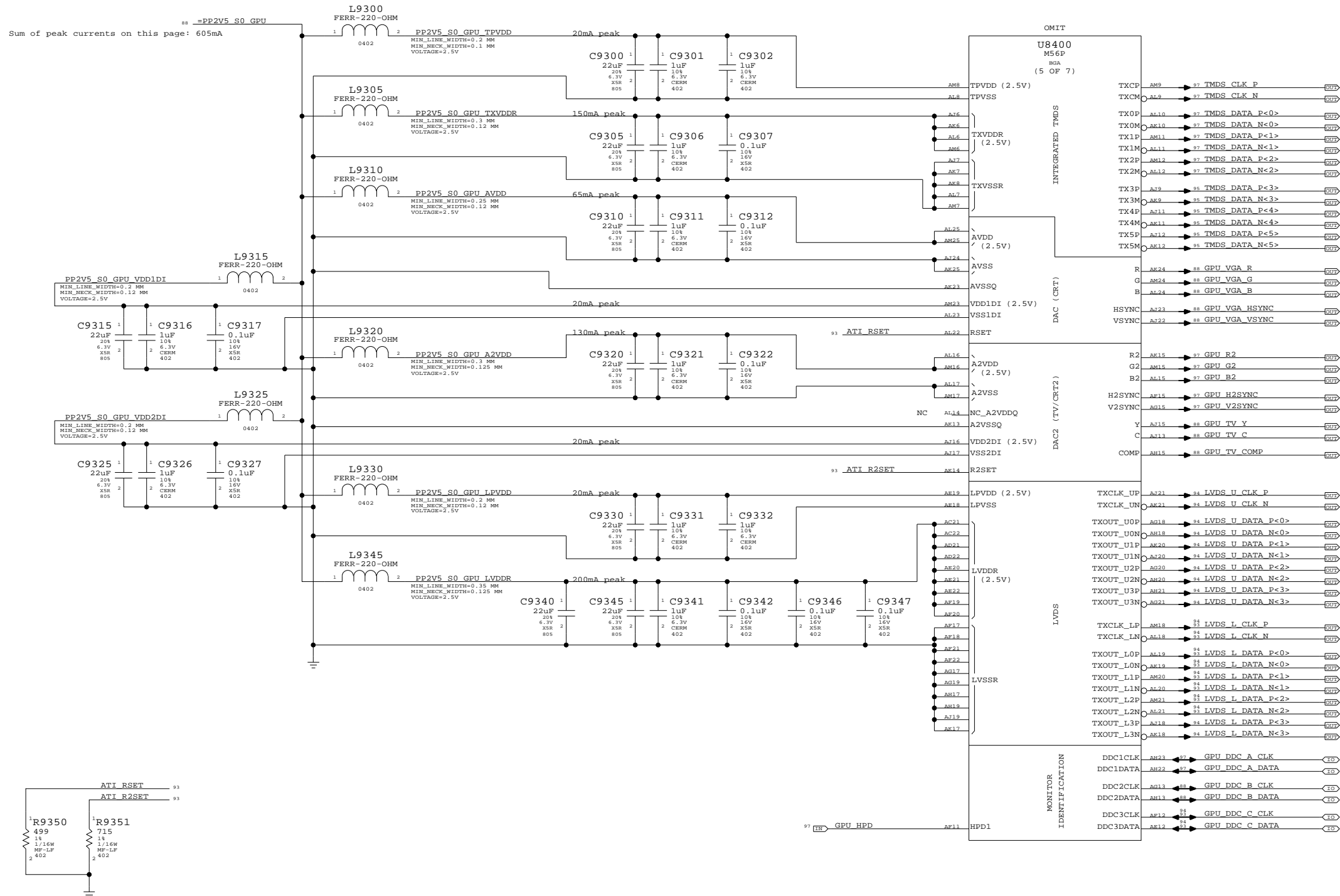


```
Power aliases required by this page:
- =PP2V5_S0_GPU
- =Pp1V8R2V5_S0_GPU_LVDDR
```

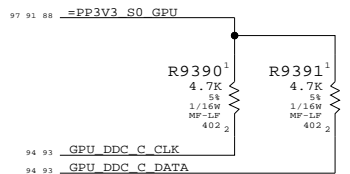
```
Signal aliases required by this page:
(NONE)
```

```
BOM options provided by this page:
(NONE)
```

TERMINATION FOR TMDS USAGE OF LVDS PINS
PLACE CLOSE TO GPU (U8400)



Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb



ATI M56 Video Interfaces

SYNC_MASTER= (MASTER)	SYNC_DATE= (MASTER)
-----------------------	---------------------

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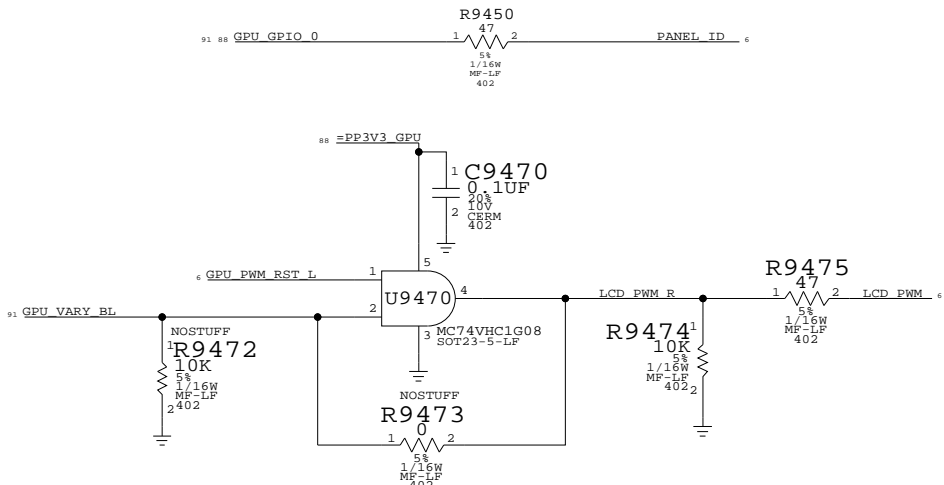
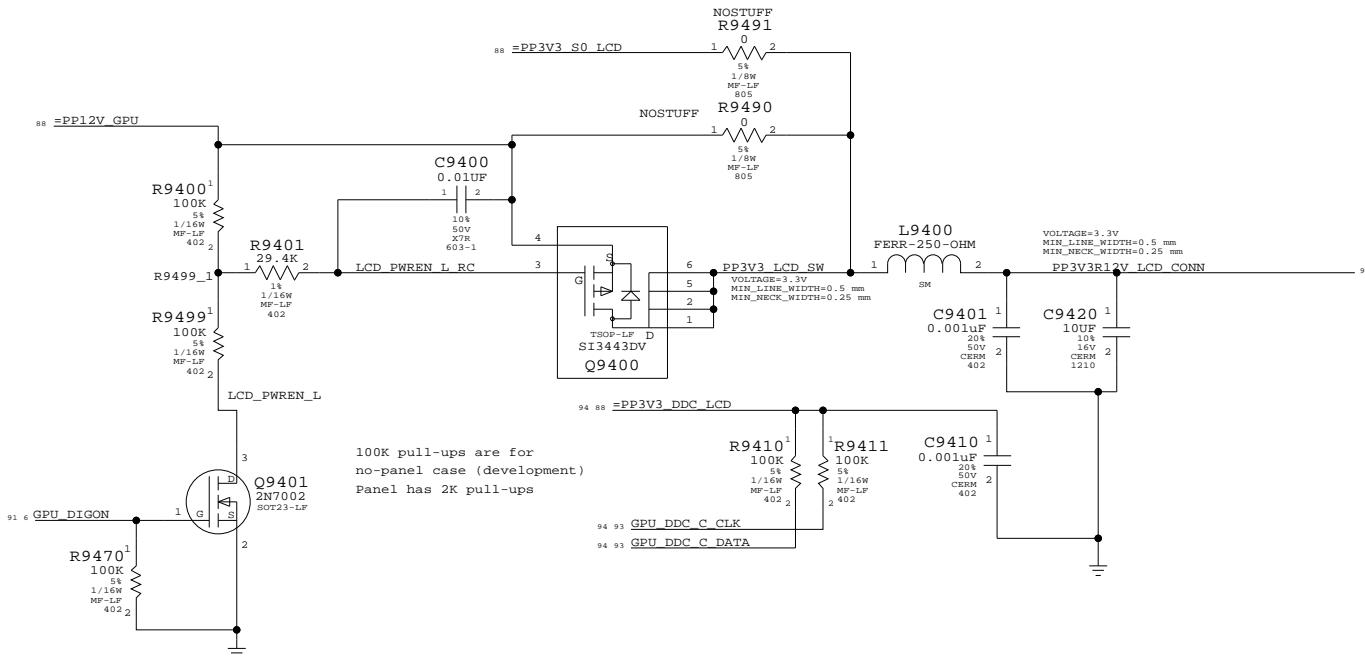
SIZE	DRAWING NUMBER	REV.
------	----------------	------

D	051-7124	H
---	----------	---

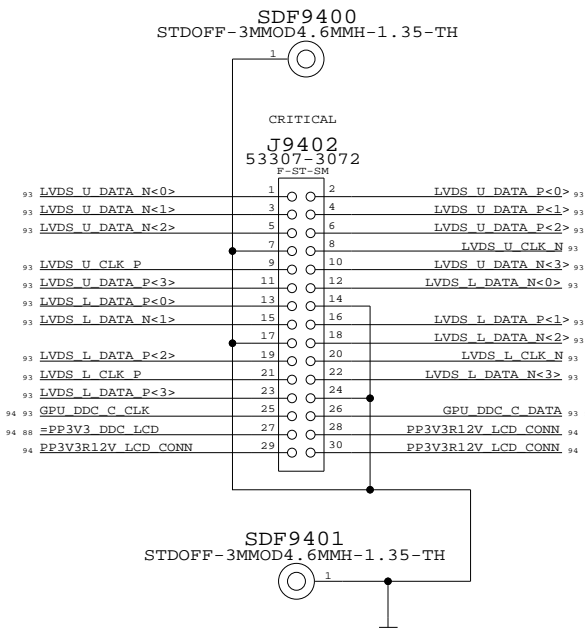
SCALE	SHT	OF

NONE	93	111
------	----	-----

LCD (LVDS) INTERFACE

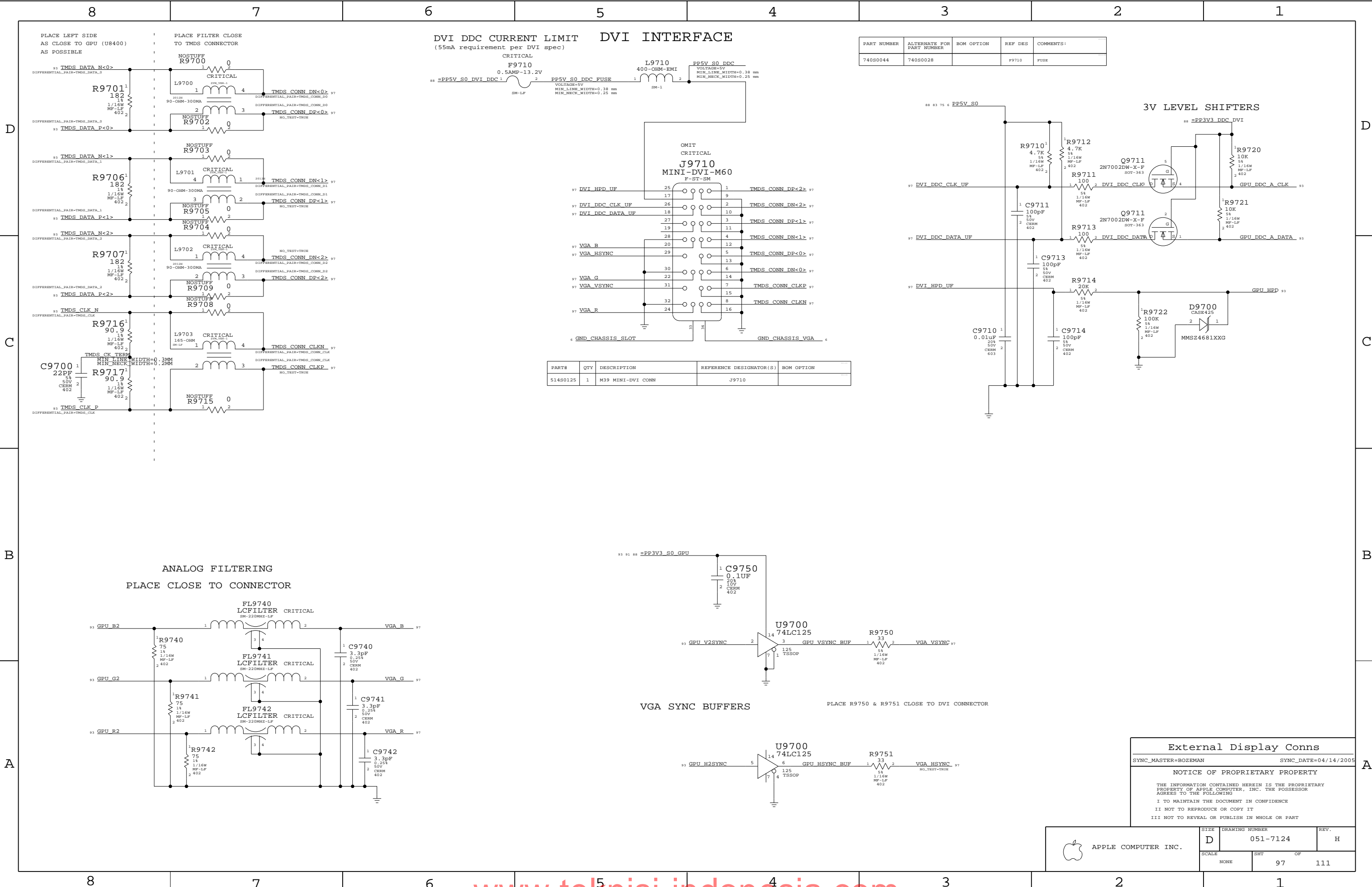


GATE TO PREVENT LEAKAGE ONTO PWM
MIGHT BE ABLE TO BYPASS IF SMC DRIVES SIGNAL



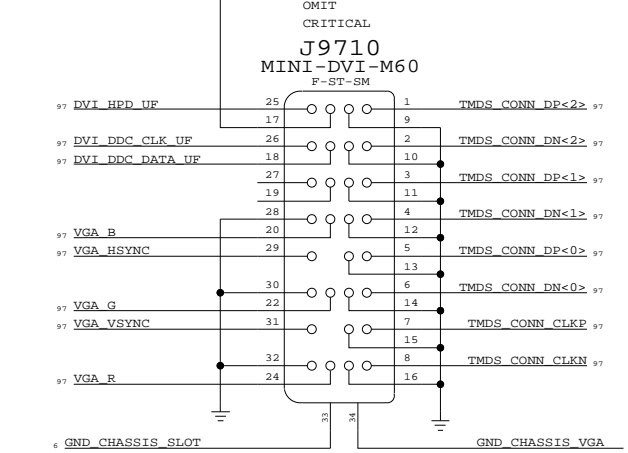
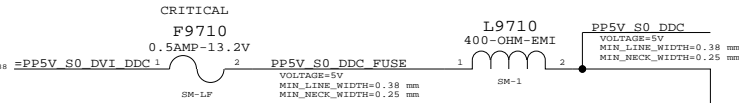
Internal Display Conns			
SYNC_MASTER=BOZEMAN		SYNC_DATE=04/27/2005	
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SIZE	DRAWING NUMBER	REV.	
D	051-7124	H	
SCALE	SHT	OF	
NONE	94	111	

[illegible]



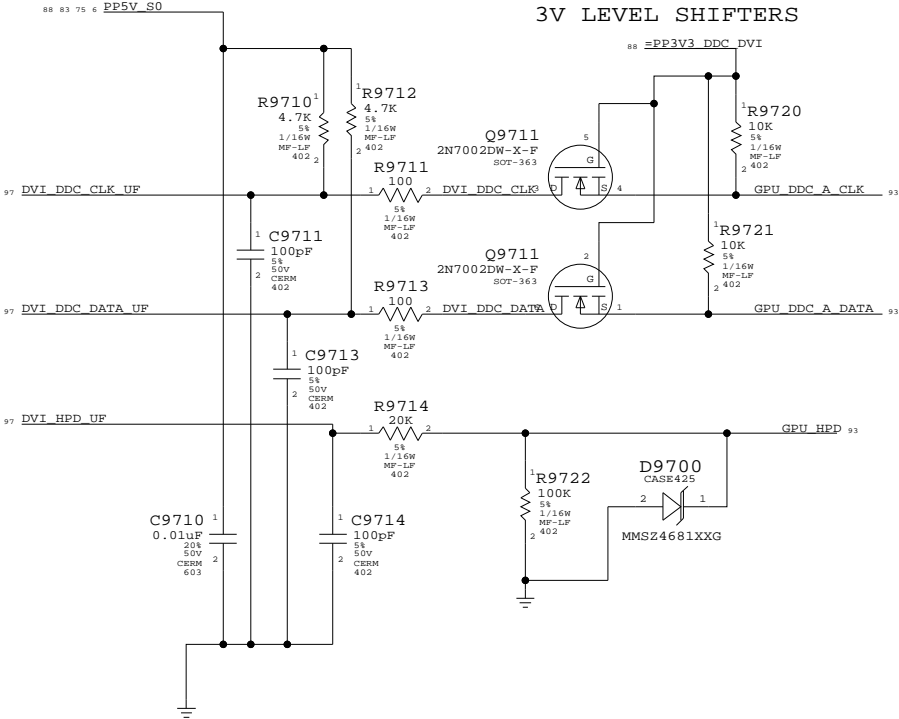
DVI DDC CURRENT LIMIT DVI INTERFACE

(55mA requirement per DVI spec)

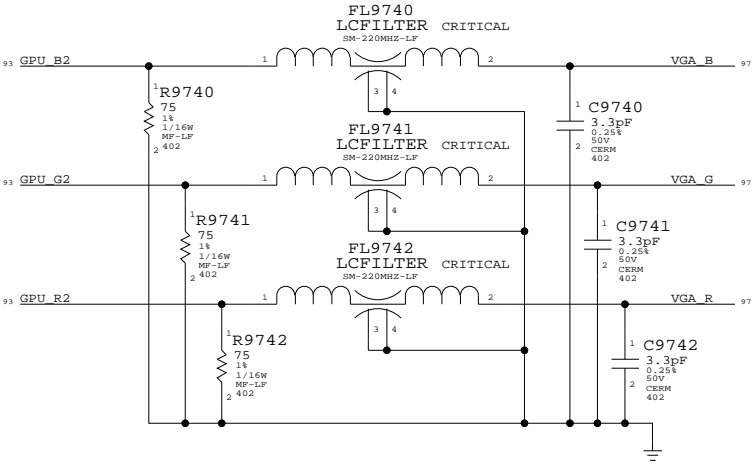


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
514S0125	1	M39 MINI-DVI CONN	J9710	

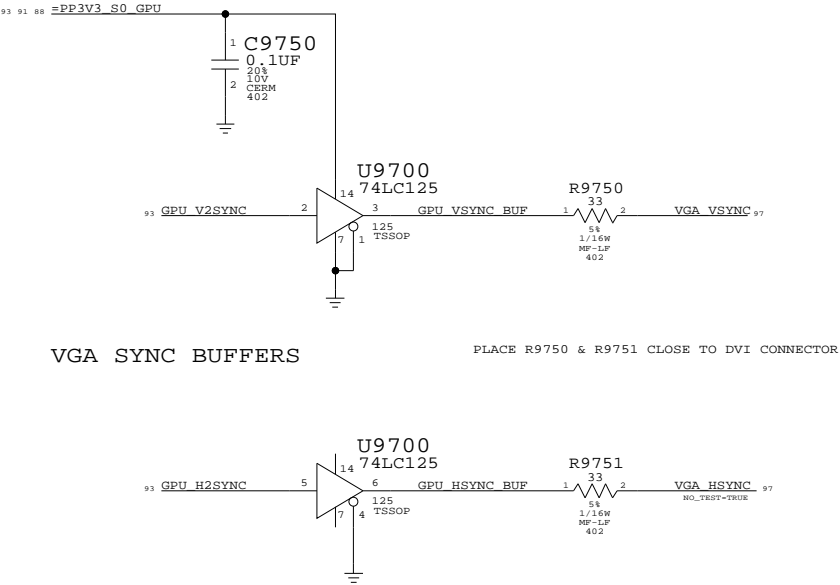
PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
740S0044	740S0028		F9710	FUSE



ANALOG FILTERING
PLACE CLOSE TO CONNECTOR



VGA SYNC BUFFERS



External Display Conns

SYNC_MASTER=BOZEMAN SYNC_DATE=04/14/2005

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SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	97	111

[illegible]

[illegible]

8				7				6				5				4				3				2				1								
D	IMVP_VID<2>	IMVP_VID<2> - @m60_1ib.M60	75C7	INT_PIRQB_L	INT_PIRQB_L - @m60_1ib.M60	22A7 26D2	MEM_A_DQ<2>	MEM_A_DQ<2> - @m60_1ib.M60	1507 28D6	MEM_B_DQ<17>	MEM_B_DQ<17> - @m60_1ib.M60	1504 29C6	NB_RST_IN_L	NB_RST_IN_L - @m60_1ib.M60	6B7 14B7	D																				
	IMVP_VID<3>	IMVP_VID<3> - @m60_1ib.M60	75C7	INT_PIRQC_L	INT_PIRQC_L - @m60_1ib.M60	22A7 26D2	MEM_A_DQ<3>	MEM_A_DQ<3> - @m60_1ib.M60	1507 28D6	MEM_B_DQ<18>	MEM_B_DQ<18> - @m60_1ib.M60	1504 29C6	NB_RST_IN_L_R	NB_RST_IN_L_R - @m60_1ib.M60	5C7 14B6																					
	IMVP_VID<4>	IMVP_VID<4> - @m60_1ib.M60	75C7	INT_PIRQD_L	INT_PIRQD_L - @m60_1ib.M60	22A7 26D2 44B5	MEM_A_DQ<4>	MEM_A_DQ<4> - @m60_1ib.M60	1507 28D3	MEM_B_DQ<19>	MEM_B_DQ<19> - @m60_1ib.M60	1504 29C6	NB_SB_SYNC_L	NB_SB_SYNC_L - @m60_1ib.M60	14B6 22A6																					
	IMVP_VID<5>	IMVP_VID<5> - @m60_1ib.M60	75C7	INT_SERIRQ	INT_SERIRQ - @m60_1ib.M60	23C8 58C7 60C1 67C6	MEM_A_DQ<5>	MEM_A_DQ<5> - @m60_1ib.M60	1507 28D3	MEM_B_DQ<20>	MEM_B_DQ<20> - @m60_1ib.M60	1504 29C3	NB_TV_DCONSEL0	NB_TV_DCONSEL0 - @m60_1ib.M60	14D6																					
	IMVP_VID<6>	IMVP_VID<6> - @m60_1ib.M60	75C7	ISENSE_CAL_EN	ISENSE_CAL_EN - @m60_1ib.M60	58B7 76A7	MEM_A_DQ<6>	MEM_A_DQ<6> - @m60_1ib.M60	1507 28D3	MEM_B_DQ<21>	MEM_B_DQ<21> - @m60_1ib.M60	1504 29C3	NB_TV_DCONSEL1	NB_TV_DCONSEL1 - @m60_1ib.M60	14C6																					
	IMVP_VR_ON	IMVP_VR_ON - @m60_1ib.M60	58D7 75C6	ITPRESET_L	ITPRESET_L - @m60_1ib.M60	11B3	MEM_A_DQ<7>	MEM_A_DQ<7> - @m60_1ib.M60	587 15D7 28D3	MEM_B_DQ<22>	MEM_B_DQ<22> - @m60_1ib.M60	1504 29C3	NB_VCCSM_LF1	NB_VCCSM_LF1 - @m60_1ib.M60	16B4																					
	INT_PIRQA_L	INT_PIRQA_L - @m60_1ib.M60	22A7 26D2	ITP_TDO	ITP_TDO - @m60_1ib.M60	11B3	MEM_A_DQ<8>	MEM_A_DQ<8> - @m60_1ib.M60	1507 28D6	MEM_B_DQ<23>	MEM_B_DQ<23> - @m60_1ib.M60	5A7 15C4 29C3	NB_VCCSM_LF2	NB_VCCSM_LF2 - @m60_1ib.M60	16B4																					
	INT_PIRQB_L	INT_PIRQB_L - @m60_1ib.M60	22A7 26D2	ITS_ALIVE	ITS_ALIVE - @m60_1ib.M60	6A7	MEM_A_DQ<9>	MEM_A_DQ<9> - @m60_1ib.M60	1507 28D6	MEM_B_DQ<24>	MEM_B_DQ<24> - @m60_1ib.M60	1504 29C6	NB_VCCSM_LF4	NB_VCCSM_LF4 - @m60_1ib.M60	16B8																					
	INT_PIRQC_L	INT_PIRQC_L - @m60_1ib.M60	22A7 26D2	ITS_PLUGGED_IN	ITS_PLUGGED_IN - @m60_1ib.M60	6A8	MEM_A_DQ<10>	MEM_A_DQ<10> - @m60_1ib.M60	1507 28D6	MEM_B_DQ<25>	MEM_B_DQ<25> - @m60_1ib.M60	5A7 15C4 29C6	NB_VCCSM_LF5	NB_VCCSM_LF5 - @m60_1ib.M60	1684																					
	INT_PIRQD_L	INT_PIRQD_L - @m60_1ib.M60	22A7 26D2 44B5	KBC_MDE	KBC_MDE - @m60_1ib.M60	58C2	MEM_A_DQ<11>	MEM_A_DQ<11> - @m60_1ib.M60	1507 28D6	MEM_B_DQ<26>	MEM_B_DQ<26> - @m60_1ib.M60	1504 29C6	NB_VTTLF_CAP1	NB_VTTLF_CAP1 - @m60_1ib.M60	17A4																					
	INT_SERIRQ	INT_SERIRQ - @m60_1ib.M60	23C8 58C7 60C1 67C6	LCD_PWM	LCD_PWM - @m60_1ib.M60	60B 94B1	MEM_A_DQ<12>	MEM_A_DQ<12> - @m60_1ib.M60	1507 28D3	MEM_B_DQ<27>	MEM_B_DQ<27> - @m60_1ib.M60	1504 29C6	NB_VTTLF_CAP2	NB_VTTLF_CAP2 - @m60_1ib.M60	17A4																					
C	ISENSE_CAL_EN	ISENSE_CAL_EN - @m60_1ib.M60	58B7 76A7	LCD_PWM_R	LCD_PWM_R - @m60_1ib.M60	94B2	MEM_A_DQ<13>	MEM_A_DQ<13> - @m60_1ib.M60	1507 28D3	MEM_B_DQ<28>	MEM_B_DQ<28> - @m60_1ib.M60	1504 29C3	NB_VTTLF_CAP3	NB_VTTLF_CAP3 - @m60_1ib.M60	17B4	C																				
	ITPRESET_L	ITPRESET_L - @m60_1ib.M60	11B3	LCD_PWREN_L	LCD_PWREN_L - @m60_1ib.M60	94C8	MEM_A_DQ<14>	MEM_A_DQ<14> - @m60_1ib.M60	587 15C7 28D3	MEM_B_DQ<29>	MEM_B_DQ<29> - @m60_1ib.M60	1504 29C3	NC_AUD1	NC_AUD1 - @m60_1ib.M60	68C4																					
	ITP_TDO	ITP_TDO - @m60_1ib.M60	11B3	LCD_PWREN_L_RC	LCD_PWREN_L_RC - @m60_1ib.M60	94C7	MEM_A_DQ<15>	MEM_A_DQ<15> - @m60_1ib.M60	1507 28D3	MEM_B_DQ<30>	MEM_B_DQ<30> - @m60_1ib.M60	1504 29C3	NC_AUD2	NC_AUD2 - @m60_1ib.M60	68B4																					
	ITS_ALIVE	ITS_ALIVE - @m60_1ib.M60	6A7	LCD_SHOULD_ON	LCD_SHOULD_ON - @m60_1ib.M60	6A6	MEM_A_DQ<16>	MEM_A_DQ<16> - @m60_1ib.M60	587 15C7 28C6	MEM_B_DQ<31>	MEM_B_DQ<31> - @m60_1ib.M60	1504 29C3	NC_AUD_GPIO_2	NC_AUD_GPIO_2 - @m60_1ib.M60	68C7																					
	ITS_PLUGGED_IN	ITS_PLUGGED_IN - @m60_1ib.M60	6A8	LED_PP1V05_S0_N	LED_PP1V05_S0_N - @m60_1ib.M60	81A4	MEM_A_DQ<17>	MEM_A_DQ<17> - @m60_1ib.M60	1507 28C6	MEM_B_DQ<32>	MEM_B_DQ<32> - @m60_1ib.M60	1504 29B6	NC_AUD_LI_TYPE JACK	NC_AUD_LI_TYPE JACK - @m60_1ib.M60	73D7																					
	KBC_MDE	KBC_MDE - @m60_1ib.M60	58C2	LED_PP1V05_S0_P	LED_PP1V05_S0_P - @m60_1ib.M60	81A4	MEM_A_DQ<18>	MEM_A_DQ<18> - @m60_1ib.M60	1507 28C6	MEM_B_DQ<33>	MEM_B_DQ<33> - @m60_1ib.M60	1504 29B6	NC_AUD_VREF_PORT_A	NC_AUD_VREF_PORT_A - @m60_1ib.M60	68C1																					
	LCD_PWM	LCD_PWM - @m60_1ib.M60	60B 94B1	LED_PP1V5_S0_N	LED_PP1V5_S0_N - @m60_1ib.M60	80A3	MEM_A_DQ<19>	MEM_A_DQ<19> - @m60_1ib.M60	1507 28C6	MEM_B_DQ<34>	MEM_B_DQ<34> - @m60_1ib.M60	15B4 29B6	NC_AUD_VREF_PORT_C	NC_AUD_VREF_PORT_C - @m60_1ib.M60	68D9																					
	LCD_PWM_R	LCD_PWM_R - @m60_1ib.M60	94B2	LED_PP1V5_S0_P	LED_PP1V5_S0_P - @m60_1ib.M60	80A4	MEM_A_DQ<20>	MEM_A_DQ<20> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<35>	MEM_B_DQ<35> - @m60_1ib.M60	15B4 29B6	NC_AUD_VREF_PORT_D	NC_AUD_VREF_PORT_D - @m60_1ib.M60	68C1																					
	LCD_PWREN_L	LCD_PWREN_L - @m60_1ib.M60	94C8	LED_PP1V8_S3_N	LED_PP1V8_S3_N - @m60_1ib.M60	79A4	MEM_A_DQ<21>	MEM_A_DQ<21> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<36>	MEM_B_DQ<36> - @m60_1ib.M60	15B4 29B3	NC_FW_NU1	NC_FW_NU1 - @m60_1ib.M60	44B3																					
	LCD_PWREN_L_RC	LCD_PWREN_L_RC - @m60_1ib.M60	94C7	LED_PP1V8_S3_P	LED_PP1V8_S3_P - @m60_1ib.M60	79A4	MEM_A_DQ<22>	MEM_A_DQ<22> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<37>	MEM_B_DQ<37> - @m60_1ib.M60	15B4 29B3	NC_FW_NU2	NC_FW_NU2 - @m60_1ib.M60	44B3																					
	LCD_SHOULD_ON	LCD_SHOULD_ON - @m60_1ib.M60	6A6	LPC_AD<0>	LPC_AD<0> - @m60_1ib.M60	21D4 58D7 60C4 67C6	MEM_A_DQ<23>	MEM_A_DQ<23> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<38>	MEM_B_DQ<38> - @m60_1ib.M60	5A7 15B4 29B3	NC_JE350_13	NC_JE350_13 - @m60_1ib.M60	47B1	C																				
B	LED_PP1V05_S0_N	LED_PP1V05_S0_N - @m60_1ib.M60	81A4	LPC_AD<1>	LPC_AD<1> - @m60_1ib.M60	21D4 58D7 60C4 67C6	MEM_A_DQ<24>	MEM_A_DQ<24> - @m60_1ib.M60	1507 28C6	MEM_B_DQ<39>	MEM_B_DQ<39> - @m60_1ib.M60	15B4 29B3	NC_SMC_P20	NC_SMC_P20 - @m60_1ib.M60	59D5																					
	LED_PP1V05_S0_P	LED_PP1V05_S0_P - @m60_1ib.M60	81A4	LPC_AD<2>	LPC_AD<2> - @m60_1ib.M60	21D4 58C7 60C1 67C6	MEM_A_DQ<25>	MEM_A_DQ<25> - @m60_1ib.M60	587 15C7 28C6	MEM_B_DQ<40>	MEM_B_DQ<40> - @m60_1ib.M60	15B4 29B6	SMC_P20	SMC_P20 - @m60_1ib.M60	58D7 59D6																					
	LED_PP1V5_S0_N	LED_PP1V5_S0_N - @m60_1ib.M60	80A3	LPC_AD<3>	LPC_AD<3> - @m60_1ib.M60	21D4 58C7 60C1 67C6	MEM_A_DQ<26>	MEM_A_DQ<26> - @m60_1ib.M60	1507 28C6	MEM_B_DQ<41>	MEM_B_DQ<41> - @m60_1ib.M60	15B4 29B6	NC_SMC_P21	NC_SMC_P21 - @m60_1ib.M60	59D5																					
	LED_PP1V5_S0_P	LED_PP1V5_S0_P - @m60_1ib.M60	80A4	LPC_AD<4>	LPC_AD<4> - @m60_1ib.M60	21C5 58C7 60C4 67C6	MEM_A_DQ<27>	MEM_A_DQ<27> - @m60_1ib.M60	1507 28C6	MEM_B_DQ<42>	MEM_B_DQ<42> - @m60_1ib.M60	15B4 29A6	SMC_P21	SMC_P21 - @m60_1ib.M60	58D7 59D6																					
	LED_PP1V8_S3_N	LED_PP1V8_S3_N - @m60_1ib.M60	79A4	LPC_FRAME_L	LPC_FRAME_L - @m60_1ib.M60	21D4 58C7 60C1 67C6	MEM_A_DQ<28>	MEM_A_DQ<28> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<43>	MEM_B_DQ<43> - @m60_1ib.M60	15B4 29A6	NC_SMC_P22	NC_SMC_P22 - @m60_1ib.M60	59D5																					
	LED_PP1V8_S3_P	LED_PP1V8_S3_P - @m60_1ib.M60	79A4	LPC_FRAME_L	LPC_FRAME_L - @m60_1ib.M60	21C5 58C7 60C4 67C6	MEM_A_DQ<29>	MEM_A_DQ<29> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<44>	MEM_B_DQ<44> - @m60_1ib.M60	5A7 15B4 29B3	SMC_P22	SMC_P22 - @m60_1ib.M60	58D7 59D6																					
	LPC_AD<0>	LPC_AD<0> - @m60_1ib.M60	21D4 58D7 60C4 67C6	LVD5_A_CLK_N	LVD5_A_CLK_N - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<30>	MEM_A_DQ<30> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<45>	MEM_B_DQ<45> - @m60_1ib.M60	15B4 29B3	NC_SMC_P23	NC_SMC_P23 - @m60_1ib.M60	59D5																					
	LPC_AD<1>	LPC_AD<1> - @m60_1ib.M60	21D4 58D7 60C4 67C6	TP_LVD5_A_CLK_N	TP_LVD5_A_CLK_N - @m60_1ib.M60	19D1	MEM_A_DQ<31>	MEM_A_DQ<31> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<46>	MEM_B_DQ<46> - @m60_1ib.M60	15B4 29A3	SMC_P23	SMC_P23 - @m60_1ib.M60	58D7 59D6																					
LPC_AD<2>	LPC_AD<2> - @m60_1ib.M60	21D4 58C7 60C1 67C6	LVD5_A_CLK_P	LVD5_A_CLK_P - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<32>	MEM_A_DQ<32> - @m60_1ib.M60	1507 28B6	MEM_B_DQ<47>	MEM_B_DQ<47> - @m60_1ib.M60	15B4 29A3	NC_SMC_P26	NC_SMC_P26 - @m60_1ib.M60	59D5																						
LPC_AD<3>	LPC_AD<3> - @m60_1ib.M60	21D4 58C7 60C1 67C6	LVD5_B_CLK_N	LVD5_B_CLK_N - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<33>	MEM_A_DQ<33> - @m60_1ib.M60	1507 28B6	MEM_B_DQ<48>	MEM_B_DQ<48> - @m60_1ib.M60	5A7 15B4 29A6	SMC_P26	SMC_P26 - @m60_1ib.M60	58D7 59D6	C																					
A	LPC_FRAME_L	LPC_FRAME_L - @m60_1ib.M60	21C5 58C7 60C4 67C6	TP_LVD5_B_CLK_N	TP_LVD5_B_CLK_N - @m60_1ib.M60	19D1	MEM_A_DQ<34>	MEM_A_DQ<34> - @m60_1ib.M60	1587 28B6	MEM_B_DQ<49>	MEM_B_DQ<49> - @m60_1ib.M60	15B4 29A6	NC_SMC_P27	NC_SMC_P27 - @m60_1ib.M60		59D5																				
	LVD5_A_CLK_N	LVD5_A_CLK_N - @m60_1ib.M60	13D5 19D2	LVD5_B_CLK_P	LVD5_B_CLK_P - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<35>	MEM_A_DQ<35> - @m60_1ib.M60	1587 28B6	MEM_B_DQ<50>	MEM_B_DQ<50> - @m60_1ib.M60	15B4 29A6	SMC_P27	SMC_P27 - @m60_1ib.M60		58D7 59D6																				
	TP_LVD5_A_CLK_N	TP_LVD5_A_CLK_N - @m60_1ib.M60	19D1	LVD5_B_DATA_N<0>	LVD5_B_DATA_N<0> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<36>	MEM_A_DQ<36> - @m60_1ib.M60	1587 28B3	MEM_B_DQ<51>	MEM_B_DQ<51> - @m60_1ib.M60	15B4 29A6	NC_VOL_DOWN	NC_VOL_DOWN - @m60_1ib.M60		68C7																				
	LVD5_A_CLK_P	LVD5_A_CLK_P - @m60_1ib.M60	13D5 19D2	LVD5_B_DATA_N<1>	LVD5_B_DATA_N<1> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<37>	MEM_A_DQ<37> - @m60_1ib.M60	1587 28B3	MEM_B_DQ<52>	MEM_B_DQ<52> - @m60_1ib.M60	15B4 29A3	NC_VOL_UP	NC_VOL_UP - @m60_1ib.M60		68C7																				
	TP_LVD5_A_CLK_P	TP_LVD5_A_CLK_P - @m60_1ib.M60	19D1	LVD5_B_DATA_N<2>	LVD5_B_DATA_N<2> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<38>	MEM_A_DQ<38> - @m60_1ib.M60	1587 28A3	MEM_B_DQ<53>	MEM_B_DQ<53> - @m60_1ib.M60	15A4 29A3	ODD_PWR_EN_L	ODD_PWR_EN_L - @m60_1ib.M60		26C2 26C3																				
	LVD5_B_CLK_N	LVD5_B_CLK_N - @m60_1ib.M60	13D5 19D2	LVD5_B_DATA_N<3>	LVD5_B_DATA_N<3> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<39>	MEM_A_DQ<39> - @m60_1ib.M60	1587 28A6	MEM_B_DQ<54>	MEM_B_DQ<54> - @m60_1ib.M60	15B4 29A3	SR_OT05	SR_OT05 - @m60_1ib.M60		68C7																				
	TP_LVD5_B_CLK_N	TP_LVD5_B_CLK_N - @m60_1ib.M60	19D1	LVD5_B_DATA_N<4>	LVD5_B_DATA_N<4> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<40>	MEM_A_DQ<40> - @m60_1ib.M60	1587 28A6	MEM_B_DQ<55>	MEM_B_DQ<55> - @m60_1ib.M60	15B4 29A3	P0V48_SMC_LSREF	P0V48_SMC_LSREF - @m60_1ib.M60		59A8 59B7																				
	LVD5_B_CLK_P	LVD5_B_CLK_P - @m60_1ib.M60	13D5 19D2	LVD5_B_DATA_P<0>	LVD5_B_DATA_P<0> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<41>	MEM_A_DQ<41> - @m60_1ib.M60	1587 28A6	MEM_B_DQ<56>	MEM_B_DQ<56> - @m60_1ib.M60	15B4 29A6	PANEL_ID	PANEL_ID - @m60_1ib.M60	6D6 94C2	A																				
A	TP_LVD5_B_CLK_P	TP_LVD5_B_CLK_P - @m60_1ib.M60	19D1	LVD5_B_DATA_P<1>	LVD5_B_DATA_P<1> - @m60_1ib.M60	13D5 19D2	MEM_A_DQ<42>	MEM_A_DQ<42> - @m60_1ib.M60	1587 28A6	MEM_B_DQ<57>	MEM_B_DQ<57> - @m60_1ib.M60	15B4 29A6	PATA_PWR_EN_L	PATA_PWR_EN_L - @m60_1ib.M60	23B3 23C3																					
	LVD5_B_DATA_N<0>	LVD5_B_DATA_N<0> - @m60_1ib.M60	13D5 19D2	LVD5_B_DATA_P<2>	LVD5_B_DATA_P<2> - @m60_1																															

<

8			7			6			5			4			3			2			1		
D		START_G_2V5_S0	START_G_2V5_S0 - @m60_lib.M60	83B8	TP_SB_XOR_AE5	TP_SB_XOR_AE5 - @m60_lib.M60	22A7	U9700_8	U9700_8 - @m60_lib.M60	6A6	D												
		START_G_3V3_S0	START_G_3V3_S0 - @m60_lib.M60	83C8	TP_SB_XOR_AE9	TP_SB_XOR_AE9 - @m60_lib.M60	22A6	USB_A_N	USB_A_N - @m60_lib.M60	22C2 47D7													
		START_G_4V5_S0	START_G_4V5_S0 - @m60_lib.M60	83D6	TP_SB_XOR_AG4	TP_SB_XOR_AG4 - @m60_lib.M60	22A7	USB_A_OC_L	USB_A_OC_L - @m60_lib.M60	22C4 22D8 47B8													
		START_G_5V_S0	START_G_5V_S0 - @m60_lib.M60	83B5	TP_SB_XOR_AG8	TP_SB_XOR_AG8 - @m60_lib.M60	22A6	USB_A_P	USB_A_P - @m60_lib.M60	22C2 47C7													
		START_G_12V_S0	START_G_12V_S0 - @m60_lib.M60	83A8	TP_SB_XOR_AH4	TP_SB_XOR_AH4 - @m60_lib.M60	22A7	USB_B_N	USB_B_N - @m60_lib.M60	22C2 53B2													
		SUS_CLK_SB	SUS_CLK_SB - @m60_lib.M60	23C3 59B5	TP_SB_XOR_AH8	TP_SB_XOR_AH8 - @m60_lib.M60	22A6	USB_B_OC_L	USB_B_OC_L - @m60_lib.M60	22C4 22D8													
		SV_SET_UP	SMC_SUS_CLK @m60_lib.M60	58C5 59B6	TP_SB_XOR_T5	TP_SB_XOR_T5 - @m60_lib.M60	21C6	USB_B_P	USB_B_P - @m60_lib.M60	22C2 53B2													
		SW_RST_BTN_L	SV_SET_UP - @m60_lib.M60	23B6 23C3 60C1	TP_SB_XOR_U3	TP_SB_XOR_U3 - @m60_lib.M60	21C6	USB_CAMERA_N	USB_CAMERA_N - @m60_lib.M60	47B2													
		SW_RST_DEBNC	SW_RST_BTN_L - @m60_lib.M60	501 26C6	TP_SB_XOR_U5	TP_SB_XOR_U5 - @m60_lib.M60	21C6	USB_CAMERA_P	USB_CAMERA_P - @m60_lib.M60	47B2													
		SYS_LED_BRT_D	SW_RST_DEBNC - @m60_lib.M60	26C4	TP_SB_XOR_U7	TP_SB_XOR_U7 - @m60_lib.M60	21C6	USB_C_N	USB_C_N - @m60_lib.M60	22C2 47B7													
C		SYS_LED_C	SYS_LED_BRT_D - @m60_lib.M60	60B8	TP_SB_XOR_V3	TP_SB_XOR_V3 - @m60_lib.M60	21C6	USB_C_OC_L	USB_C_OC_L - @m60_lib.M60	22C4 22D8 47B8	C												
		SYS_LED_CTL_B	SYS_LED_C - @m60_lib.M60	60B6	TP_SB_XOR_V4	TP_SB_XOR_V4 - @m60_lib.M60	21C6	USB_C_P	USB_C_P - @m60_lib.M60	22C2 47B7													
		SYS_LED_CTL_C	SYS_LED_CTL_B - @m60_lib.M60	60B7	TP_SB_XOR_V6	TP_SB_XOR_V6 - @m60_lib.M60	21C6	USB_D_N	USB_D_N - @m60_lib.M60	22C2 47B3													
		SYS_LED_CTL_D	SYS_LED_CTL_C - @m60_lib.M60	60B6	TP_SB_XOR_V7	TP_SB_XOR_V7 - @m60_lib.M60	21C6	USB_D_OC_L	USB_D_OC_L - @m60_lib.M60	22C4 22D8													
		SYS_ONEWIRE	SYS_LED_CTL_D - @m60_lib.M60	60B6	TP_SB_XOR_W1	TP_SB_XOR_W1 - @m60_lib.M60	21C6	USB_D_P	USB_D_P - @m60_lib.M60	22C2 47B3													
		SYS_POWERFAIL_L	SYS_ONEWIRE - @m60_lib.M60	58B7 59B4	TP_SB_XOR_W3	TP_SB_XOR_W3 - @m60_lib.M60	21C6	USB_E_N	USB_E_N - @m60_lib.M60	22C2 47A7													
		THERM_DX_N	SYS_POWERFAIL_L - @m60_lib.M60	6D8 57B7 76D2 82B7	TP_SB_XOR_Y1	TP_SB_XOR_Y1 - @m60_lib.M60	21C6	USB_E_OC_L	USB_E_OC_L - @m60_lib.M60	22C4 22D8 47B8													
		THERM_DX_P	THERM_DX_N - @m60_lib.M60	10B5 10C5	TP_SB_XOR_Y2	TP_SB_XOR_Y2 - @m60_lib.M60	21C6	USB_E_P	USB_E_P - @m60_lib.M60	22C2 47A7													
		THRM_ALERT_L	THERM_DX_P - @m60_lib.M60	10B5 10C5	TP_U8400_AG14	TP_U8400_AG14 - @m60_lib.M60	91A5	USB_F_N	USB_F_N - @m60_lib.M60	22C2													
		THRM_THM	THRM_ALERT_L - @m60_lib.M60	10D3	TP_U8900_J2	TP_U8900_J2 - @m60_lib.M60	89A7	USB_F_P	USB_F_P - @m60_lib.M60	22C2													
B		THERM_TIM	THRM_THM - @m60_lib.M60	10C4	TP_U8900_J3	TP_U8900_J3 - @m60_lib.M60	89A7	USB_G_N	USB_G_N - @m60_lib.M60	22C2 47A3	B												
		TMD5_CHK_TERM	TMD5_CHK_TERM - @m60_lib.M60	97C8	TP_U8950_J2	TP_U8950_J2 - @m60_lib.M60	89A4	USB_BT_N	USB_BT_N - @m60_lib.M60	47A3													
		TMD5_CLK_N	TMD5_CLK_N - @m60_lib.M60	93C3 97C8	TP_U8950_J3	TP_U8950_J3 - @m60_lib.M60	89A4	USB_G_P	USB_G_P - @m60_lib.M60	22C2 47A3													
		TMD5_CLK_P	TMD5_CLK_P - @m60_lib.M60	93C3 97C8	TP_U9000_J2	TP_U9000_J2 - @m60_lib.M60	90A7	USB_BT_P	USB_BT_P - @m60_lib.M60	47A3													
		TMD5_CONN_CLKN	TMD5_CONN_CLKN - @m60_lib.M60	97C4 97C7	TP_U9000_J3	TP_U9000_J3 - @m60_lib.M60	90A7	USB_H_N	USB_H_N - @m60_lib.M60	22C2 47C3													
		TMD5_CONN_CLKP	TMD5_CONN_CLKP - @m60_lib.M60	97C4 97C7	TP_U9050_J2	TP_U9050_J2 - @m60_lib.M60	90A4	USB_H_P	USB_H_P - @m60_lib.M60	22C2 47C3													
		TMD5_CONN_DM<0>	TMD5_CONN_DM<0> - @m60_lib.M60	97C4 97D7	TP_U9050_J3	TP_U9050_J3 - @m60_lib.M60	90A4	USB_IR_N	USB_IR_N - @m60_lib.M60	47C2													
		TMD5_CONN_DM<1>	TMD5_CONN_DM<1> - @m60_lib.M60	97C4 97D7	TSSENSE_GPU_DXP	TSSENSE_GPU_DXP - @m60_lib.M60	61C5	USB_IR_P	USB_IR_P - @m60_lib.M60	47C2													
		TMD5_CONN_DM<2>	TMD5_CONN_DM<2> - @m60_lib.M60	97C7 97D4	TSSENSE_NB_DXP	TSSENSE_NB_DXP - @m60_lib.M60	61B5	USB_PORT0_N	USB_PORT0_N - @m60_lib.M60	47D5													
		TMD5_CONN_DP<0>	TMD5_CONN_DP<0> - @m60_lib.M60	97C4 97D7	TSSENSE_NB_GPU_DXP	TSSENSE_NB_GPU_DXP - @m60_lib.M60	61B5	USB_PORT0_P	USB_PORT0_P - @m60_lib.M60	47C5													
A		TMD5_CONN_DP<1>	TMD5_CONN_DP<1> - @m60_lib.M60	97C4 97D7	TV_DACA_OUT	TV_DACA_OUT - @m60_lib.M60	13C5 19B1	USB_PORT1_N	USB_PORT1_N - @m60_lib.M60	47B5	A												
		TMD5_CONN_DP<2>	TMD5_CONN_DP<2> - @m60_lib.M60	97C7 97D4	TV_DACC_OUT	TV_DACC_OUT - @m60_lib.M60	13C5 19B1	USB_PORT1_P	USB_PORT1_P - @m60_lib.M60	47B5													
		TMD5_DATA_N<0>	TMD5_CONN_DP<2> - @m60_lib.M60	93C3 97D8	TV_IRTNC	TV_IRTNC - @m60_lib.M60	13C5 19B1	USB_PORT2_N	USB_PORT2_N - @m60_lib.M60	47A5													
		TMD5_DATA_N<1>	TMD5_DATA_N<0> - @m60_lib.M60	93C3 97D8	TV_IRTNA	TV_IRTNA - @m60_lib.M60	13C5 19B1	USB_PORT2_P	USB_PORT2_P - @m60_lib.M60	47A5													
		TMD5_DATA_N<2>	TMD5_DATA_N<1> - @m60_lib.M60	93C3 97C8	TV_IRTNC	TV_IRTNC - @m60_lib.M60	13C5 19B1	USB_RBIAS_PN	USB_RBIAS_PN - @m60_lib.M60	22C2													
		TMD5_DATA_N<3>	TMD5_DATA_N<2> - @m60_lib.M60	93C3 95D6	TV_IRTND	TV_IRTND - @m60_lib.M60	13C5 19B1	VGA_B	VGA_B - @m60_lib.M60	97B6 97C5													
		TMD5_DATA_N<4>	TMD5_DATA_N<3> - @m60_lib.M60	93C3 95D6	TV_IREF	TV_IREF - @m60_lib.M60	13C5 19B1	VGA_G	VGA_G - @m60_lib.M60	97A6 97C5													
		TMD5_DATA_N<5>	TMD5_DATA_N<4> - @m60_lib.M60	93C3 95D6	PP3V3_S0_NB_VCCA_TVBG	PP3V3_S0_NB_VCCA_TVBG - @m60_lib.M60	17C6 19B1	VGA_HSYNC	VGA_HSYNC - @m60_lib.M60	97A3 97C5													
		TMD5_DATA_P<0>	TMD5_DATA_N<5> - @m60_lib.M60	93C3 97D8	PP3V3_S0_NB_VCCA_TVDDAC	PP3V3_S0_NB_VCCA_TVDDAC - @m60_lib.M60	17C6 19B1	VGA_R	VGA_R - @m60_lib.M60	97A6 97C5													
		TMD5_DATA_P<1>	TMD5_DATA_P<0> - @m60_lib.M60	93C3 97C8	PP3V3_S0_NB_VCCA_TVDDACB	PP3V3_S0_NB_VCCA_TVDDACB - @m60_lib.M60	17C6 19B1	VGA_VSYNC	VGA_VSYNC - @m60_lib.M60	97A3 97C5													

U9700_8

USB_A_N

USB_A_OC_L

USB_A_P

USB_B_N

USB_B_OC_L

USB_B_P

USB_CAMERA_N

USB_CAMERA_P

USB_C_N

USB_C_OC_L

USB_C_P

USB_D_N

USB_D_OC_L

USB_D_P

USB_E_N

USB_E_OC_L

USB_E_P

USB_F_N

USB_F_P

USB_G_N

USB_G_P

USB_BT_P

USB_H_N

USB_H_P

USB_IR_N

USB_IR_P

USB_PORT0_N

USB_PORT0_P

USB_PORT1_N

USB_PORT1_P

USB_PORT2_N

USB_PORT2_P

USB_RBIAS_PN

USB_H_P

USB_IR_N

VGA_G

VGA_HSYNC

VGA_R

VGA_VSYNC

VMAIN_AVLBL

VR_PWRGD_CK410

VR_PWRGOOD_DELAY

XDP_BPM_L<0>

XDP_BPM_L<1>

XDP_BPM_L<2>

XDP_BPM_L<3>

XDP_BPM_L<4>

XDP_BPM_L<5>

XDP_DBRESET_L

XDP_TCK

XDP_TDI

XDP_TDO

XDP_TMS

XDP_TRST_L

ZH701P1

ZH702P1

ZH703P1

U9700_8 - @m60_lib.M60

USB_A_N - @m60_lib.M60

USB_A_OC_L - @m60_lib.M60

USB_A_P - @m60_lib.M60

USB_B_N - @m60_lib.M60

USB_B_OC_L - @m60_lib.M60

USB_B_P - @m60_lib.M60

USB_CAMERA_N - @m60_lib.M60

USB_CAMERA_P - @m60_lib.M60

USB_C_N - @m60_lib.M60

USB_C_OC_L - @m60_lib.M60

USB_C_P - @m60_lib.M60

USB_D_N - @m60_lib.M60

USB_D_OC_L - @m60_lib.M60

USB_D_P - @m60_lib.M60

USB_E_N - @m60_lib.M60

USB_E_OC_L - @m60_lib.M60

USB_E_P - @m60_lib.M60

USB_F_N - @m60_lib.M60

USB_F_P - @m60_lib.M60

USB_G_N - @m60_lib.M60

USB_G_P - @m60_lib.M60

USB_BT_P - @m60_lib.M60

USB_H_N - @m60_lib.M60

USB_H_P - @m60_lib.M60

USB_IR_N - @m60_lib.M60

USB_IR_P - @m60_lib.M60

USB_PORT0_N - @m60_lib.M60

USB_PORT0_P - @m60_lib.M60

USB_PORT1_N - @m60_lib.M60

USB_PORT1_P - @m60_lib.M60

USB_PORT2_N - @m60_lib.M60

USB_PORT2_P - @m60_lib.M60

USB_RBIAS_PN - @m60_lib.M60

VGA_B - @m60_lib.M60

VGA_G - @m60_lib.M60

VGA_HSYNC - @m60_lib.M60

VGA_R - @m60_lib.M60

VGA_VSYNC - @m60_lib.M60

VMAIN_AVLBL - @m60_lib.M60

VR_PWRGD_CK410 - @m60_lib.M60

VR_PWRGOOD_DELAY - @m60_lib.M60

XDP_BPM_L<0> - @m60_lib.M60

XDP_BPM_L<1> - @m60_lib.M60

XDP_BPM_L<2> - @m60_lib.M60

XDP_BPM_L<3> - @m60_lib.M60

XDP_BPM_L<4> - @m60_lib.M60

XDP_BPM_L<5> - @m60_lib.M60

XDP_DBRESET_L - @m60_lib.M60

XDP_TCK - @m60_lib.M60

XDP_TDI - @m60_lib.M60

XDP_TDO - @m60_lib.M60

XDP_TMS - @m60_lib.M60

XDP_TRST_L - @m60_lib.M60

ZH701P1 - @m60_lib.M60

ZH702P1 - @m60_lib.M60

ZH703P1 - @m60_lib.M60

8			7			6			5			4			3			2			1		
D		START_G_2V5_S0	START_G_2V5_S0 - @m60_lib.M60	83B8	TP_SB_XOR_AE5	TP_SB_XOR_AE5 - @m60_lib.M60	22A7	U9700_8	U9700_8 - @m60_lib.M60	6A6	D												
		START_G_3V3_S0	START_G_3V3_S0 - @m60_lib.M60	83C8	TP_SB_XOR_AE9	TP_SB_XOR_AE9 - @m60_lib.M60	22A6	USB_A_N	USB_A_N - @m60_lib.M60	22C2 47D7													
		START_G_4V5_S0	START_G_4V5_S0 - @m60_lib.M60	83D6	TP_SB_XOR_AG4	TP_SB_XOR_AG4 - @m60_lib.M60	22A7	USB_A_OC_L	USB_A_OC_L - @m60_lib.M60	22C4 22D8 47B8													
		START_G_5V_S0	START_G_5V_S0 - @m60_lib.M60	83B5	TP_SB_XOR_AG8	TP_SB_XOR_AG8 - @m60_lib.M60	22A6	USB_A_P	USB_A_P - @m60_lib.M60	22C2 47C7													
		START_G_12V_S0	START_G_12V_S0 - @m60_lib.M60	83A8	TP_SB_XOR_AH4	TP_SB_XOR_AH4 - @m60_lib.M60	22A7	USB_B_N	USB_B_N - @m60_lib.M60	22C2 53B2													
		SUS_CLK_SB	SUS_CLK_SB - @m60_lib.M60	23C3 59B5	TP_SB_XOR_AH8	TP_SB_XOR_AH8 - @m60_lib.M60	22A6	USB_B_OC_L	USB_B_OC_L - @m60_lib.M60	22C4 22D8													
		SV_SET_UP	SMC_SUS_CLK @m60_lib.M60	58C5 59B6	TP_SB_XOR_T5	TP_SB_XOR_T5 - @m60_lib.M60	21C6	USB_B_P	USB_B_P - @m60_lib.M60	22C2 53B2													
		SW_RST_BTN_L	SV_SET_UP - @m60_lib.M60	23B6 23C3 60C1	TP_SB_XOR_U3	TP_SB_XOR_U3 - @m60_lib.M60	21C6	USB_CAMERA_N	USB_CAMERA_N - @m60_lib.M60	47B2													
		SW_RST_DEBNC	SW_RST_BTN_L - @m60_lib.M60	501 26C6	TP_SB_XOR_U5	TP_SB_XOR_U5 - @m60_lib.M60	21C6	USB_CAMERA_P	USB_CAMERA_P - @m60_lib.M60	47B2													
		SYS_LED_BRT_D	SW_RST_DEBNC - @m60_lib.M60	26C4	TP_SB_XOR_U7	TP_SB_XOR_U7 - @m60_lib.M60	21C6	USB_C_N	USB_C_N - @m60_lib.M60	22C2 47B7													
C		SYS_LED_C	SYS_LED_BRT_D - @m60_lib.M60	60B8	TP_SB_XOR_V3	TP_SB_XOR_V3 - @m60_lib.M60	21C6	USB_C_OC_L	USB_C_OC_L - @m60_lib.M60	22C4 22D8 47B8	C												
		SYS_LED_CTL_B	SYS_LED_C - @m60_lib.M60	60B6	TP_SB_XOR_V4	TP_SB_XOR_V4 - @m60_lib.M60	21C6	USB_C_P	USB_C_P - @m60_lib.M60	22C2 47B7													
		SYS_LED_CTL_C	SYS_LED_CTL_B - @m60_lib.M60	60B7	TP_SB_XOR_V6	TP_SB_XOR_V6 - @m60_lib.M60	21C6	USB_D_N	USB_D_N - @m60_lib.M60	22C2 47B3													
		SYS_LED_CTL_D	SYS_LED_CTL_C - @m60_lib.M60	60B6	TP_SB_XOR_V7	TP_SB_XOR_V7 - @m60_lib.M60	21C6	USB_D_OC_L	USB_D_OC_L - @m60_lib.M60	22C4 22D8													
		SYS_ONEWIRE	SYS_LED_CTL_D - @m60_lib.M60	60B6	TP_SB_XOR_W1	TP_SB_XOR_W1 - @m60_lib.M60	21C6	USB_D_P	USB_D_P - @m60_lib.M60	22C2 47B3													
		SYS_POWERFAIL_L	SYS_ONEWIRE - @m60_lib.M60	58B7 59B4	TP_SB_XOR_W3	TP_SB_XOR_W3 - @m60_lib.M60	21C6	USB_E_N	USB_E_N - @m60_lib.M60	22C2 47A7													
		THERM_DX_N	SYS_POWERFAIL_L - @m60_lib.M60	6D8 57B7 76D2 82B7	TP_SB_XOR_Y1	TP_SB_XOR_Y1 - @m60_lib.M60	21C6	USB_E_OC_L	USB_E_OC_L - @m60_lib.M60	22C4 22D8 47B8													
		THERM_DX_P	THERM_DX_N - @m60_lib.M60	10B5 10C5	TP_SB_XOR_Y2	TP_SB_XOR_Y2 - @m60_lib.M60	21C6	USB_E_P	USB_E_P - @m60_lib.M60	22C2 47A7													
		THRM_ALERT_L	THERM_DX_P - @m60_lib.M60	10B5 10C5	TP_U8400_AG14	TP_U8400_AG14 - @m60_lib.M60	91A5	USB_F_N	USB_F_N - @m60_lib.M60	22C2													
		THRM_THM	THRM_ALERT_L - @m60_lib.M60	10D3	TP_U8900_J2	TP_U8900_J2 - @m60_lib.M60	89A7	USB_F_P	USB_F_P - @m60_lib.M60	22C2													
B		THERM_TIM	THRM_THM - @m60_lib.M60	10C4	TP_U8900_J3	TP_U8900_J3 - @m60_lib.M60	89A7	USB_G_N	USB_G_N - @m60_lib.M60	22C2 47A3	B												
		TMD5_CHK_TERM	TMD5_CHK_TERM - @m60_lib.M60	97C8	TP_U8950_J2	TP_U8950_J2 - @m60_lib.M60	89A4	USB_BT_N	USB_BT_N - @m60_lib.M60	47A3													
		TMD5_CLK_N	TMD5_CLK_N - @m60_lib.M60	93C3 97C8	TP_U8950_J3	TP_U8950_J3 - @m60_lib.M60	89A4	USB_G_P	USB_G_P - @m60_lib.M60	22C2 47A3													
		TMD5_CLK_P	TMD5_CLK_P - @m60_lib.M60	93C3 97C8	TP_U9000_J2	TP_U9000_J2 - @m60_lib.M60	90A7	USB_BT_P	USB_BT_P - @m60_lib.M60	47A3													
		TMD5_CONN_CLKN	TMD5_CONN_CLKN - @m60_lib.M60	97C4 97C7	TP_U9000_J3	TP_U9000_J3 - @m60_lib.M60	90A7	USB_H_N	USB_H_N - @m60_lib.M60	22C2 47C3													
		TMD5_CONN_CLKP	TMD5_CONN_CLKP - @m60_lib.M60	97C4 97C7	TP_U9050_J2	TP_U9050_J2 - @m60_lib.M60	90A4	USB_H_P	USB_H_P - @m60_lib.M60	22C2 47C3													
		TMD5_CONN_DM<0>	TMD5_CONN_DM<0> - @m60_lib.M60	97C4 97D7	TP_U9050_J3	TP_U9050_J3 - @m60_lib.M60	90A4	USB_IR_N	USB_IR_N - @m60_lib.M60	47C2													
		TMD5_CONN_DM<1>	TMD5_CONN_DM<1> - @m60_lib.M60	97C4 97D7	TSSENSE_GPU_DXP	TSSENSE_GPU_DXP - @m60_lib.M60	61C5	USB_IR_P	USB_IR_P - @m60_lib.M60	47C2													
		TMD5_CONN_DM<2>	TMD5_CONN_DM<2> - @m60_lib.M60	97C7 97D4	TSSENSE_NB_DXP	TSSENSE_NB_DXP - @m60_lib.M60	61B5	USB_PORT0_N	USB_PORT0_N - @m60_lib.M60	47D5													
		TMD5_CONN_DP<0>	TMD5_CONN_DP<0> - @m60_lib.M60	97C4 97D7	TSSENSE_NB_GPU_DXP	TSSENSE_NB_GPU_DXP - @m60_lib.M60	61B5	USB_PORT0_P	USB_PORT0_P - @m60_lib.M60	47C5													
A		TMD5_CONN_DP<1>	TMD5_CONN_DP<1> - @m60_lib.M60	97C4 97D7	TV_DACA_OUT	TV_DACA_OUT - @m60_lib.M60	13C5 19B1	USB_PORT1_N	USB_PORT1_N - @m60_lib.M60	47B5	A												
		TMD5_CONN_DP<2>	TMD5_CONN_DP<2> - @m60_lib.M60	97C7 97D4	TV_DACC_OUT	TV_DACC_OUT - @m60_lib.M60	13C5 19B1	USB_PORT1_P	USB_PORT1_P - @m60_lib.M60	47B5													
		TMD5_DATA_N<0>	TMD5_CONN_DP<2> - @m60_lib.M60	93C3 97D8	TV_IRTNC	TV_IRTNC - @m60_lib.M60	13C5 19B1	USB_PORT2_N	USB_PORT2_N - @m60_lib.M60	47A5													
		TMD5_DATA_N<1>	TMD5_DATA_N<0> - @m60_lib.M60	93C3 97D8	TV_IRTNA	TV_IRTNA - @m60_lib.M60	13C5 19B1	USB_PORT2_P	USB_PORT2_P - @m60_lib.M60	47A5													
		TMD5_DATA_N<2>	TMD5_DATA_N<1> - @m60_lib.M60	93C3 97C8	TV_IRTNC	TV_IRTNC - @m60_lib.M60	13C5 19B1	USB_RBIAS_PN	USB_RBIAS_PN - @m60_lib.M60	22C2													
		TMD5_DATA_N<3>	TMD5_DATA_N<2> - @m60_lib.M60	93C3 95D6	TV_IRTND	TV_IRTND - @m60_lib.M60	13C5 19B1	VGA_B	VGA_B - @m60_lib.M60	97B6 97C5													
		TMD5_DATA_N<4>	TMD5_DATA_N<3> - @																				

8			7			6			5			4			3			2			1			
D	Title: Cref Part Report Design: m60 Date: Jun 23 16:28:15 2006																							
	C85A0	CAP_402	m60[85D1]				C2513	CAP_402	m60[25C6]				C4112	CAP_402	m60[41C4]				C5792	CAP_603	m60[57D5]			
	C600	CAP_402	m60[6C7]				C2514	CAP_402	m60[25C6]				C4113	CAP_402	m60[41C4]				C5797	CAP_1210	m60[57D2]			
	C601	CAP_402	m60[6A3]				C2515	CAP_402	m60[25B6]				C4115	CAP_402	m60[41B5]				C5798	CAP_1210	m60[57D2]			
	C602	CAP_402	m60[6A3]				C2516	CAP_P_CASE-C2	m60[25D3]				C4116	CAP_402	m60[41B5]				C5799	CAP_1210	m60[57D3]			
	C603	CAP_402	m60[6A3]				C2517	CAP_402	m60[25D6]				C4117	CAP_402	m60[41B2]				C5800	CAP_402	m60[59B8]			
	C620	CAP_603	m60[6D6]				C2518	CAP_402	m60[25D4]				C4118	CAP_402	m60[41B2]				C5801	CAP_402	m60[59B8]			
	C621	CAP_603	m60[6D6]				C2519	CAP_402	m60[25D3]				C4126	CAP_402	m60[41A8]				C5802	CAP_805	m60[58D3]			
	C622	CAP_805	m60[6D7]				C2520	CAP_402	m60[25B6]				C4127	CAP_402	m60[41A8]				C5803	CAP_402	m60[58D2]			
	C623	CAP_805-2	m60[6D7]				C2521	CAP_402	m60[25C3]				C4128	CAP_402	m60[41A8]				C5804	CAP_402	m60[58D2]			
C	C624	CAP_1210	m60[6D8]				C2522	CAP_402	m60[25B3]				C4129	CAP_402	m60[41A8]				C5805	CAP_402	m60[58D2]			
	C625	CAP_P_6_3X5.5-SM	m60[6D8]				C2523	CAP_402	m60[25B4]				C4130	CAP_402	m60[41A7]				C5806	CAP_402	m60[58D1]			
	C0800	CAP_402	m60[8B5]				C2524	CAP_603	m60[25B3]				C4131	CAP_402	m60[41A7]				C5807	CAP_402	m60[58D2]			
	C0801	CAP_603	m60[8B5]				C2525	CAP_402	m60[25B3]				C4132	CAP_402	m60[41A7]				C5820	CAP_402	m60[58C3]			
	C900	CAP_805	m60[9B6]				C2526	CAP_402	m60[25A4]				C4133	CAP_402	m60[41A6]				C5900	CAP_402	m60[59D8]			
	C901	CAP_805	m60[9B6]				C2527	CAP_402	m60[25A3]				C4134	CAP_402	m60[41A6]				C5901	CAP_402	m60[59D8]			
	C902	CAP_805	m60[9A6]				C2528	CAP_402	m60[25A3]				C4135	CAP_402	m60[41A5]				C5902	CAP_402	m60[59B7]			
	C903	CAP_805	m60[9A6]				C2529	CAP_402	m60[25A3]				C4136	CAP_402	m60[41A5]				C5903	CAP_402	m60[59A8]			
	C904	CAP_805	m60[9A6]				C2530	CAP_402	m60[25A3]				C4137	CAP_402	m60[41A5]				C5919	CAP_402	m60[59B4]			
	C905	CAP_805	m60[9A6]				C2531	CAP_402	m60[25D1]				C4138	CAP_402	m60[41A4]				C5940	CAP_402	m60[59A4]			
B	C906	CAP_805	m60[9A6]				C2532	CAP_402	m60[25C1]				C4139	CAP_402	m60[41A4]				C5941	CAP_402	m60[59A3]			
	C907	CAP_805	m60[9B5]				C2533	CAP_402	m60[25C1]				C4140	CAP_402	m60[41B3]				C5942	CAP_805-1	m60[59A3]			
	C908	CAP_805	m60[9B7]				C2534	CAP_402	m60[25D1]				C4150	CAP_402	m60[41D5]				C5943	CAP_402	m60[59A5]			
	C909	CAP_805	m60[9B5]				C2605	CAP_402	m60[26C7]				C4200	CAP_1210	m60[42D8]				C5951	CAP_402	m60[60B6]			
	C910	CAP_805	m60[9B7]				C2607	CAP_402	m60[26D5]				C4201	CAP_402	m60[42D7]				C6000	CAP_402	m60[60D4]			
	C911	CAP_805	m60[9B7]				C2608	CAP_402	m60[26D8]				C4202	CAP_1210	m60[42D7]				C6001	CAP_402	m60[60D4]			
	C912	CAP_805	m60[9A7]				C2609	CAP_402	m60[26D8]				C4203	CAP_1206-1	m60[42D6]				C6002	CAP_402	m60[60D4]			
	C913	CAP_805	m60[9A7]				C2610	CAP_402	m60[26C7]				C4204	CAP_402	m60[42D6]				C6003	CAP_402	m60[60D4]			
	C914	CAP_805	m60[9A7]				C2611	CAP_402	m60[26B7]				C4205	CAP_1210	m60[42C5]				C6100	CAP_402	m60[61B5]			
	C915	CAP_805	m60[9A7]				C2698	CAP_402	m60[26C4]				C4206	CAP_402	m60[42C5]				C6101	CAP_402	m60[61B5]			
A	C916	CAP_805	m60[9A7]				C2699	CAP_402	m60[26C5]				C4209	CAP_603	m60[42B7]				C6301	CAP_402	m60[63C2]			
	C917	CAP_805	m60[9A7]				C2800	CAP_402	m60[28D6]				C4210	CAP_402	m60[42B6]				C6308	CAP_402	m60[63C5]			
	C918	CAP_805	m60[9A7]				C2801	CAP_603	m60[28B2]				C4300	CAP_402	m60[43D7]				C6309	CAP_402	m60[63C6]			
	C919	CAP_805	m60[9A7]				C2802	CAP_603	m60[28B2]				C4301	CAP_402	m60[43D6]				C6311	CAP_402	m60[63C2]			
	C920	CAP_805	m60[9A5]				C2803	CAP_603	m60[28B1]				C4304	CAP_402	m60[43C6]				C6312	CAP_402	m60[63D3]			
	C921	CAP_805	m60[9A7]				C2804	CAP_603	m60[28B1]				C4305	CAP_402	m60[43B6]				C6500	CAP_603	m60[65D5]			
	C922	CAP_805	m60[9A7]				C2810	CAP_402	m60[28B2]				C4401	CAP_402	m60[44D1]				C6501	CAP_805	m60[65D5]			
	C923	CAP_805	m60[9B7]				C2811	CAP_402	m60[28B2]				C4402	CAP_402	m60[44C1]				C6502	CAP_603	m60[65B4]			
	C924	CAP_805	m60[9A7]				C2812	CAP_402	m60[28B1]				C4410	CAP_402	m60[44D6]				C6503	CAP_805	m60[65B5]			
	C925	CAP_805	m60[9A7]				C2813	CAP_402	m60[28B1]				C4412	CAP_402	m60[44D1]				C6504	CAP_P_6_3X11-TH-LF1	m60[65C4]			

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8			7			6			5			4			3			2			1		

8			7			6			5			4			3			2			1		
D	C7516	CAP_402	m60[75B4]	C8310	CAP_603-1	m60[83D8]	C8631	CAP_402	m60[86C6]	C9122	CAP_402	m60[91B5]	D										
	C7517	CAP_P_SM-3	m60[75D2]	C8311	CAP_603-1	m60[83D7]	C8632	CAP_402	m60[86C5]	C9125	CAP_805	m60[91B5]											
	C7518	CAP_P_SM-3	m60[75D2]	C8315	CAP_603-1	m60[83B5]	C8633	CAP_402	m60[86C5]	C9126	CAP_402	m60[91B5]											
	C7521	CAP_402	m60[75A6]	C8316	CAP_603	m60[83A6]	C8634	CAP_402	m60[86C5]	C9127	CAP_402	m60[91B5]											
	C7526	CAP_603	m60[75D6]	C8317	CAP_603-1	m60[83B8]	C8650	CAP_805	m60[86B7]	C9130	CAP_805	m60[91A6]											
	C7527	CAP_603	m60[75C5]	C8318	CAP_603-1	m60[83B7]	C8651	CAP_805	m60[86B7]	C9131	CAP_402	m60[91A6]											
	C7528	CAP_402	m60[75B5]	C8319	CAP_603	m60[83A7]	C8652	CAP_805	m60[86B7]	C9132	CAP_402	m60[91A5]											
	C7529	CAP_402	m60[75B5]	C8320	CAP_402	m60[83A7]	C8653	CAP_805	m60[86B6]	C9135	CAP_805	m60[91A6]											
	C7530	CAP_402	m60[75D6]	C8321	CAP_603-1	m60[83C5]	C8655	CAP_402	m60[86B6]	C9136	CAP_402	m60[91A6]											
	C7531	CAP_402	m60[75B5]	C8325	CAP_603-1	m60[83D6]	C8656	CAP_402	m60[86B6]	C9137	CAP_402	m60[91A5]											
C	C7532	CAP_402	m60[75B6]	C8326	CAP_603-1	m60[83D5]	C8657	CAP_402	m60[86B6]	C9140	CAP_805	m60[91A6]	C										
	C7533	CAP_402	m60[75B6]	C8398	CAP_603	m60[83C2]	C8658	CAP_402	m60[86B5]	C9141	CAP_402	m60[91A6]											
	C7534	CAP_402	m60[75B5]	C8399	CAP_603	m60[83D2]	C8659	CAP_402	m60[86B5]	C9142	CAP_402	m60[91A5]											
	C7535	CAP_603	m60[75D5]	C8400	CAP_805	m60[84C7]	C8660	CAP_402	m60[86B5]	C9191	CAP_402	m60[91D2]											
	C7550	CAP_603	m60[75D1]	C8401	CAP_402	m60[84C7]	C8661	CAP_402	m60[86B6]	C9300	CAP_805	m60[93C6]											
	C7551	CAP_603	m60[75D1]	C8402	CAP_402	m60[84C7]	C8662	CAP_402	m60[86B6]	C9301	CAP_402	m60[93C6]											
	C7590	CAP_402	m60[75C3]	C8405	CAP_805	m60[84B7]	C8663	CAP_402	m60[86B6]	C9302	CAP_402	m60[93C5]											
	C7592	CAP_402	m60[75B3]	C8406	CAP_402	m60[84B7]	C8664	CAP_402	m60[86B5]	C9305	CAP_805	m60[93C6]											
	C7596	CAP_402	m60[75D6]	C8407	CAP_402	m60[84B7]	C8665	CAP_402	m60[86B5]	C9306	CAP_402	m60[93C6]											
	C7597	CAP_1210	m60[75D1]	C8410	CAP_805	m60[84B6]	C8666	CAP_402	m60[86B5]	C9307	CAP_402	m60[93C5]											
B	C7598	CAP_1210	m60[75D1]	C8411	CAP_402	m60[84B7]	C8667	CAP_402	m60[86B6]	C9310	CAP_805	m60[93C6]	B										
	C7599	CAP_402	m60[76D6]	C8412	CAP_402	m60[84B7]	C8668	CAP_402	m60[86B6]	C9311	CAP_402	m60[93C6]											
	C7612	CAP_402	m60[76B2]	C8413	CAP_402	m60[84B7]	C8669	CAP_402	m60[86B6]	C9312	CAP_402	m60[93C5]											
	C7633	CAP_402	m60[76C7]	C8420	CAP_402	m60[84D5]	C8670	CAP_402	m60[86B5]	C9315	CAP_805	m60[93B8]											
	C7710	CAP_402	m60[77C7]	C8421	CAP_402	m60[84D5]	C8671	CAP_402	m60[86B5]	C9316	CAP_402	m60[93B8]											
	C7711	CAP_402	m60[77B7]	C8422	CAP_402	m60[84D5]	C8672	CAP_402	m60[86B5]	C9317	CAP_402	m60[93B7]											
	C7712	CAP_402	m60[77C7]	C8423	CAP_402	m60[84D5]	C8673	CAP_402	m60[86B6]	C9320	CAP_805	m60[93B6]											
	C7750	CAP_402	m60[77B2]	C8424	CAP_402	m60[84D5]	C8674	CAP_402	m60[86B6]	C9321	CAP_402	m60[93B6]											
	C7751	CAP_805	m60[77C3]	C8425	CAP_402	m60[84D5]	C8675	CAP_402	m60[86B6]	C9322	CAP_402	m60[93B5]											
	C7752	CAP_805	m60[77C3]	C8426	CAP_402	m60[84D5]	C8676	CAP_402	m60[86B5]	C9325	CAP_805	m60[93B8]											
A	C7753	CAP_402	m60[77B5]	C8427	CAP_402	m60[84D5]	C8677	CAP_402	m60[86B5]	C9326	CAP_402	m60[93B8]	A										
	C7754	CAP_402	m60[77B4]	C8428	CAP_402	m60[84D5]	C8678	CAP_402	m60[86B5]	C9327	CAP_402	m60[93B7]											
	C7755	CAP_805	m60[77B2]	C8429	CAP_402	m60[84C5]	C8679	CAP_402	m60[86A6]	C9330	CAP_805	m60[93B6]											
	C7756	CAP_805	m60[77B2]	C8430	CAP_402	m60[84C5]	C8680	CAP_402	m60[86A6]	C9331	CAP_402	m60[93B6]											
	C7757	CAP_402	m60[77B5]	C8431	CAP_402	m60[84C5]	C8681	CAP_402	m60[86A6]	C9332	CAP_402	m60[93B5]											
	C7797	CAP_402	m60[77C3]	C8432	CAP_402	m60[84C5]	C8682	CAP_402	m60[86A5]	C9340	CAP_805	m60[93A6]											
	C7798	CAP_603	m60[77B5]	C8433	CAP_402	m60[84C5]	C8683	CAP_402	m60[86A5]	C9341	CAP_402	m60[93A6]											
	C7799	CAP_402	m60[77A2]	C8434	CAP_402	m60[84C5]	C8690	CAP_805	m60[86D5]	C9342	CAP_402	m60[93A5]											
	C7800	CAP_1210	m60[78C3]	C8435	CAP_402	m60[84C5]	C8691	CAP_402	m60[86D5]	C9345	CAP_805	m60[93A6]											
	C7801	CAP_P_TH-MCZ	m60[78C4]	C8436	CAP_402	m60[84C5]	C8692	CAP_402	m60[86D5]	C9346	CAP_402	m60[93A5]											
A	C7802	CAP_603	m60[78C6]	C8437	CAP_402	m60[84C5]	C8711	CAP_402	m60[87B7]	C9347	CAP_402	m60[93A5]	A										
	C7803	CAP_603	m60[78B7]	C8438	CAP_402	m60[84C5]	C8713	CAP_402	m60[87B7]	C9400	CAP_603-1	m60[94C7]											
	C7804	CAP_402	m60[78C7]	C8439	CAP_402	m60[84C5]	C8715	CAP_402	m60[87A7]	C9401	CAP_402	m60[94C6]											
	C7805	CAP_603	m60[78C5]	C8440	CAP_402	m60[84B5]	C8716	CAP_402	m60[87A6]	C9410	CAP_402	m60[94C6]											
	C7806	CAP_805-1	m60[78B3]	C8441	CAP_402	m60[84B5]	C8721	CAP_402	m60[87B4]	C9420	CAP_1210	m60[94C5]											
	C7807	CAP_P_TH	m60[78B3]	C8442	CAP_402	m60[84B5]	C8723	CAP_402	m60[87B4]	C9470	CAP_402	m60[94B2]											
	C7809	CAP_402	m60[78B3]	C8443	CAP_402	m60[84B5]	C8725	CAP_402	m60[87A4]	C9700	CAP_402	m60[97C8]											
	C7810	CAP_402	m60[78B5]	C8444	CAP_402	m60[84B5]	C8726	CAP_402	m60[87A3]	C9710	CAP_603	m60[97C3]											
	C7811	CAP_402	m60[78B5]	C8445	CAP_402	m60[84B5]	C8900	CAP_805	m60[89D7]	C9711	CAP_402	m60[97D3]											
	C7813	CAP_1206	m60[78B4]	C8446	CAP_402	m60[84B5]	C8901	CAP_402	m60[89D7]	C9713	CAP_402	m60[97C2]											
A	C7814	CAP_402	m60[78B5]	C8447	CAP_402	m60[84B5]	C8902	CAP_402	m60[89D7]	C9714	CAP_402	m60[97C2]	A										
	C7897	CAP_805-1	m60[78B2]	C8448	CAP_402	m60[84B5]	C8903	CAP_402	m60[89D7]	C9740	CAP_402	m60[97A7]											
	C7899	CAP_1210	m60[78C3]	C8449	CAP_402	m60[84B5]	C8904	CAP_402	m60[89D6]	C9741	CAP_402	m60[97A6]											
	C7900	CAP_402	m60[79D6]	C8450	CAP_402	m60[84B5]	C8910	CAP_402	m60[89D7]	C9742	CAP_402	m60[97A6]											
	C7901	CAP_603	m60[79D5]	C8451	CAP_402	m60[84B5]	C8915	CAP_402	m60[89D6]	C9750	CAP_402	m60[97B4]											
	C7902	CAP_402	m60[79C5]	C8455	CAP_402	m60[84D2]	C8920	CAP_805	m60[89C8]	D2500	DIODE_SCHOT_SOT23	m60[25C8]											
	C7903	CAP_603	m60[79C7]	C8456	CAP_402	m60[84D2]	C8921	CAP_402	m60[89C8]	D2501	DIODE_SCHOT_SOT23	m60[25D8]											
	C7906	CAP_402	m60[79C5]	C8457	CAP_402	m60[84D2]	C8922	CAP_402	m60[89C7]	D2600	DIODE_SCHOT_SOT23	m60[26D8]											
	C7907	CAP_402	m60[79C3]	C8458	CAP_402	m60[84D2]	C8923	CAP_402	m60[89C7]	D2601	DIODE_SCHOT_SOT23	m60[26C8]											
	C7908	CAP_402	m60[79C5]	C8459	CAP_402	m60[84D2]	C8924	CAP_402	m60[89C7]	D4600	DIODE_SMC	m60[46D5]											
A	C7909	CAP_1206	m60[79C3]	C8460	CAP_402	m60[84D2]	C8925	CAP_402	m60[89C7]	D4690	ZENER_SOT23	m60[46A6]	A										
	C7910	CAP_P_TH-MCZ	m60[79D4]	C8461	CAP_402	m60[84D2]	C8926	CAP_402	m60[89C6]	D4700	DIODE_SCHOT_3P_A_SC-	m60[47C5]											
	C7911	CAP_1210	m60[79D3]	C8462	CAP_402	m60[84D2]	C8931	CAP_402	m60[89C7]	75													
	C7912	CAP_P_CASE-D2E-LF	m60[79C2]	C8463	CAP_402	m60[84D2]	C8933	CAP_402	m60[89C6]	D4701	DIODE_SCHOT_3P_A_SC-	m60[47B5]											
	C7913	CAP_1206	m60[79C2]	C8464	CAP_402	m60[84C2]	C8934	CAP_805	m60[89D4]	75													
	C7980	CAP_402	m60[79A3]	C8465	CAP_402	m60[84C2]	C8951	CAP_402	m60[89D4]	D4702	DIODE_SCHOT_3P_A_SC-	m60[47A5]											
	C7992	CAP_603	m60[79D6]	C8466	CAP_402	m60[84C2]	C8952	CAP_402	m60[89D4]	75													
	C7997	CAP_1210	m60[79D3]	C8467	CAP_402	m60[84C2]	C8953	CAP_402	m60[89D3]	D5700	DIODE_SCHOT_5P_TLM83	m60[57C4]											
	C7998	CAP_P_CASE-D2E-LF	m60[79C2]	C8468	CAP_402	m60[84C2]	C8954	CAP_402	m60[89D3]	3													
	C8000	CAP_1210	m60[80D3]	C8469	CAP_402	m60[84C2]	C8960	CAP_402	m60[89D3]	D6500	DIODE_SOT23	m60[65C4]											
A	C8001	CAP_P_CASE-D2E-LF	m60[80C2]	C8470	CAP_402	m60[84C2]	C8965	CAP_402	m60[89D3]	D6501	DIODE_SOT23	m60[65B4]	A										
	C8002	CAP_603	m60[80D6]	C8471	CAP_402	m60[84C2]	C8970	CAP_805	m60[89C5]	D6502	DIODE_SCHOT_SMB	m60[65C4]											
	C8003	CAP_402	m60[80C3]	C8472	CAP_402	m60[84C2]	C8971	CAP_402	m60[89C4]	D6503	DIODE_SCHOT_SMB	m60[65B4]											
	C8004	CAP_1206	m60[80C1]	C8473	CAP_402	m60[84C2]	C8972	CAP_402	m60[89C4]	D6600	DIODE_SCHOT23	m60[66C4]											
	C8005	CAP_402	m60[80C4]	C8474	CAP_402	m60[84C2]	C8973	CAP_402	m60[89C4]	D6601	DIODE_SCHOT_SMB	m60[66C3]											
	C8006	CAP_402	m60[80D6]	C8475	CAP_402	m60[84B2]	C8974	CAP_402	m60[89C4]	D7500	DIODE_SCHOT_SMB	m60[75C3]											
	C8009	CAP_603	m60[80C6]	C8476	CAP_402	m60[84B2]	C8975	CAP_402	m60[89C3]	D7501	DIODE_SCHOT_SMB	m60[75B2]											
	C8010	CAP_603	m60[80C4]	C8477	CAP_402	m60[84B2]	C8976	CAP_402	m60[89C3]	D7599	DIODE_SOT23	m60[76D6]											
	C8011	CAP_402	m60[80C5]	C8478	CAP_402	m60[84B2]	C8981	CAP_402	m60[89C3]	D7800	DIODE_SCHOT_5P_TLM83	m60[78B4]											
	C8012	CAP_402	m60[80C5]	C8479	CAP_402	m60[84B2]	C8983	CAP_402	m60[89C3]	3													
A	C8015	CAP_1210	m60[80D3]	C8480	CAP_402	m60[84B2]	C9000	CAP_805	m60[90D7]	D7900	DIODE_SCHOT_5P_TLM83	m60[79C4]	A										
	C8016	CAP_805-1	m60[80C2]	C8481	CAP_402	m60[84B2]	C9001	CAP_402	m60[90D7]	3													
	C8090	CAP_1210	m60[80D3]	C8482	CAP_402	m60[84B2]	C9002	CAP_402	m60[90D7]	D8000	DIODE_SCHOT_5P_TLM83	m60[80C4]											
	C8091	CAP_1210	m60																				

8			7			6			5			4			3			2			1		
D	GV3801	HOLE_VIA	m60[38A8]			L7324	IND_0603-LF	m60[73B5]			PP627	PROBEPOINT_SM	m60[5C8]		PP8908	PROBEPOINT_SM	m60[5B5]						
	GV3802	HOLE_VIA	m60[38A7]			L7325	IND_0603-LF	m60[73A5]			PP628	PROBEPOINT_SM	m60[5C8]		PP8909	PROBEPOINT_SM	m60[5B5]						
	GV3803	HOLE_VIA	m60[38A8]			L7326	IND_0603-LF	m60[73B5]			PP629	PROBEPOINT_SM	m60[5C8]		PP8910	PROBEPOINT_SM	m60[5B5]						
	GV3804	HOLE_VIA	m60[38A7]			L7327	IND_0603-LF	m60[73A5]			PP630	PROBEPOINT_SM	m60[5C8]		PP8911	PROBEPOINT_SM	m60[5B5]						
	GV3805	HOLE_VIA	m60[38A8]			L7381	IND_0603-LF	m60[73A5]			PP631	PROBEPOINT_SM	m60[5D6]		PP8912	PROBEPOINT_SM	m60[5B5]						
	GV3806	HOLE_VIA	m60[38A7]			L7500	IND_SM	m60[75D1]			PP632	PROBEPOINT_SM	m60[5D6]		PP8913	PROBEPOINT_SM	m60[5B5]						
	GV3807	HOLE_VIA	m60[38A8]			L7501	IND_SM	m60[75B2]			PP633	PROBEPOINT_SM	m60[5D6]		PP8914	PROBEPOINT_SM	m60[5A5]						
	GV3808	HOLE_VIA	m60[38A7]			L7502	IND_TH-VERT-LF	m60[76D8]			PP634	PROBEPOINT_SM	m60[5D6]		PP8915	PROBEPOINT_SM	m60[5A5]						
	J3	CON_2RTSM_125_SM-2MT	m60[61B6]			L7750	IND_SM-LF	m60[77B2]			PP635	PROBEPOINT_SM	m60[5D6]		PP8916	PROBEPOINT_SM	m60[5A5]						
		-BLK-LF				L7800	IND_3P_SM	m60[78C3]			PP636	PROBEPOINT_SM	m60[5D6]		PP8920	PROBEPOINT_SM	m60[5A5]						
C	J600	CON_M14RT_D_THA_M-RT	m60[6D7]			L7880	IND_0603-LF	m60[73A7]			PP637	PROBEPOINT_SM	m60[5D6]		PP8921	PROBEPOINT_SM	m60[5A5]						
		-TH1				L7900	IND_3P_SM	m60[79C3]			PP638	PROBEPOINT_SM	m60[5D6]		PP8922	PROBEPOINT_SM	m60[5A5]						
	J0700	CPU_YONAH_SKT_BGA	m60[7C3 7D7]			L8000	IND_IHLP	m60[80C3]			PP639	PROBEPOINT_SM	m60[5D6]		PP8923	PROBEPOINT_SM	m60[5A5]						
	J0700	CPU_YONAH_SKT_BGA	m60[8D4 8D8]			L8100	IND_IHLP	m60[81C3]			PP640	PROBEPOINT_SM	m60[5D6]		PP8924	PROBEPOINT_SM	m60[5A5]						
	J1000	CON_2RTSM_125_SM-2MT	m60[10B7]			L8203	IND_IHLP	m60[82D3]			PP641	PROBEPOINT_SM	m60[5D6]		PP8925	PROBEPOINT_SM	m60[5A5]						
		-BLK-LF				L8400	IND_0402	m60[84B7]			PP642	PROBEPOINT_SM	m60[5D6]		PP8926	PROBEPOINT_SM	m60[5A5]						
	J1101	CON_F28RT_S2MT_SM_F-	m60[11C2]			L8520	IND_IHLP	m60[85C3]			PP643	PROBEPOINT_SM	m60[5D6]		PP8927	PROBEPOINT_SM	m60[5A5]						
		RT-SM				L8715	IND_0402	m60[87A7]			PP644	PROBEPOINT_SM	m60[5D6]		PP8928	PROBEPOINT_SM	m60[5A5]						
	J2600	BATTERY_2P_SM	m60[26C8]			L8725	IND_0402	m60[87A4]			PP645	PROBEPOINT_SM	m60[5D6]		PP8929	PROBEPOINT_SM	m60[5A5]						
	J2800	CON_F200RT_DDR2DIMM_	m60[28D5]			L8910	IND_0402	m60[89D7]			PP646	PROBEPOINT_SM	m60[5D6]		PP8930	PROBEPOINT_SM	m60[5A5]						
B		SMT_SM_F-RT-SM1				L8915	IND_0402	m60[89D7]			PP647	PROBEPOINT_SM	m60[5D6]		PP8931	PROBEPOINT_SM	m60[5A5]						
	J2900	CON_F200RT_DDR2DIMM_	m60[29D5]			L8960	IND_0402	m60[89D4]			PP648	PROBEPOINT_SM	m60[5D6]		PP8932	PROBEPOINT_SM	m60[5A5]						
		SMT_SM_F-RT-SM1				L8965	IND_0402	m60[89D4]			PP649	PROBEPOINT_SM	m60[5D6]		PP8933	PROBEPOINT_SM	m60[5A5]						
	J2901	CON_F4ST_S2MT_SM_F-S	m60[59C7]			L9010	IND_0402	m60[90D7]			PP650	PROBEPOINT_SM	m60[5D6]		PP8934	PROBEPOINT_SM	m60[5A5]						
		T-SM				L9015	IND_0402	m60[90D7]			PP651	PROBEPOINT_SM	m60[5D6]		PP8935	PROBEPOINT_SM	m60[5A5]						
	J2903	CON_M2ST_S2MT_SM_M-S	m60[59C8]			L9060	IND_0402	m60[90D4]			PP652	PROBEPOINT_SM	m60[5C6]		PP8936	PROBEPOINT_SM	m60[5A5]						
		T-SM				L9065	IND_0402	m60[90D4]			PP653	PROBEPOINT_SM	m60[5C6]		PP9000	PROBEPOINT_SM	m60[5B4]						
	J4700	CON_F10ST_D_SMA_F-ST	m60[47A2]			L9120	IND_0402	m60[91B6]			PP654	PROBEPOINT_SM	m60[5C6]		PP9001	PROBEPOINT_SM	m60[5B4]						
		-SM				L9125	IND_0402	m60[91B6]			PP655	PROBEPOINT_SM	m60[5C6]		PP9002	PROBEPOINT_SM	m60[5B4]						
	J5300	CON_F52RT_D2MT_SM_F-	m60[53C5]			L9130	IND_0402	m60[91B7]			PP656	PROBEPOINT_SM	m60[5C6]		PP9003	PROBEPOINT_SM	m60[5B4]						
A		RT-SM				L9135	IND_0402	m60[91A7]			PP657	PROBEPOINT_SM	m60[5C6]		PP9004	PROBEPOINT_SM	m60[5B4]						
	J6000	CON_F30STSM_5047_SM1	m60[60C3]			L9140	IND_0402	m60[91A7]			PP658	PROBEPOINT_SM	m60[5C6]		PP9005	PROBEPOINT_SM	m60[5B4]						
	J6500	CON_M4RT_S2MT_SM_M-R	m60[65D3]			L9300	IND_0402	m60[93C7]			PP659	PROBEPOINT_SM	m60[5C6]		PP9006	PROBEPOINT_SM	m60[5B4]						
		T-SM				L9305	IND_0402	m60[93C7]			PP660	PROBEPOINT_SM	m60[5C6]		PP9007	PROBEPOINT_SM	m60[5B4]						
	J6501	CON_M5RT_S2MT_SM_M-	m60[65B2]			L9310	IND_0402	m60[93C7]			PP661	PROBEPOINT_SM	m60[5C6]		PP9008	PROBEPOINT_SM	m60[5B4]						
		RT-SM				L9315	IND_0402	m60[93C7]			PP662	PROBEPOINT_SM	m60[5C6]		PP9009	PROBEPOINT_SM	m60[5B4]						
	J6600	CON_F4ST_S2MT_SM_F-S	m60[66C2]			L9320	IND_0402	m60[93B7]			PP663	PROBEPOINT_SM	m60[5C6]		PP9010	PROBEPOINT_SM	m60[5B4]						
		T-SM				L9325	IND_0402	m60[93B7]			PP664	PROBEPOINT_SM	m60[5C6]		PP9011	PROBEPOINT_SM	m60[5B4]						
	J6601	CON_M4RT_S2MT_SM_M-R	m60[66B5]			L9330	IND_0402	m60[93B7]			PP665	PROBEPOINT_SM	m60[5C6]		PP9012	PROBEPOINT_SM	m60[5B4]						
		T-SM				L9345	IND_0402	m60[93B7]			PP666	PROBEPOINT_SM	m60[5C6]		PP9013	PROBEPOINT_SM	m60[5B4]						
A	J6602	CON_M4RT_S2MT_SM_M-R	m60[66B3]			L9400	IND_SM	m60[94C6]			PP667	PROBEPOINT_SM	m60[5C6]		PP9014	PROBEPOINT_SM	m60[5A4]						
		T-SM				L9700	FILTER_4P_2012H	m60[97D7]			PP668	PROBEPOINT_SM	m60[5C6]		PP9015	PROBEPOINT_SM	m60[5A4]						
	J7300	CON_F9ANG_S4MT_TH3_F	m60[73D8]			L9701	FILTER_4P_2012H	m60[97D7]			PP673	PROBEPOINT_SM	m60[5C6]		PP9016	PROBEPOINT_SM	m60[5A4]						
		-ANG-TH				L9702	FILTER_4P_2012H	m60[97C7]			PP674	PROBEPOINT_SM	m60[5C6]		PP9020	PROBEPOINT_SM	m60[5A4]	</					

8			7			6			5			4			3			2			1		
D	Q8300	TRA_IRF7413_SO-8	m60[83D2]	R2303	RES_402	m60[23D3]	R3446	RES_402	m60[34B1]	R5809	RES_402	m60[58C2]	D										
	Q8301	TRA_IRF7413_SO-8	m60[83C2]	R2305	RES_402	m60[23D3]	R3451	RES_402	m60[34C4]	R5815	RES_402	m60[59B3]											
	Q8302	TRA_2N7002_SOT23-LF	m60[83C3]	R2306	RES_402	m60[23B7]	R3452	RES_402	m60[34B7]	R5817	RES_402	m60[59B3]											
	Q8303	TRA_2N7002_SOT23-LF	m60[83D3]	R2307	RES_402	m60[23A7]	R3453	RES_402	m60[34B8]	R5818	RES_402	m60[59B3]											
	Q8310	TRA_S13446DV_TSOP-LF	m60[83D8]	R2308	RES_402	m60[23B7]	R3454	RES_402	m60[34B7]	R5819	RES_402	m60[59B3]											
	Q8311	TRA_2N7002_SOT23-LF	m60[83C7]	R2309	RES_402	m60[23A7]	R3455	RES_402	m60[34B8]	R5821	RES_402	m60[59B3]											
	Q8312	TRA_S13446DV_TSOP-LF	m60[83C5]	R2310	RES_402	m60[23A7]	R3456	RES_402	m60[34B7]	R5822	RES_402	m60[59B3]											
	Q8313	TRA_IRF7410_SO-8	m60[83B6]	R2311	RES_402	m60[23A7]	R3457	RES_402	m60[34B7]	R5823	RES_402	m60[59B3]											
	Q8315	TRA_2N7002_SOT23-LF	m60[83B5]	R2313	RES_402	m60[23A7]	R3458	RES_402	m60[34B8]	R5824	RES_402	m60[59B3]											
	Q8316	TRA_2N7002_SOT23-LF	m60[83C5]	R2314	RES_402	m60[23A7]	R3459	RES_402	m60[34A7]	R5825	RES_402	m60[59B3]											
C	Q8317	TRA_S13446DV_TSOP-LF	m60[83D5]	R2316	RES_402	m60[23D7]	R3460	RES_402	m60[34A7]	R5826	RES_402	m60[59B3]	C										
	Q8318	TRA_DUAL_2N7002A_SOT563	m60[83A8 83A7]	R2317	RES_402	m60[23D7]	R3461	RES_402	m60[34A7]	R5827	RES_402	m60[59C5]											
	Q8319	TRA_S13446DV_TSOP-LF	m60[83C8]	R2318	RES_402	m60[23D7]	R3462	RES_402	m60[34A8]	R5828	RES_402	m60[59B3]											
	Q8320	TRA_2N7002_SOT23-LF	m60[83B7]	R2319	RES_402	m60[23D2]	R3463	RES_402	m60[34A7]	R5829	RES_402	m60[59C3]											
	Q8520	TRA_HAT2168H_LFPAK	m60[85D4]	R2320	RES_402	m60[23D7]	R3470	RES_402	m60[34A5]	R5830	RES_402	m60[59C3]											
	Q8521	TRA_HAT2168H_LFPAK	m60[85C4]	R2323	RES_402	m60[23D5]	R3471	RES_402	m60[34A5]	R5831	RES_402	m60[59C3]											
	Q8522	TRA_HAT2165H_LFPAK	m60[85C5]	R2326	RES_402	m60[23D6]	R3485	RES_402	m60[34D1]	R5832	RES_402	m60[59C5]											
	Q9400	TRA_S13443DV_TSOP-LF	m60[94C7]	R2327	RES_402	m60[23D6]	R3486	RES_402	m60[34D1]	R5833	RES_402	m60[59B3]											
	Q9401	TRA_2N7002_SOT23-LF	m60[94C8]	R2343	RES_402	m60[23D1]	R3487	RES_402	m60[34D1]	R5898	RES_402	m60[58C2]											
	Q9711	TRA_2N7002DW_SOT-363	m60[97D2 97C2]	R2388	RES_402	m60[23A3]	R3488	RES_402	m60[34D1]	R5899	RES_402	m60[58D3]											
B	R75A0	RES_402	m60[75C7]	R2389	RES_402	m60[38D5]	R3489	RES_402	m60[34D2]	R5900	RES_402	m60[59D7]	B										
	R85A0	RES_402	m60[85D1]	R2390	RES_402	m60[23B3]	R3490	RES_402	m60[34D2]	R5903	RES_402	m60[59D2]											
	R600	RES_402	m60[6A7]	R2395	RES_402	m60[23D7]	R3491	RES_402	m60[34D2]	R5904	RES_402	m60[59D2]											
	R602	RES_402	m60[6A8]	R2396	RES_402	m60[23D6]	R3492	RES_402	m60[34D2]	R5905	RES_402	m60[59D2]											
	R603	RES_402	m60[6B1]	R2397	RES_402	m60[23D6]	R3493	RES_402	m60[34D7]	R5906	RES_402	m60[59D2]											
	R605	RES_603	m60[6A6]	R2398	RES_402	m60[23D8]	R3494	RES_402	m60[34D7]	R5907	RES_402	m60[59B7]											
	R611	RES_402	m60[6B7]	R2399	RES_402	m60[23C1]	R3495	RES_402	m60[34D7]	R5910	RES_402	m60[59D2]											
	R612	RES_402	m60[6B7]	R2500	RES_603	m60[25A8]	R3496	RES_402	m60[34C5]	R5911	RES_402	m60[59D2]											
	R614	RES_402	m60[6B7]	R2501	RES_402	m60[25C8]	R3497	RES_402	m60[34D4]	R5912	RES_402	m60[59D2]											
	R615	RES_402	m60[6B7]	R2502	RES_402	m60[25D8]	R3498	RES_402	m60[34D5]	R5913	RES_402	m60[59D2]											
A	R616	RES_402	m60[6A7]	R2600	RES_402	m60[26C7]	R3499	RES_402	m60[34D5]	R5914	RES_402	m60[59D2]	A										
	R617	RES_402	m60[6A7]	R2606	RES_402	m60[26C7]	R3824	RES_402	m60[38D2]	R5915	RES_402	m60[59D2]											
	R618	RES_402	m60[6A7]	R2607	RES_402	m60[26C8]	R3851	RES_402	m60[38D3]	R5916	RES_402	m60[59C2]											
	R619	RES_402	m60[6C7]	R2609	RES_402	m60[26D7]	R3852	RES_402	m60[38D2]	R5917	RES_402	m60[59C2]											
	R0701	RES_402	m60[7C6]	R2611	RES_402	m60[26D5]	R3853	RES_402	m60[38D2]	R5919	RES_402	m60[59B4]											
	R0702	RES_402	m60[7D6]	R2612	RES_402	m60[26D5]	R3857	RES_402	m60[38B3]	R5920	RES_402	m60[59B5]											
	R0703	RES_402	m60[7C5]	R2622	RES_402	m60[26D4]	R3858	RES_402	m60[38B3]	R5921	RES_402	m60[59B5]											
	R0704	RES_402	m60[7C5]	R2623	RES_402	m60[26D2]	R3859	RES_402	m60[38B2]	R5922	RES_402	m60[59B5]											
	R0705	RES_402	m60[7B4]	R2624	RES_402	m60[26D2]	R3897	RES_402	m60[38B7]	R5923	RES_402	m60[59B5]											
	R0706	RES_402	m60[7B5]	R2625	RES_402	m60[26D2]	R3899	RES_402	m60[38B5]	R5924	RES_402	m60[59B5]											

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ZH526	HOLE_VIA	m60[5B1]					
ZH527	HOLE_VIA	m60[5B1]					
ZH528	HOLE_VIA	m60[5B1]					
ZH529	HOLE_VIA	m60[5B1]					
ZH601	MTGHOLE	m60[6A3]					
ZH602	MTGHOLE	m60[6A3]					
ZH603	MTGHOLE	m60[6A3]					
ZH606	MTGHOLE	m60[6A1]					
ZH607	MTGHOLE	m60[9D4]					
ZH608	MTGHOLE	m60[9D3]					
ZH609	MTGHOLE	m60[9D2]					
ZH610	MTGHOLE	m60[9D2]					

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